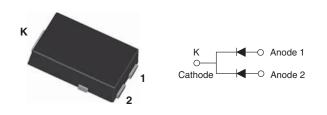
Vishay Semiconductors





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TO-277A (SMPC)

PRODUCT SUMMARY						
Package	TO-277A (SMPC)					
I _{F(AV)}	2 x 4 A					
V _R	200 V					
V _F at I _F	0.95 V					
t _{rr} (typ.)	25 ns					
T _J max.	175 °C					
Diode variation	Dual die					

FEATURES

- Hyperfast recovery time, reduced Q_{rr}, and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, lighting, piezo-injection, as high frequency rectifiers, and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

ABSOLUTE MAXIMUM RATINGS							
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Peak repetitive reverse voltage		V _{RRM}		200	V		
Average rectified forward current	per device	1	$T_{-} = 160 ^{\circ}C_{-}$	8			
Average rectilied forward current per o		IF(AV)	T _{Sp} = 160 °C	4	А		
Nen venetitive neek euwee euwent	per device	I _{FSM}	T _J = 25 °C	130			
Non-repetitive peak surge current	per diode			70			
Operating junction and storage temperatures		T _J , T _{Stg}		-65 to +175	°C		

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Breakdown voltage, blocking voltage	V _{BR} , V _R	I _R = 100 μA	200	-	-		
Forward voltage, per diode	V _F	$I_F = 4 A$	-	0.89	0.95	V	
r of ward voltage, per diode		I _F = 4 A, T _J = 150 °C	-	0.72	0.78		
Reverse leakage current, per diode	I _R	$V_{R} = V_{R}$ rated	-	-	2	μA	
neverse leakage current, per diode		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	6	80		
Junction capacitance	CT	V _R = 200 V	-	17	-	pF	

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(e3) RoHS



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DYNAMIC RECOVERY CHARACTERISTICS (T _J = 25 °C unless otherwise specified)								
PARAMETER	SYMBOL	TEST CO	NDITIONS	MIN.	TYP.	MAX.	UNITS	
		$I_F = 1.0 \text{ A}, \text{ d}I_F/\text{d}t = 50$	I_F = 1.0 A, dI_F/dt = 50 A/µs, V_R = 30 V		25	-		
Povoroo rocovor timo	+	I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	25		
Reverse recovery time	t _{rr}	T _J = 25 °C		-	18	-	A nC	
		T _J = 125 °C	I _F = 4 A dI _F /dt = 200 A/μs V _R = 160 V	-	27	-		
Peak recovery current	I _{RRM}	T _J = 25 °C		-	2	-		
		T _J = 125 °C		-	3.6	-		
Reverse recovery charge	Q _{rr}	T _J = 25 °C		-	18	-		
		T _J = 125 °C		-	50	-		

THERMAL - MECHANICAL SPECIFICATIONS							
PARAMETER	SYMBOL	SYMBOL TEST CONDITIONS MIN. TYP. MAX.					
Maximum junction and storage temperature range	T _J , T _{Stg}		-65	-	175	°C	
Thermal resistance, junction to solder pad, per diode	R _{thJ-Sp}		-	2.5	3.5	°C/W	
Approximate weight				0.1		g	
				0.0035		oz.	
Marking device		Case style TO-277A (SMPC)		QC	H2		

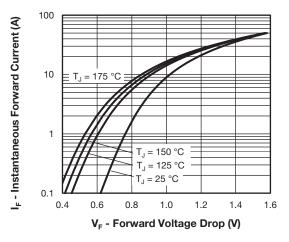


Fig. 1 - Typical Forward Voltage Drop Characteristics

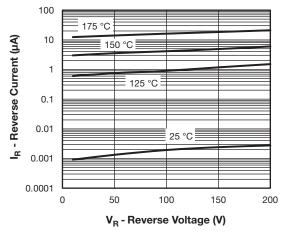
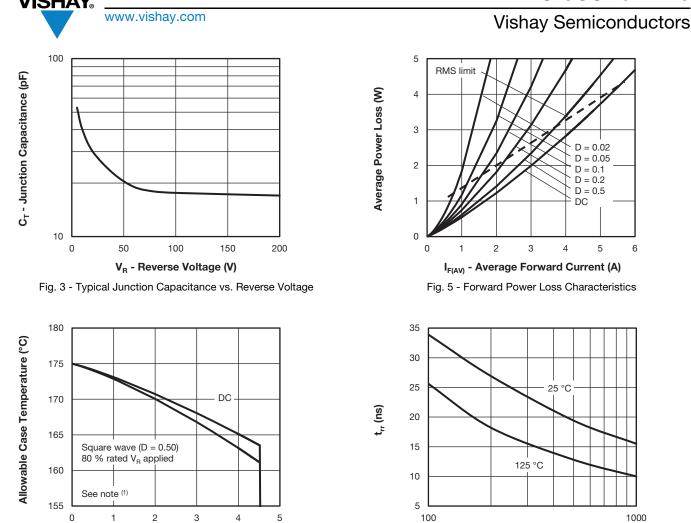
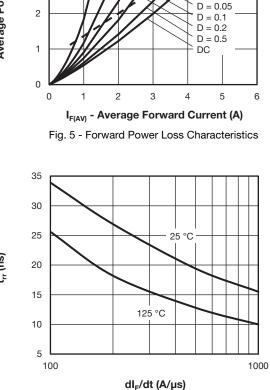


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage



I_{F(AV)} - Average Forward Current (A)

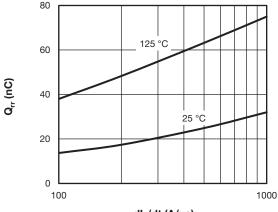
Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current



VS-8CSH02HM3

D = 0.02

Fig. 6 - Typical Reverse Recovery Time vs. dl_F/dt



dl_F/dt (A/µs)

Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 5}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

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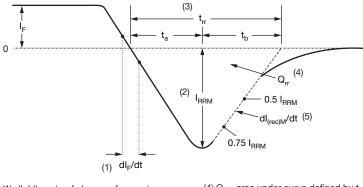
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- (1) dl_F/dt rate of change of current through zero crossing
- (2) I_{RRM} peak reverse recovery current
- (3) t_{rr} reverse recovery time measured from zero crossing point of negative going I_F to point where a line passing through 0.75 I_{RRM} and 0.50 I_{RRM} extrapolated to zero current.

(4) Q_{rr} - area under curve defined by t_{rr} and I_{RRM}

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) $dI_{(rec)M}/dt$ - peak rate of change of current during t_b portion of t_{rr}

Fig. 8 - Reverse Recovery Waveform and Definitions

ORDERING INFORMATION TABLE

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Device code

VS	-	8	С	S	Н	02	Н	M3	
1)	2	3	4	5	6	7	8	
1	-	Visł	nay Sem	niconduc	ctors pro	oduct			
2	-	Cur	rent rati	ng (8 = 8	3 A)				
3	-	Circ	uit conf	guratior	n:				
		C =	C = common cathode						
4	-	S = SMPC package							
5	-	Pro	Process type,						
_		H = hyperfast recovery							
6	-	Volt	Voltage code $(02 = 200 \text{ V})$						
7	-	H =	H = AEC-Q101 qualified						
8	-	М3	= halog	en-free,	RoHS-0	complia	nt, and	terminat	tions lead (Pb

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-8CSH02HM3/86A	1500	1500	7" diameter plastic tape and reel				
VS-8CSH02HM3/87A	6500	6500	13" diameter plastic tape and reel				

LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95570					
Part marking information	www.vishay.com/doc?95565					
Packaging information	www.vishay.com/doc?88869					

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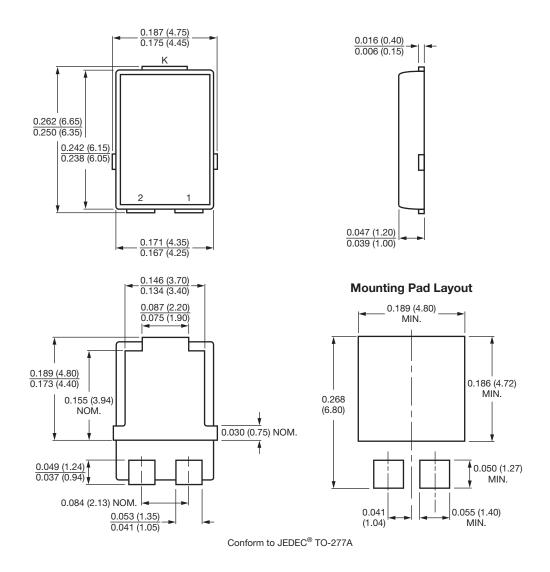
Outline Dimensions





TO-277A (SMPC)

DIMENSIONS in inches (millimeters)





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