

650V 40A Field Stop Trench IGBT

V _{CES}	650V
I _{C(100°C)}	19A
V _{CE(sat) (Typ.)}	1.6V@I _C =40A
P_D	66W

Features

- 1) Low Collector Emitter Saturation Voltage
- 2) High Speed Switching
- 3) Low Switching Loss & Soft Switching
- 4) Built in Very Fast & Soft Recovery FRD (RFN - Series)
- 5) Pb free Lead Plating; RoHS Compliant

Applications

PFC

UPS

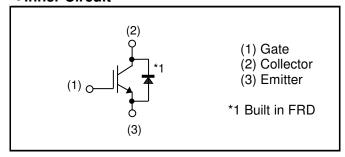
Power Conditioner

ΙH

Outline



●Inner Circuit



Packaging Specifications

	Packaging	Tube	
	Reel Size (mm)	1	
Typo	Tape Width (mm)	-	
Туре	Basic Ordering Unit (pcs)	450	
	Packing Code	C11	
	Marking	RGTH80TK65D	

● Absolute Maximum Ratings (at T_C = 25°C unless otherwise specified)

V _{CES} V _{GES}	650 ±30 31	V
		V
I _C	31	
	01	Α
I _C	19	А
I _{CP} *1	160	Α
I _F	28	А
I _F	16	А
I _{FP} *1	160	А
P _D	66	W
P _D	33	W
T _j	-40 to +175	°C
T _{stg}	−55 to +175	°C
	I _C I _{CP} *1 I _F I _F I _F P _D T _j	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$

^{*1} Pulse width limited by T_{imax.}

●Thermal Resistance

Parameter	Symbol	Values			Unit
Farameter		Min.	Тур.	Max.	Offic
Thermal Resistance IGBT Junction - Case	$R_{\theta(j-c)}$	-	-	2.27	°C/W
Thermal Resistance Diode Junction - Case	$R_{\theta(j-c)}$	-	-	3.76	°C/W

ullet IGBT Electrical Characteristics (at $T_j = 25$ °C unless otherwise specified)

Parameter	Symbol	Conditions	Values			Unit
i didilletei			Min.	Тур.	Max.	Offic
Collector - Emitter Breakdown Voltage	BV _{CES}	$I_{C} = 10 \mu A, V_{GE} = 0 V$	650	-	1	V
Collector Cut - off Current	I _{CES}	$V_{CE} = 650V, V_{GE} = 0V$	ı	ı	10	μΑ
Gate - Emitter Leakage Current	I _{GES}	$V_{GE} = \pm 30V, V_{CE} = 0V$	1	-	±200	nA
Gate - Emitter Threshold Voltage	$V_{\text{GE(th)}}$	$V_{CE} = 5V, I_{C} = 27.6mA$	4.5	5.5	6.5	V
Collector - Emitter Saturation Voltage	V _{CE(sat)}	$I_C = 40A$, $V_{GE} = 15V$ $T_j = 25$ °C $T_j = 175$ °C	-	1.6 2.1	2.1 -	V

●IGBT Electrical Characteristics (at T_j = 25°C unless otherwise specified)

Darameter	Parameter Symbol	Conditions -		Unit		
Parameter			Min.	Тур.	Max.	Uniii
Input Capacitance	C _{ies}	V _{CE} = 30V	-	2210	-	
Output Capacitance	C _{oes}	$V_{GE} = 0V$	-	85	-	pF
Reverse Transfer Capacitance	C _{res}	f = 1MHz	-	35	-	
Total Gate Charge	Q _g	V _{CE} = 300V	-	79	-	
Gate - Emitter Charge	Q_{ge}	I _C = 40A	-	21	-	nC
Gate - Collector Charge	Q_{gc}	V _{GE} = 15V	-	29	-	
Turn - on Delay Time	t _{d(on)}	$I_C = 40A, V_{CC} = 400V$	-	34	-	
Rise Time	t _r	$V_{GE} = 15V, R_{G} = 10\Omega$	-	50	-	
Turn - off Delay Time	t _{d(off)}	T _j = 25°C	-	120	-	ns
Fall Time	t _f	Inductive Load	-	47	-	
Turn - on Delay Time	t _{d(on)}	$I_C = 40A, V_{CC} = 400V$	-	34	-	
Rise Time	t _r	$V_{GE} = 15V, R_{G} = 10\Omega$	-	50	-	200
Turn - off Delay Time	t _{d(off)}	T _j = 175°C	-	135	-	ns
Fall Time	t _f	Inductive Load	-	59	-	
		$I_C = 160A, V_{CC} = 520V$				
Reverse Bias Safe Operating Area	RBSOA	$V_P = 650 V, V_{GE} = 15 V$	FU	LL SQUA	RE	-
		$R_G = 60\Omega, T_j = 175^{\circ}C$				

•FRD Electrical Characteristics (at $T_j = 25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Values			Lloit
			Min.	Тур.	Max.	Unit
		I _F = 20A		4.05	4.0	.,
Diode Forward Voltage	V _F	$T_j = 25^{\circ}C$ $T_j = 175^{\circ}C$	- -	1.35 1.15	1.8	V
Diode Reverse Recovery Time	t _{rr}	I _F = 20A	-	58	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	$V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 25^{\circ}C$	-	6.5	-	А
Diode Reverse Recovery Charge	Q _{rr}		-	0.21	-	μC
Diode Reverse Recovery Time	t _{rr}	I _F = 20A	-	236	-	ns
Diode Peak Reverse Recovery Current	I _{rr}	$V_{CC} = 400V$ $di_F/dt = 200A/\mu s$ $T_j = 175^{\circ}C$	-	10.7	-	Α
Diode Reverse Recovery Charge	Q_{rr}		-	1.36	-	μC

Fig.1 Power Dissipation vs. Case Temperature

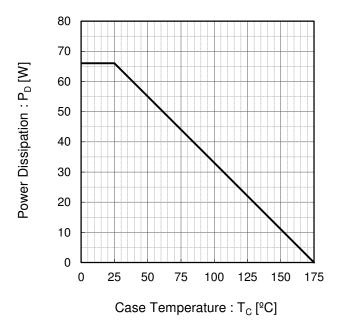


Fig.2 Collector Current vs. Case Temperature

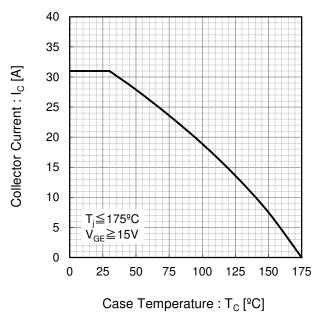


Fig.3 Forward Bias Safe Operating Area

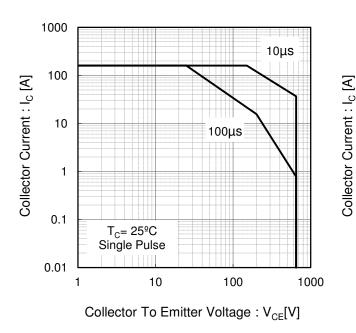
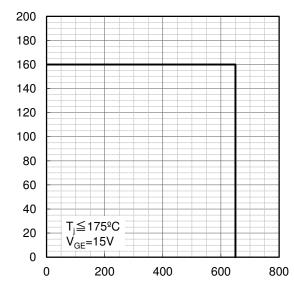


Fig.4 Reverse Bias Safe Operating Area



Collector To Emitter Voltage : $V_{CE}[V]$

Fig.5 Typical Output Characteristics

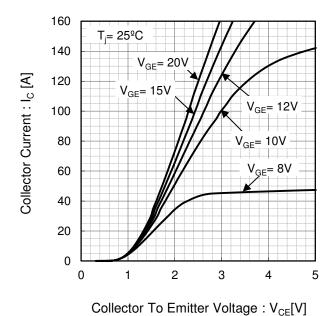
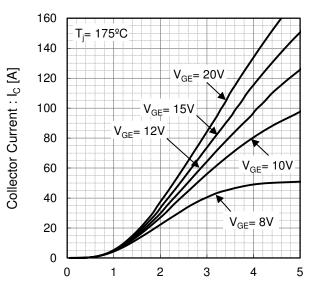


Fig.6 Typical Output Characteristics



Collector To Emitter Voltage: V_{CE}[V]

Fig.7 Typical Transfer Characteristics

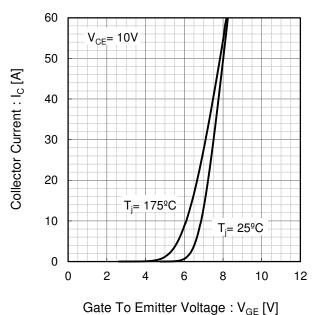
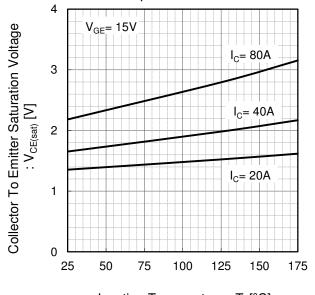
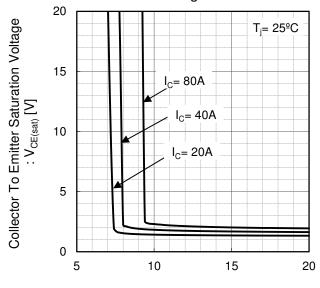


Fig.8 Typical Collector To Emitter Saturation Voltage vs. Junction Temperature



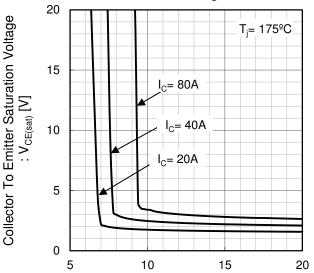
Junction Temperature : T_i [°C]

Fig.9 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage

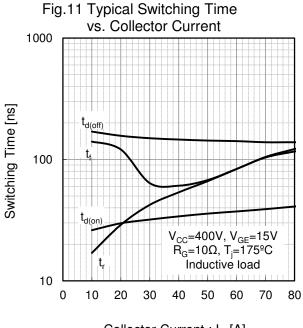


Gate To Emitter Voltage: V_{GE} [V]

Fig.10 Typical Collector To Emitter Saturation Voltage vs. Gate To Emitter Voltage



Gate To Emitter Voltage: V_{GE} [V]



Collector Current : I_C [A]

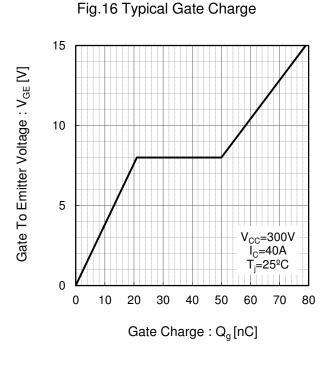
Fig.12 Typical Switching Time vs. Gate Resistance 1000 Switching Time [ns] 100 V_{CC}=400V, I_C=40A V_{GE}=15V, T_i=175°C $t_{d(on)}$ Inductive load 10 0 10 20 30 40 50 Gate Resistance : $R_G[\Omega]$

Fig.13 Typical Switching Energy Losses vs. Collector Current 10 Switching Energy Losses [mJ] 1 Eor 0.1 V_{CC} =400V, V_{GE} =15V R_{G} =10 Ω , T_{j} =175 $^{\circ}$ C Înductive load 0.01 60 0 10 20 30 40 50 70 80 Collector Current : I_C [A]

vs. Gate Resistance 10 Switching Energy Losses [mJ] $\mathsf{E}_{\mathsf{off}}$ 1 Eon 0.1 V_{CC} =400V, I_{C} =40A V_{GE} =15V, T_{j} =175 $^{\circ}$ C Inductive load 0.01 0 10 20 30 40 50 Gate Resistance : $R_G[\Omega]$

Fig.14 Typical Switching Energy Losses

Fig.15 Typical Capacitance vs. Collector To Emitter Voltage 10000 Cies 1000 Capacitance [pF] Coes 100 Cres 10 f=1MHz $V_{GE}=0V$ T_i=25ºC 1 0.01 0.1 1 10 100 Collector To Emitter Voltage: V_{CE}[V]



8/11

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Fig.17 Typical Diode Forward Current vs. Forward Voltage 160 140 Forward Current : I_F [A] 120 100 80 60 40 = 175ºC T_i= 25ºC 20 0 0 0.5 1.5 2 2.5 3

Fig.18 Typical Diode Reverse Recovery Time vs. Forward Current 400 V_{CC} =400V di_F/dt=200A/µs Reverse Recovery Time: t_{rr} [ns] Inductive load 300 T_j= 175ºC 200 100 T_i= 25ºC 0 0 10 20 30 40 50

Fig.19 Typical Diode Reverse Recovery Current vs. Forward Current

Forward Voltage : V_F[V]

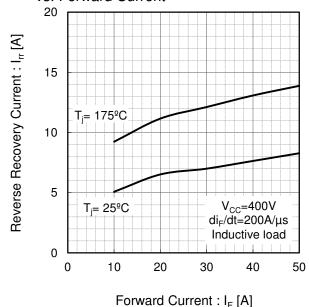
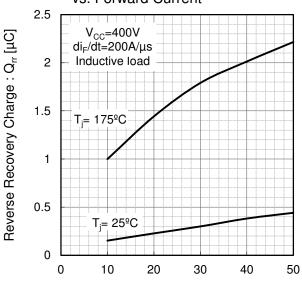


Fig.20 Typical Diode Reverse Recovery Charge vs. Forward Current

Forward Current : I_F [A]



Forward Current : I_F [A]

Fig.21 IGBT Transient Thermal Impedance

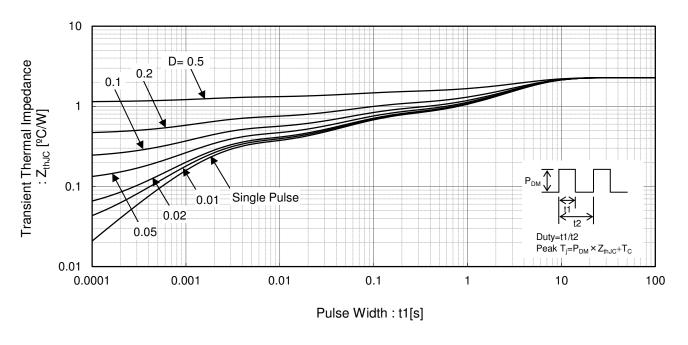
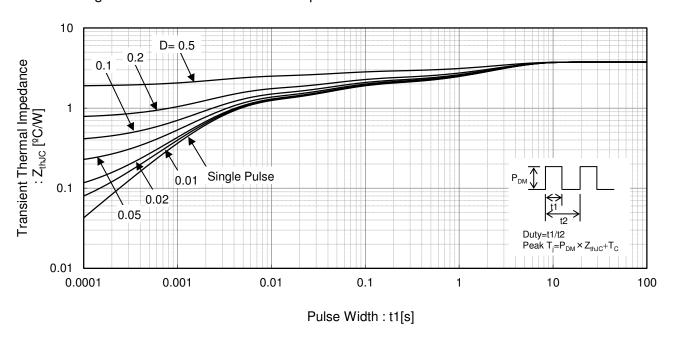


Fig.22 Diode Transient Thermal Impedance



●Inductive Load Switching Circuit and Waveform

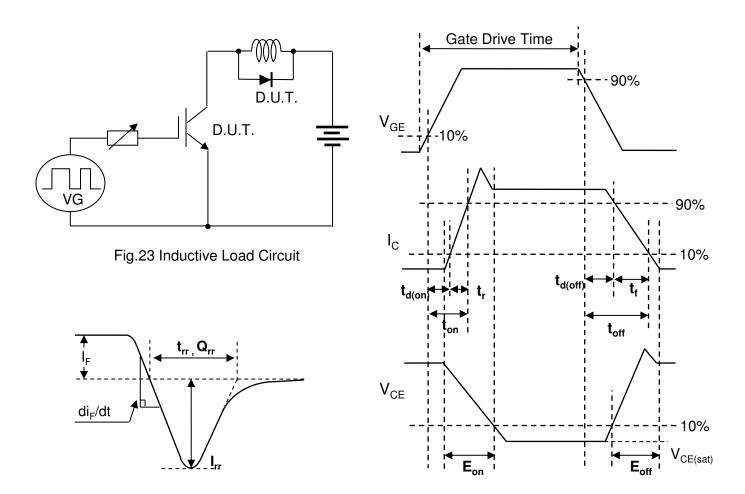


Fig.25 Diode Reverce Recovery Waveform

Fig.24 Inductive Load Waveform

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