

Dual N-Channel Enhancement Mode Power MOSFET

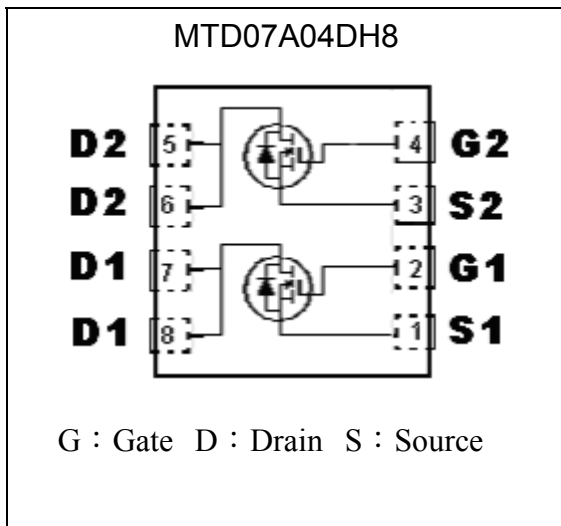
MTD07A04DH8

BV_{DSS}	40V
I_D@V_{GS}=10V, T_C=25°C	40A
I_D@V_{GS}=10V, T_C=100°C	25.3A
I_D@V_{GS}=10V, T_A=25°C	9A
I_D@V_{GS}=10V, T_A=70°C	7.2A
R_{DS(ON)}@V_{GS}=10V, I_D=17A	6.7mΩ (typ)
R_{DS(ON)}@V_{GS}=4.5V, I_D=10A	8.7mΩ (typ)

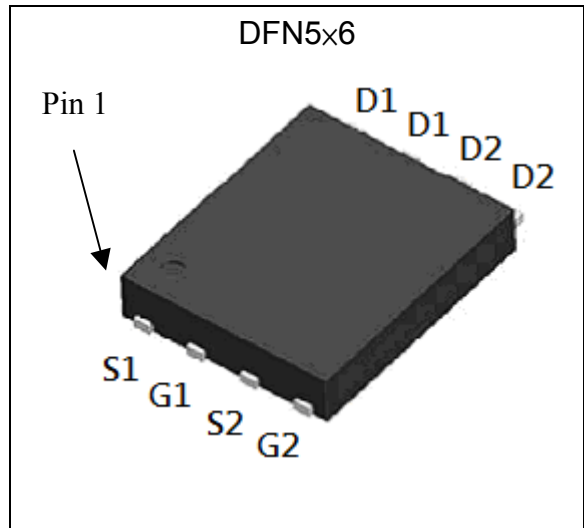
Features

- Low On Resistance
- Simple Drive Requirement
- Low Gate Charge
- Fast Switching Characteristic
- Pb-free lead plating and Halogen-free package

Equivalent Circuit

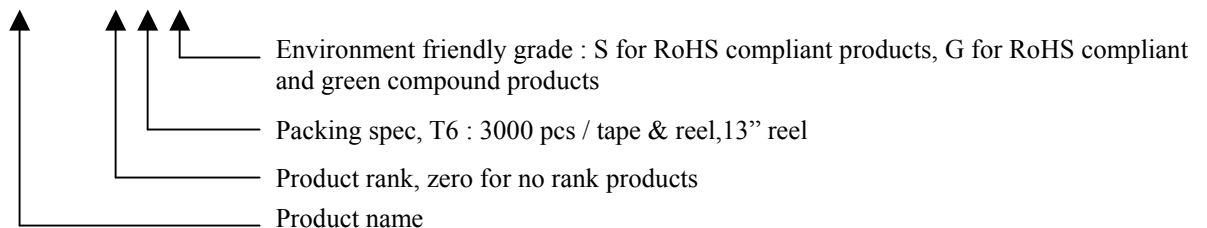


Outline



Ordering Information

Device	Package	Shipping
MTD07A04DH8-0-T6-G	DFN 5 ×6 (Pb-free lead plating and halogen-free package)	3000 pcs / tape & reel





Absolute Maximum Ratings ($T_C=25^{\circ}C$, unless otherwise noted)

Parameter		Symbol	Limits	Unit
Drain-Source Voltage		V_{DS}	40	V
Gate-Source Voltage		V_{GS}	± 20	
Continuous Drain Current @ $T_C=25^{\circ}C$, $V_{GS}=10V$ (Note 1)		I_D	40	A
Continuous Drain Current @ $T_C=100^{\circ}C$, $V_{GS}=10V$ (Note 1)			25.3	
Continuous Drain Current @ $T_A=25^{\circ}C$, $V_{GS}=10V$ (Note 2)		I_{DSM}	9	
Continuous Drain Current @ $T_A=70^{\circ}C$, $V_{GS}=10V$ (Note 2)			7.2	
Pulsed Drain Current @ $V_{GS}=10V$ (Note 3)		I_{DM}	160	
Avalanche Current (Note 3)		I_{AS}	20	
Single Pulse Avalanche Energy @ $L=0.5mH$, $I_D=20Amps$, $V_{DD}=50V$ (Note 5)		E_{AS}	100	mJ
Repetitive Avalanche Energy (Note 3)		E_{AR}	3.1	W
Power Dissipation	$T_C=25^{\circ}C$ (Note 1)	P_D	31	
	$T_C=100^{\circ}C$ (Note 1)		12.4	
	$T_A=25^{\circ}C$ (Note 2)	P_{DSM}	1.5	
	$T_A=70^{\circ}C$ (Note 2)		0.96	
Operating Junction and Storage Temperature		T_j, T_{stg}	-55~+150	$^{\circ}C$

*Drain current limited by maximum junction temperature

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{\theta JC}$	4	$^{\circ}C/W$
Thermal Resistance, Junction-to-ambient, max (Note 4)	$R_{\theta JA}$	85	

- Note :
- The power dissipation P_D is based on $T_{j(MAX)}=150^{\circ}C$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
 - The value of $R_{\theta JA}$ is measured with the device mounted on 1 in²FR-4 board with 2 oz. copper, in a still air environment with $T_A=25^{\circ}C$. The value in any given application depends on the user's specific board design. The power dissipation P_{DSM} is based on $R_{\theta JA}$ and the maximum allowed junction temperature of $150^{\circ}C$.
 - Ratings are based on low frequency and low duty cycles to keep initial $T_j=25^{\circ}C$.
 - When mounted on 1 in² copper pad of FR-4 board ; $125^{\circ}C/W$ when mounted on minimum copper pad.
 - 100% tested by conditions of $L=0.5mH$, $I_{AS}=12A$, $V_{GS}=10V$, $V_{DD}=25V$.

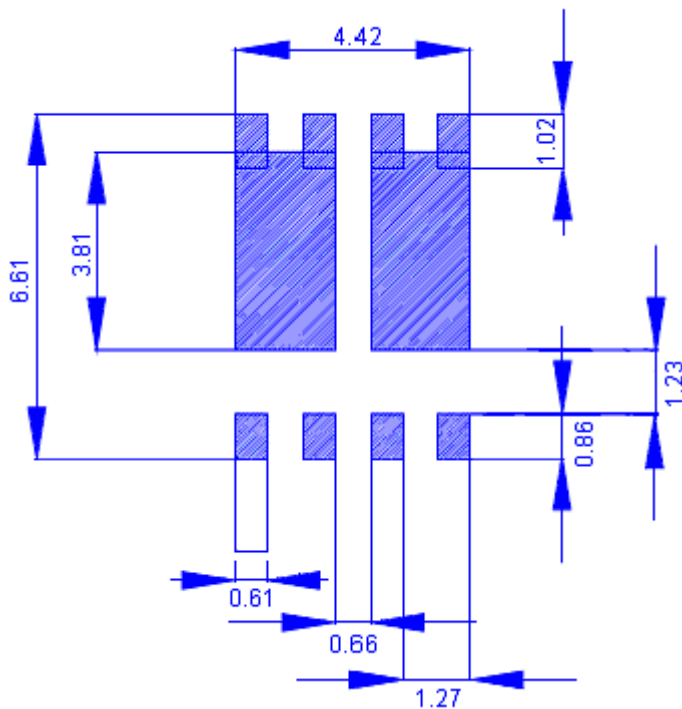
Characteristics ($T_j=25^{\circ}C$, unless otherwise specified)

Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV_{DSS}	40	-	-	V	$V_{GS}=0V, I_D=250\mu A$
$\Delta BV_{DSS}/\Delta T_j$	-	0.03	-	$V/^{\circ}C$	Reference to $25^{\circ}C, I_D=250\mu A$
$V_{GS(th)}$	1.5	-	2.5	V	$V_{DS} = V_{GS}, I_D=250\mu A$
* G_{FS}	-	24	-	S	$V_{DS} = 5V, I_D=17A$
I_{GSS}	-	-	± 100	nA	$V_{GS}=\pm 20V$
I_{DSS}	-	-	1	μA	$V_{DS} = 40V, V_{GS} = 0V$
	-	-	25		$V_{DS} = 40V, V_{GS} = 0V, T_j=125^{\circ}C$

*R _{DS(ON)}	-	6.7	9.3	mΩ	V _{GS} =10V, I _D =17A
	-	8.7	13.8		V _{GS} =4.5V, I _D =10A
Dynamic					
*Q _g	-	29.4	44.1	nC	V _{DS} =20V, I _D =16A, V _{GS} =10V
*Q _{gs}	-	6.5	-		
*Q _{gd}	-	7.3	-		
*t _{d(ON)}	-	15.6	24	ns	V _{DS} =20V, I _D =1A, V _{GS} =10V, R _G =6Ω
*t _r	-	19.6	30		
*t _{d(OFF)}	-	49.6	75		
*t _f	-	12.4	19		
C _{iss}	-	1514	1895	pF	V _{GS} =0V, V _{DS} =20V, f=1MHz
C _{oss}	-	188	235		
C _{rss}	-	119	150		
R _g	-	1.7	-	Ω	f=1MHz
Source-Drain Diode					
*I _S	-	-	40	A	
*I _{SM}	-	-	160		
*V _{SD}	-	0.83	1.1	V	I _S =17A, V _{GS} =0V
*t _{rr}	-	14.3	-	ns	V _{GS} =0, I _F =10A, dI/dt=100A/μs
*Q _{rr}	-	9.2	-	nC	

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

Recommended Soldering Footprint

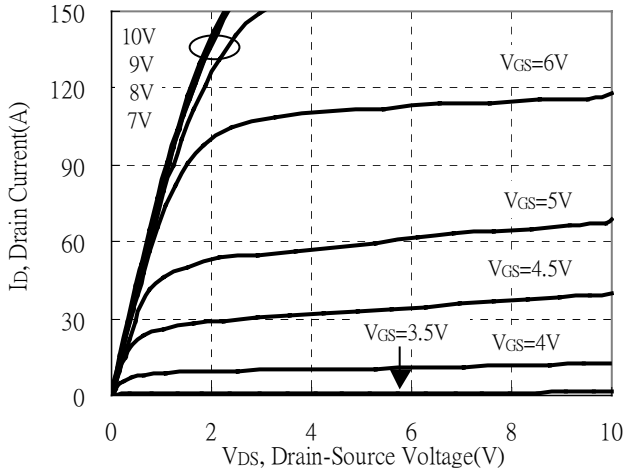


unit : mm

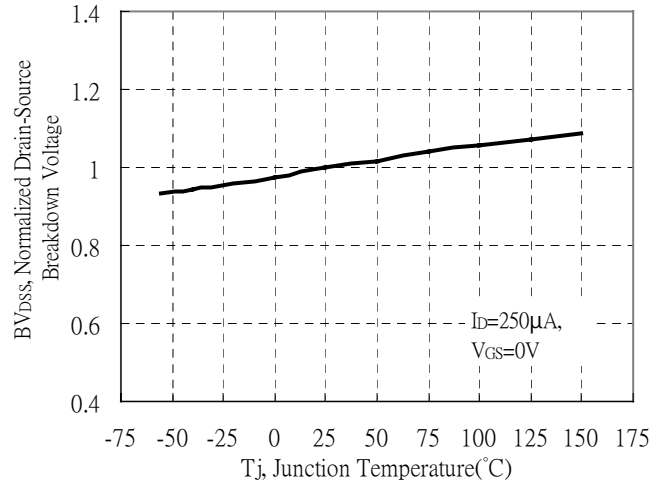


Typical Characteristics

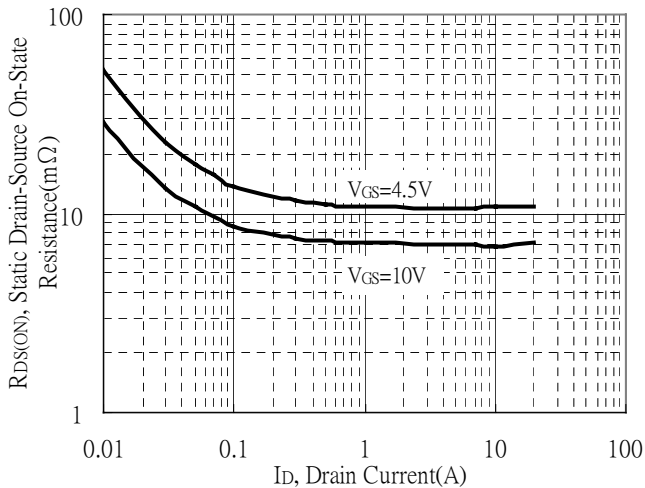
Typical Output Characteristics



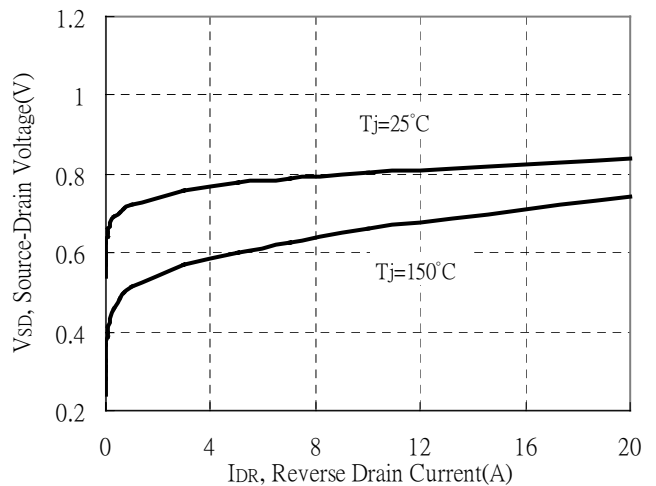
Breakdown Voltage vs Ambient Temperature



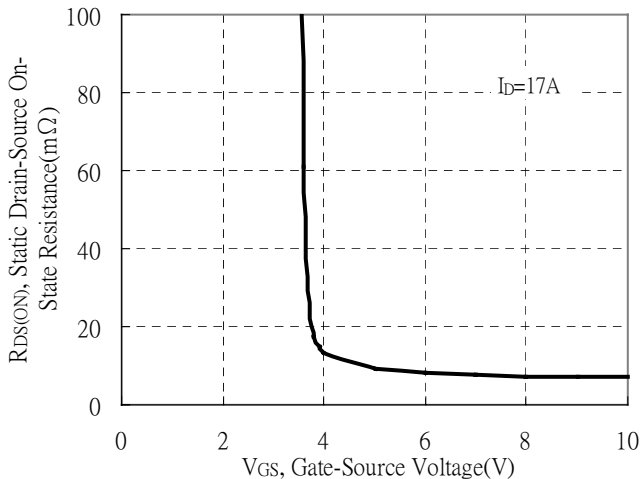
Static Drain-Source On-State resistance vs Drain Current



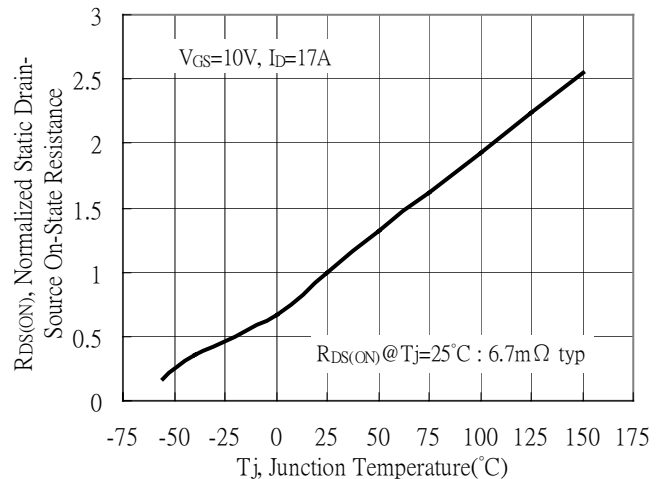
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

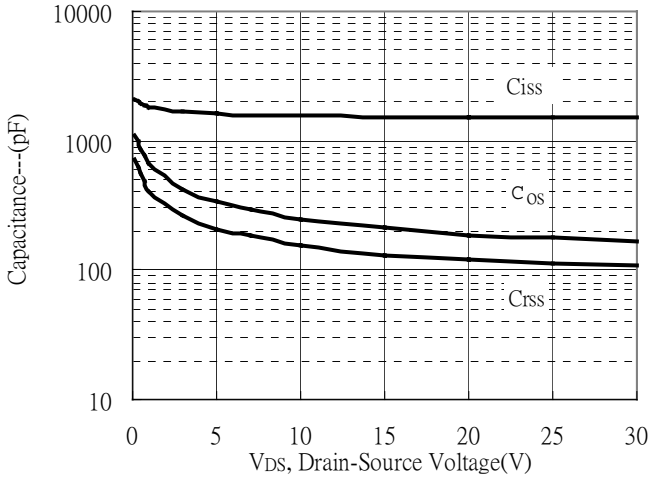


Drain-Source On-State Resistance vs Junction Temperature

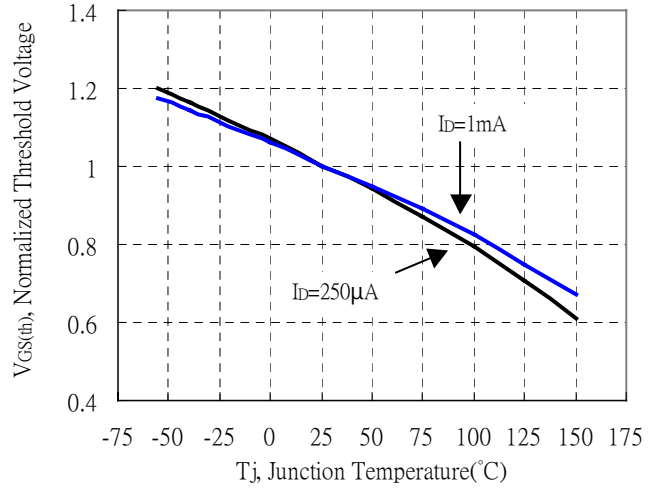


Typical Characteristics(Cont.)

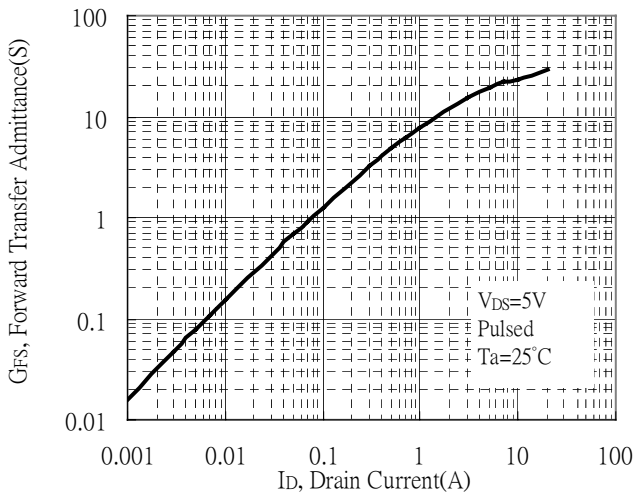
Capacitance vs Drain-to-Source Voltage



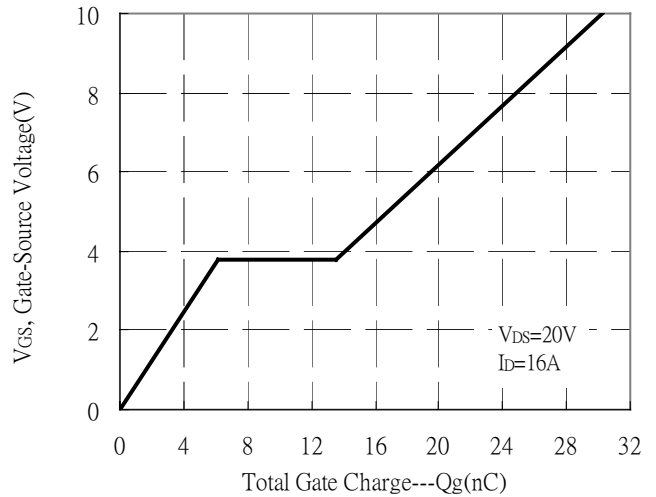
Normalized Threshold Voltage vs Junction Temperature



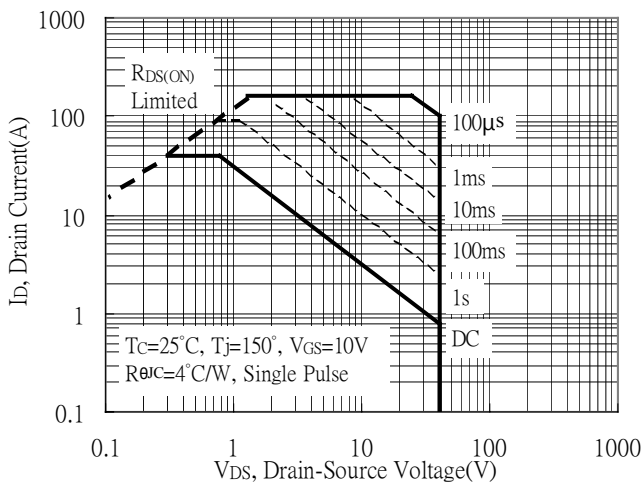
Forward Transfer Admittance vs Drain Current



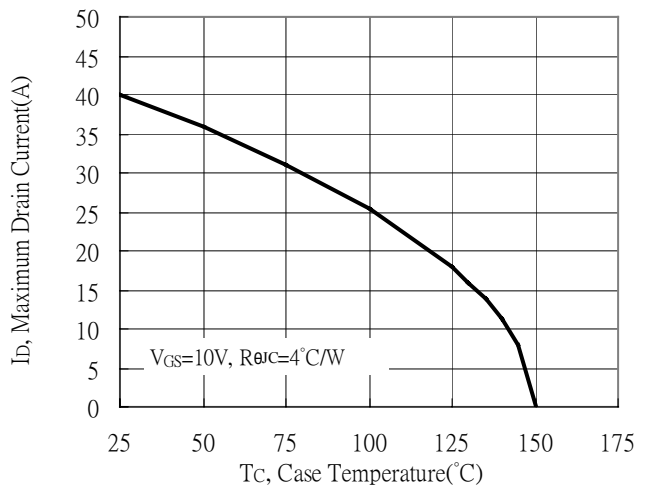
Gate Charge Characteristics



Maximum Safe Operating Area

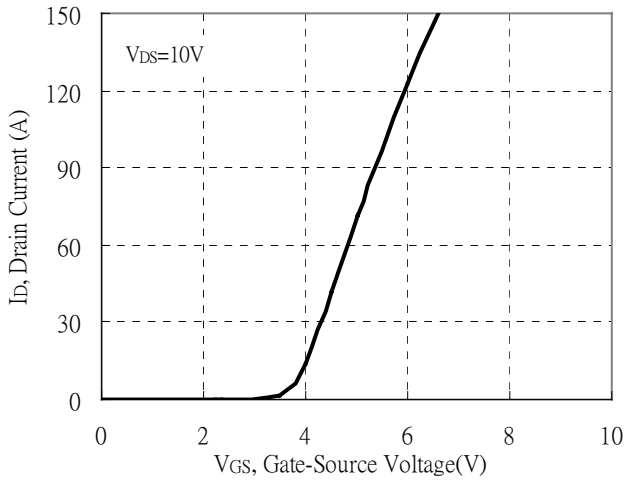


Maximum Drain Current vs Case Temperature

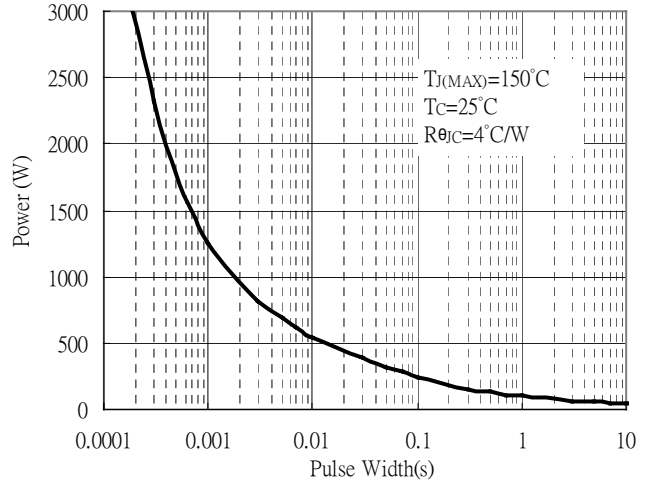


Typical Characteristics(Cont.)

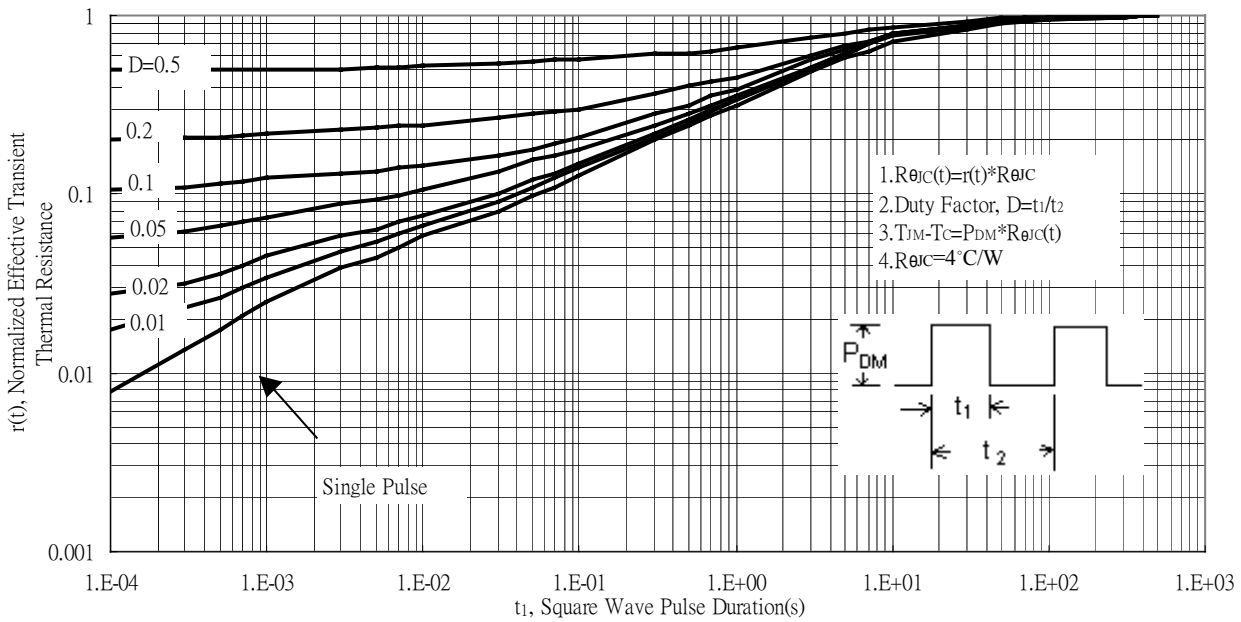
Typical Transfer Characteristics



Single Pulse Maximum Power Dissipation

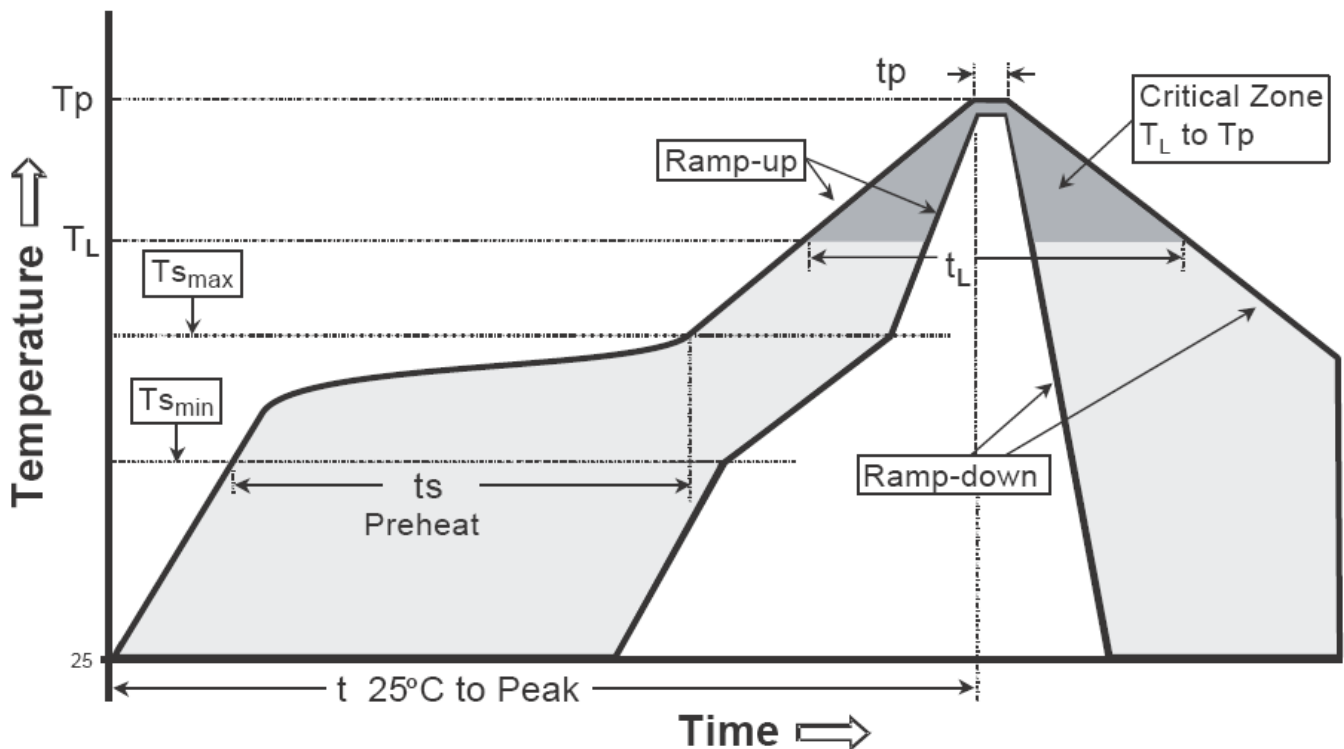


Transient Thermal Response Curves



Recommended wave soldering condition

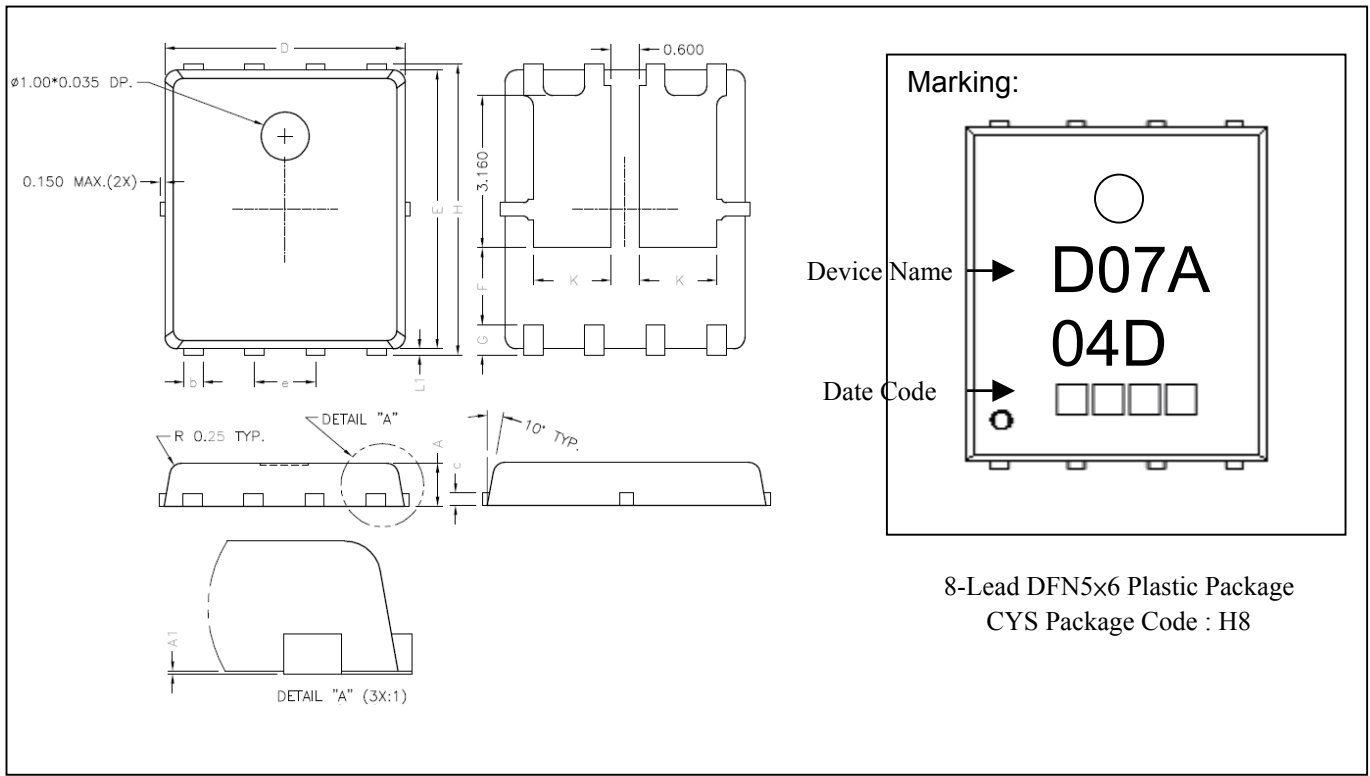
Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

Recommended temperature profile for IR reflow


Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (T _{smax} to T _p)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T _{s min})	100°C	150°C
-Temperature Max(T _{s max})	150°C	200°C
-Time(t _{s min} to t _{s max})	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T _L)	183°C	217°C
- Time (t _L)	60-150 seconds	60-150 seconds
Peak Temperature(T _P)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(tp)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

DFN5x6 Dimension



8-Lead DFN5x6 Plastic Package
 CYS Package Code : H8

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	0.80	1.00	0.031	0.039	E	5.70	5.90	0.224	0.232
A1	0.00	0.05	0.000	0.002	e	1.27 BSC		0.050	BSC
b	0.35	0.49	0.014	0.019	H	5.95	6.20	0.234	0.244
c	0.254 REF		0.010 REF		L1	0.10	0.18	0.004	0.007
D	4.90	5.10	0.193	0.201	G	0.60 REF		0.024	REF
F	1.60 REF		0.063 REF		K	1.60 REF		0.063	REF

Notes: 1. Controlling dimension: millimeters.
 2. Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3. If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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