

C1220X7R0J105M085AC

TDK Item Description : C1220X7R0J105MT****



Application & Main Feature

Commercial Grade (Low ESL Reverse Geometry)

Series

C1220 [EIA CC0508]



Dimensions

L 1.25mm +/-0.2mm
 W 2.00mm +/-0.2mm
 T 0.85mm +/-0.15mm
 B 0.20mm Min.

Temperature Characteristic

X7R (-55 to 125 degC +/-15%)

Rated Voltage

0J (6.3Vdc)

Capacitance

1uF

Capacitance Tolerance

M (+/-20%)

Dissipation Factor

5% Max.

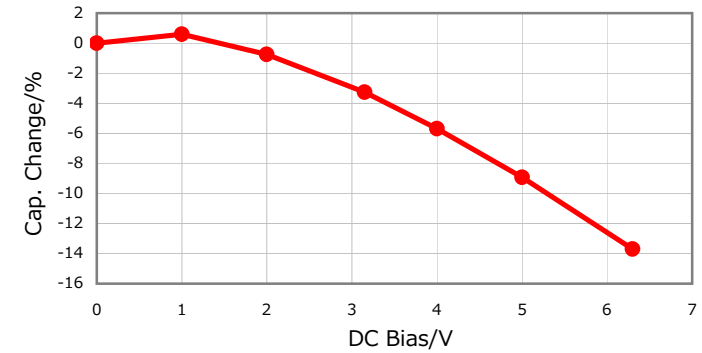
Insulation Resistance

100Mohm Min.

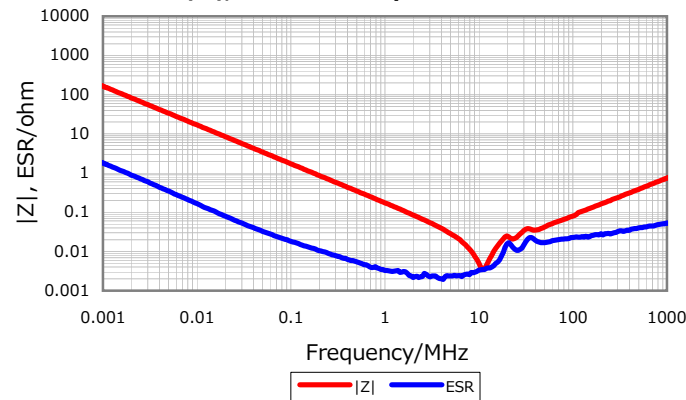
AEC-Q200

Not Applicable

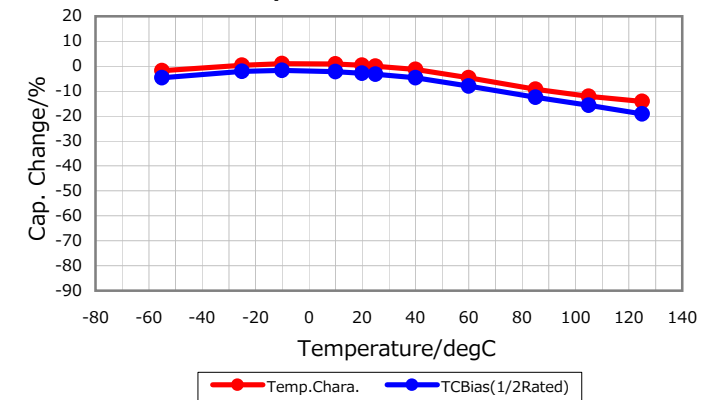
DC Bias Characteristic



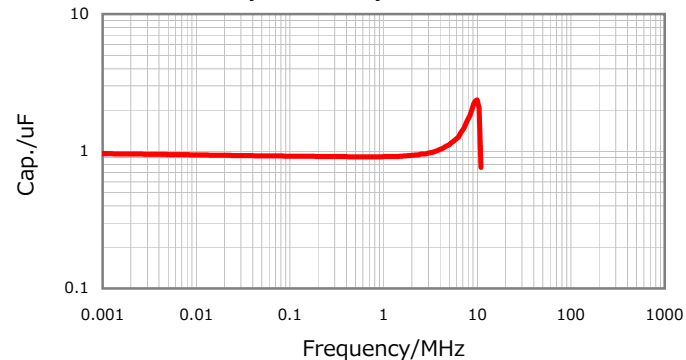
|Z|, ESR vs. Freq. Characteristics



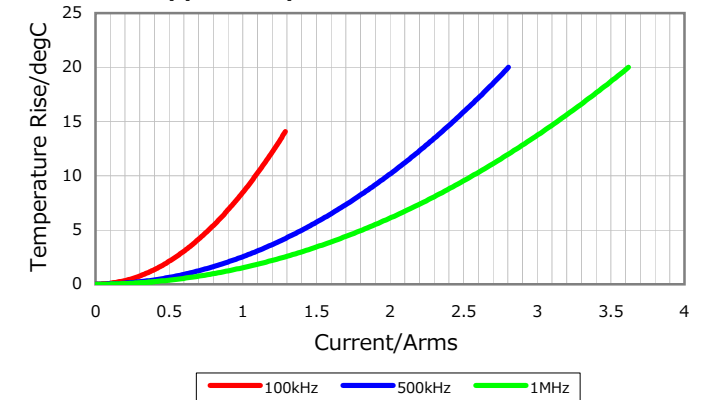
Temperature Characteristics



Cap. vs. Freq. Characteristic



Ripple Temperature Rise Characteristics



Characterization Sheet (Multilayer Ceramic Chip Capacitors)



All specifications are subject to change without notice.

January 3, 2016