

# N-Channel 100 V (D-S) MOSFET

## PRODUCT SUMMARY

$V_{DS}$ (V)	$R_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
100	0.030 at $V_{GS} = 10$ V	38.5
	0.034 at $V_{GS} = 6$ V	36

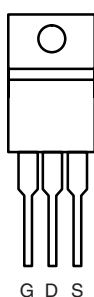
## FEATURES

- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET® Power MOSFET
- Compliant to RoHS Directive 2002/95/EC

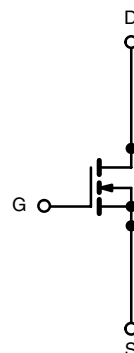


**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**  
Available

TO-220AB



Top View



N-Channel MOSFET

Ordering Information: SUP40N10-30-GE3 (Lead (Pb)-free and Halogen-free)

## ABSOLUTE MAXIMUM RATINGS $T_C = 25$ °C, unless otherwise noted

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current ( $T_J = 150$ °C)	$I_D$	$T_C = 25$ °C	A
		$T_C = 125$ °C	
Pulsed Drain Current	$I_{DM}$	75	
Avalanche Current	$I_{AS}$	35	
Single Pulse Avalanche Energy <sup>a</sup>	$E_{AS}$	61	mJ
Maximum Power Dissipation <sup>a</sup>	$P_D$	$T_C = 25$ °C	W
		$T_A = 25$ °C <sup>c</sup>	
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to 150	°C

## THERMAL RESISTANCE RATINGS

Parameter	Symbol	Limit	Unit
Junction-to-Ambient	$R_{thJA}$	40	°C/W
		62.5	
Junction-to-Case (Drain)	$R_{thJC}$	1.4	

Notes:

a. Duty cycle  $\leq 1$  %.

b. See SOA curve for voltage derating.

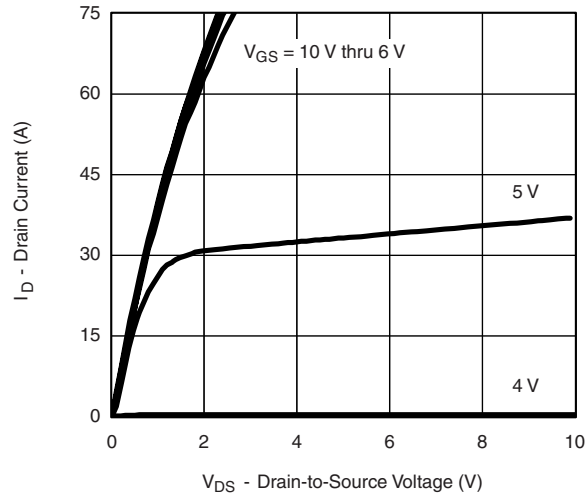
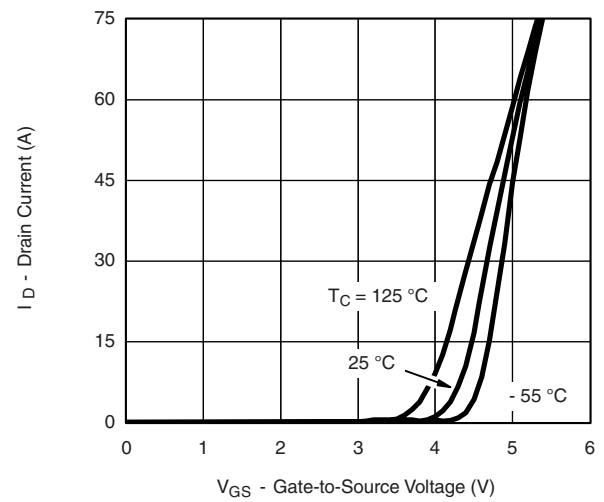
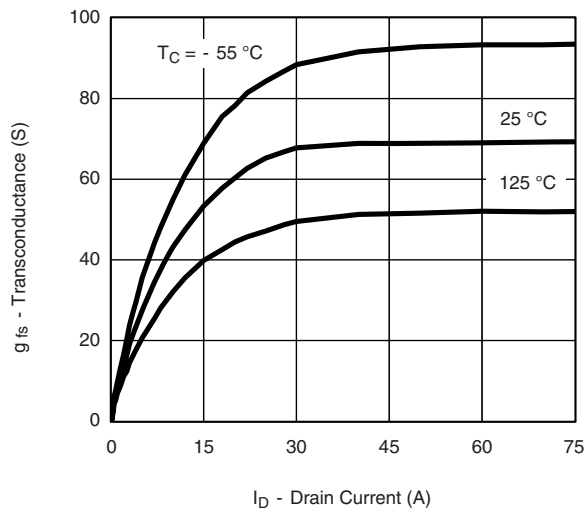
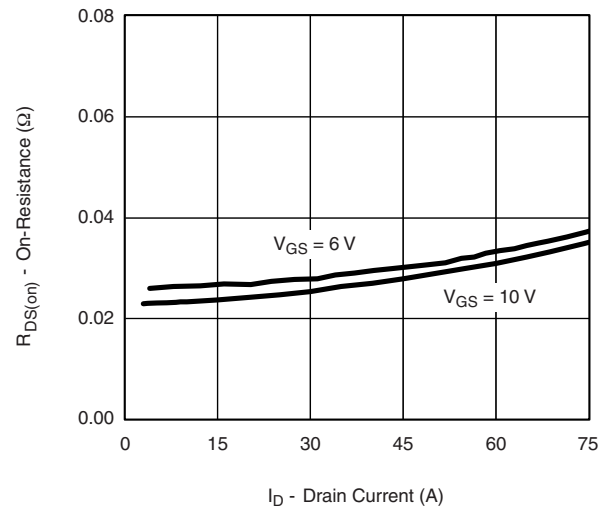
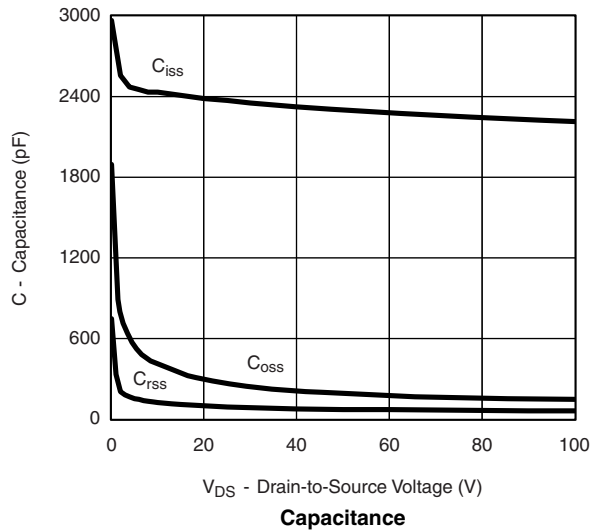
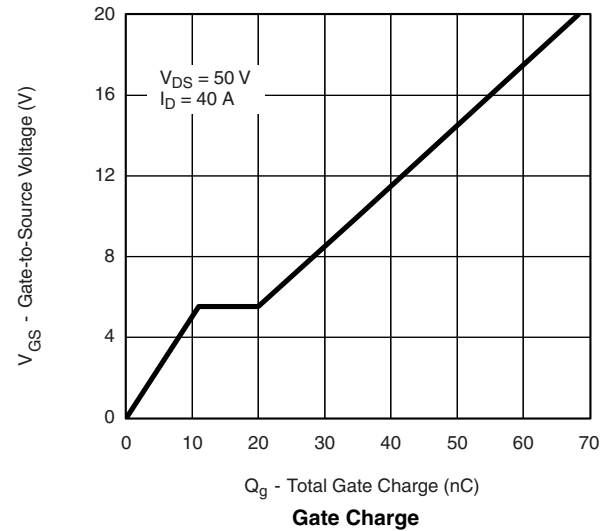
c. When mounted on 1" square PCB (FR-4 material).

SPECIFICATIONS T <sub>J</sub> = 25 °C, unless otherwise noted						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	100			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	2		4	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V			1	μA
		V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	
		V <sub>DS</sub> = 80 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 150 °C			150	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> ≥ 5 V, V <sub>GS</sub> = 10 V	75			A
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A		0.024	0.030	Ω
		V <sub>GS</sub> = 6 V, I <sub>D</sub> = 10 A		0.026	0.034	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A, T <sub>J</sub> = 125 °C			0.054	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A, T <sub>J</sub> = 150 °C			0.060	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 15 A	10			S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz		2400		pF
Output Capacitance	C <sub>oss</sub>			270		
Reverse Transfer Capacitance	C <sub>rss</sub>			90		
Total Gate Charge <sup>c</sup>	Q <sub>g</sub>	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 40 A		35	60	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>			11		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			9		
Gate Resistance	R <sub>g</sub>			1.7		Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 50 V, R <sub>L</sub> = 1.25 Ω I <sub>D</sub> ≅ 40 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 2.5 Ω		11	20	ns
Rise Time <sup>c</sup>	t <sub>r</sub>			12	20	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			30	45	
Fall Time <sup>c</sup>	t <sub>f</sub>			12	20	
Source-Drain Diode Ratings and Characteristics T <sub>C</sub> = 25 °C <sup>b</sup>						
Continuous Current	I <sub>S</sub>				40	A
Pulsed Current	I <sub>SM</sub>				75	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 30 A, V <sub>GS</sub> = 0 V		1.0	1.5	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 30 A, dI/dt = 100 A/μs		60	100	ns
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>			5	8	A
Reverse Recovery Charge	Q <sub>rr</sub>				0.15	0.4

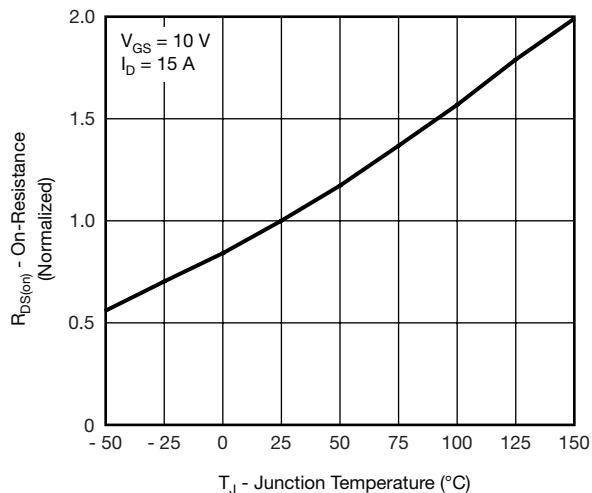
Notes:

- a. Pulse test; pulse width  $\leq 300\text{ }\mu\text{s}$ , duty cycle  $\leq 2\%$ .  
b. Guaranteed by design, not subject to production testing.  
c. Independent of operating temperature.

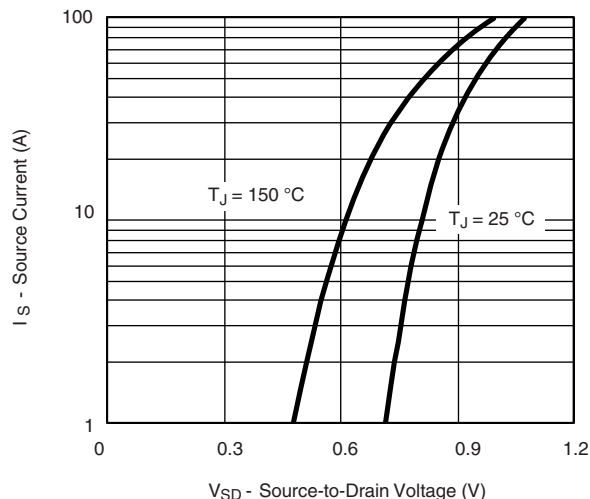
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

**TYPICAL CHARACTERISTICS** 25 °C, unless otherwise noted

**Output Characteristics**

**Transfer Characteristics**

**Transconductance**

**On-Resistance vs. Drain Current**

**Capacitance**

**Gate Charge**

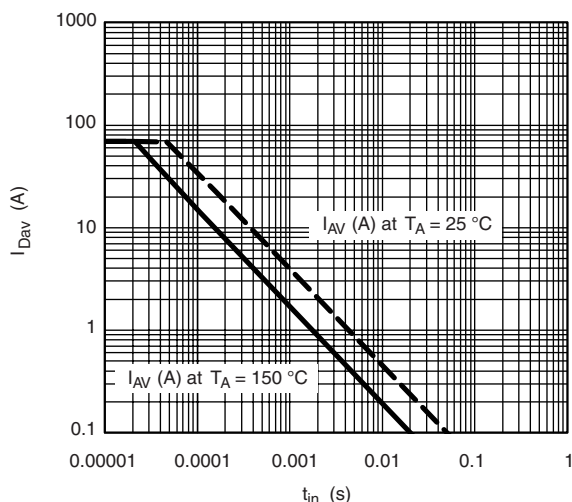
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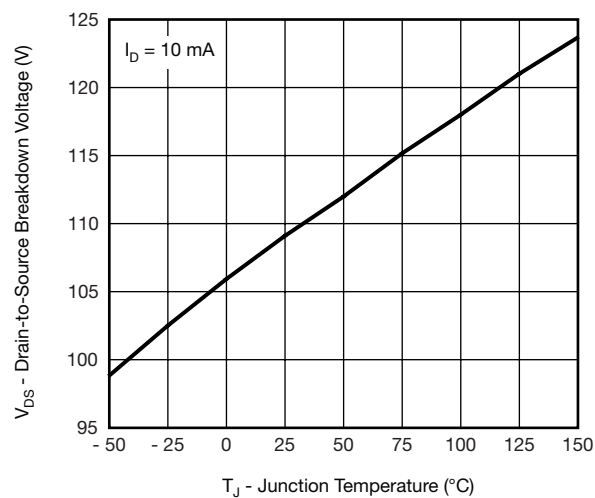
On-Resistance vs. Junction Temperature



Source-Drain Diode Forward Voltage

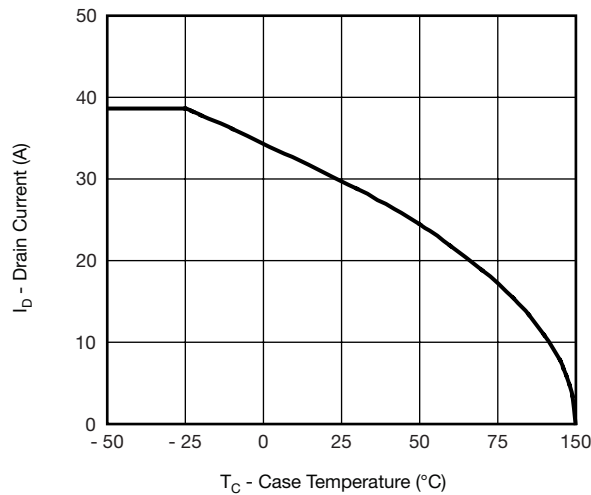


Avalanche Current vs. Time

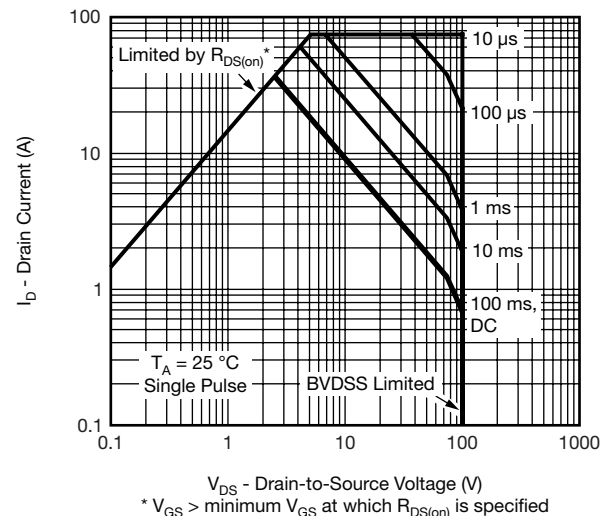


Drain-Source Breakdown Voltage vs. Junction Temperature

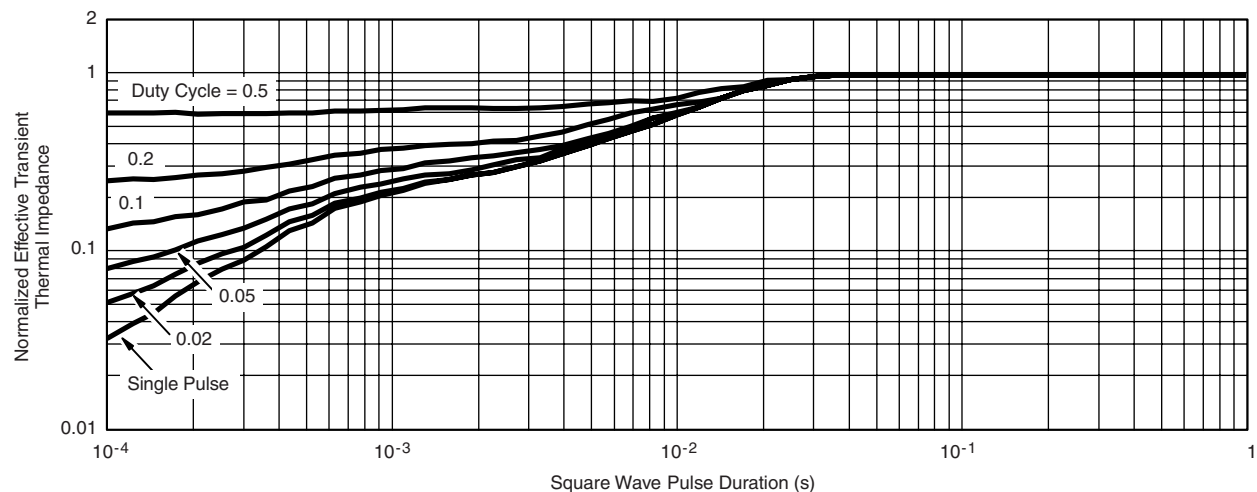
## THERMAL RATINGS



**Maximum Avalanche and Drain Current vs. Case Temperature**



**Safe Operating Area**



**Normalized Thermal Transient Impedance, Junction-to-Case**

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