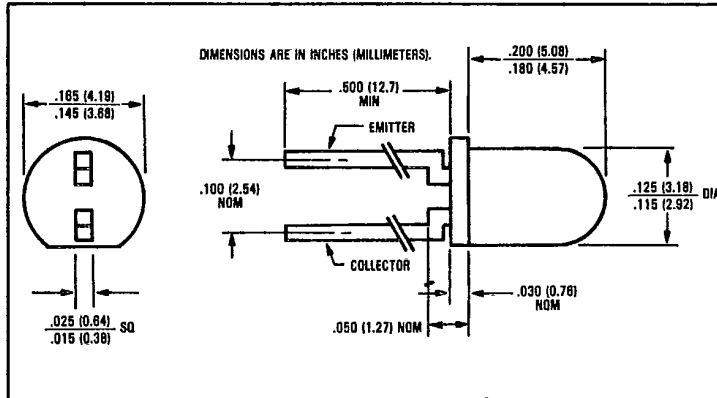
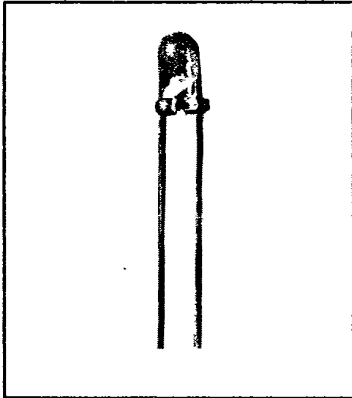




# Infrared Selected NPN Silicon Phototransistors

## Types OP501SR, OP501SRD, OP501SRC, OP501SRB, OP501SRA



### Features

- Tested using infrared for close correlation to TRW infrared emitters
- 0.100" (2.54 mm) lead spacing
- Wide range of collector currents
- Lensed for high sensitivity

### Description

The OP501SR and OP501SRD through SRA each consist of an NPN silicon phototransistor mounted in a lensed, clear plastic, end-looking package. The lensing effect of the package allows an acceptance half angle of 8° measured from the optical axis to the half power point. This series is identical to the OP500 except for lead spacing. The series is 100% factory tested using infrared for close correlation to TRW GaAs or GaAlAs emitters and the most accurate design-in possible. This series is mechanically and spectrally matched to the OP160SL and OP260SL series of infrared emitting diodes.

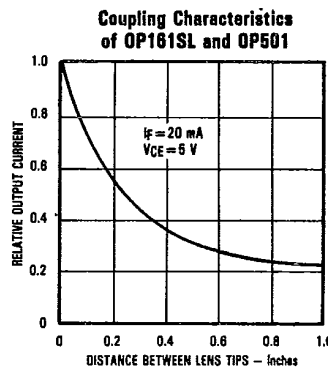
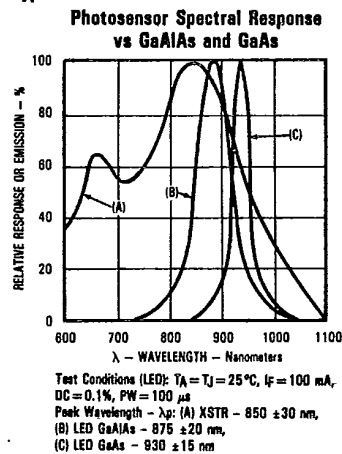
### Absolute Maximum Ratings (T<sub>A</sub> = 25°C unless otherwise noted)

Collector-Emitter Voltage	30 V
Emitter-Collector Voltage	5.0 V
Storage and Operating Temperature Range	-40°C to +100°C
Lead Soldering Temperature (1/16 inch [1.6 mm] from case for 5 sec. with soldering iron) <sup>(1)</sup>	240°C
Power Dissipation	100 mW <sup>(2)</sup>

### Notes:

- (1) RMA flux is recommended. Duration can be extended to 10 sec. max. when wave soldering.
- (2) Derate linearly 1.33 mW/°C above 25°C.
- (3) Junction temperature maintained at 25°C.
- (4) Light source is an unfiltered GaAs LED with a peak emission wavelength of 930 nm and a radiometric intensity level which varies less than 10% over the entire lens surface of the phototransistor being tested.
- (5) To calculate typical collector dark current in  $\mu A$ , use the formula  $I_{CE0} = 10^{(0.040 T_A - 3.4)}$  where T<sub>A</sub> is ambient temperature in °C.

### Typical Performance Curves



Types OP501SR, OP501SRD, OP501SRC, OP501SRB, OP501SRA

T-41-61

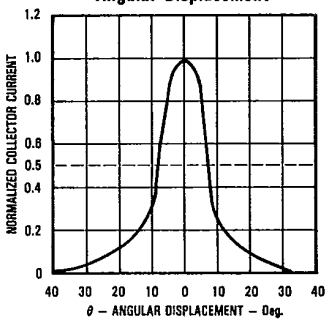
Electrical Characteristics (TA = 25°C unless otherwise noted)

Symbol	Parameter	Min.	Typ.	Max.	Units	Test Conditions
$I_C(ION)^{(3)}$	On-State Collector Current	OP501SR OP501SRD OP501SRC OP501SRB OP501SRA	0.080 0.080 0.160 0.32 0.64	0.24 0.48 0.96	mA	$V_{CE} = 5.0 V, E_0 = 0.130 mW/cm^2$ (4) $V_{CE} = 5.0 V, E_0 = 0.130 mW/cm^2$ (4) $V_{CE} = 5.0 V, E_0 = 0.130 mW/cm^2$ (4) $V_{CE} = 5.0 V, E_0 = 0.130 mW/cm^2$ (4) $V_{CE} = 5.0 V, E_0 = 0.130 mW/cm^2$ (4)
$\Delta I_C/\Delta T$	Relative $I_C$ Changes with Temperature		1.00		%/°C	$V_{CE} = 5.0 V, E_0 = 1.00 mW/cm^2, \lambda = 830 nm$
$I_{CEO}^{(5)}$	Collector Dark Current			100	nA	$V_{CE} = 10.0 V, E_0 = 0$
$V_{(BR)CEO}$	Collector-Emitter Breakdown Voltage	30			V	$I_C = 100 \mu A$
$V_{(BR)ECO}$	Emitter-Collector Breakdown Voltage	5.0			V	$I_E = 100 \mu A$
$V_{CE(SAT)}^{(3)}$	Collector-Emitter Saturation Voltage		0.50		V	$I_C = 50 \mu A, E_0 = 0.130 mW/cm^2$ (4), $\lambda = 830 nm$

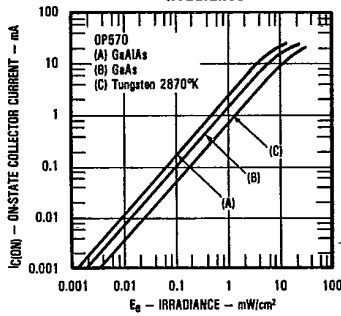


Typical Performance Curves

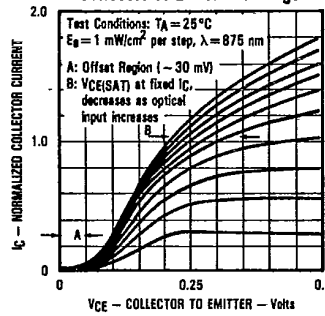
Normalized Collector Current vs Angular Displacement



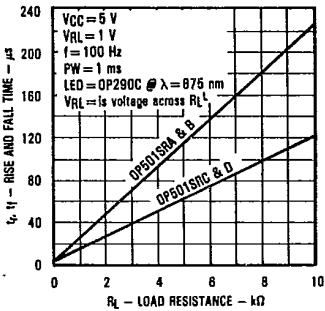
On-State Collector Current vs Irradiance



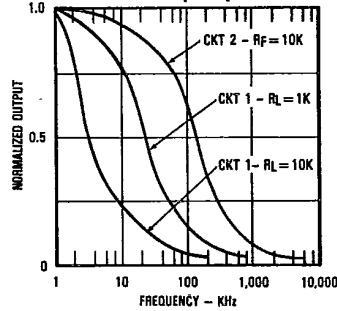
Normalized Collector Current vs Collector-to-Emitter Voltage



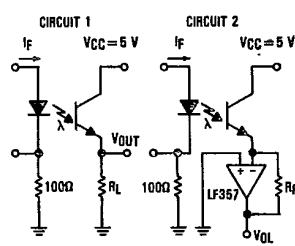
Rise and Fall Time vs Load Resistance



Normalized Output vs Frequency



Switching Time Test Circuit



Test Conditions:  
Light source is pulsed LED with  $t_r$  and  $t_f \leq 500 ns$ .  
 $I_f$  is adjusted for  $V_{OUT} = 1 Volt$ .

TRW reserves the right to make changes at any time in order to improve design and to supply the best product possible.

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