

UNISONIC TECHNOLOGIES CO., LTD

MGBR10L40C

Preliminary

DIODE

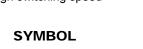
DUAL MOS GATED BARRIER RECTIFIER

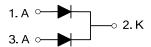
■ DESCRIPTION

The UTC MGBR10L40C is a dual mos gated barrier rectifiers, it uses UTC's advanced technology to provide customers with low forward voltage drop and high switching speed, etc.

■ FEATURES

- * Low forward voltage drop
- * High switching speed

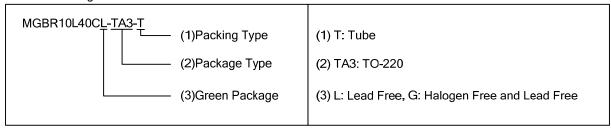




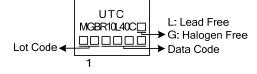
■ ORDERING INFORMATION

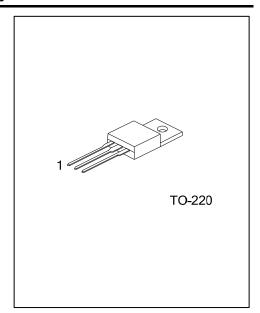
Ordering Number		Packago	Pin Assignment			Packing	
Lead Free	Halogen Free	Package	1	2	3	Facking	
MGBR10L40CL-TA3-T	GBR10L40CL-TA3-T MGBR10L40CG-TA3-T		Α	K	Α	Tube	

Note: Pin Assignment: A: Anode K: Cathode



■ MARKING





■ ABSOLUTE MAXIMUM RATINGS (PER LEG) (T_A=25°C unless otherwise specified)

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

PARAMETER		SYMBOL	RATINGS	UNIT
DC Blocking Voltage	V_{RM}	40	V	
Working Peak Reverse Voltage		V_{RWM}	40	V
Peak Repetitive Reverse Voltage		V_{RRM}	40	V
Average Rectified Forward Current	Per Leg	l _o	5	Α
(Rated VR-20KHz Square Wave) – 50% duty cycle	Total		10	Α
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load		I _{FSM}	120	Α
Peak Repetitive Reverse Surge Current (2µS-1kHz)		I_{RRM}	2	Α
Operating Junction Temperature		T_J	-65~+150	°C
Storage Temperature		T_{STG}	-65~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

■ THERMAL CHARACTERISTICS (PER LEG)

PARAMETER	SYMBOL	RATINGS	UNIT	
Junction to Ambient	θ_{JA}	62.5	°C/W	
Junction to Case	θ _{JC}	2	°C/W	

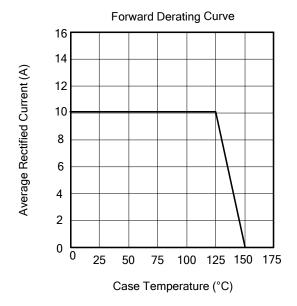
■ ELECTRICAL CHARACTERISTICS (T_A =25°C, unless otherwise specified.)

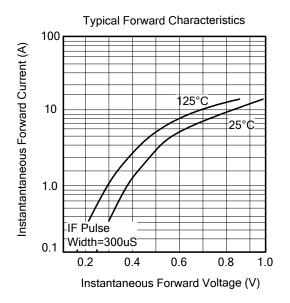
PARAMETER	SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNIT
Reverse Breakdown Voltage (Note 1)	$V_{(BR)R}$	I _R =0.50mA	40			V
Forward Voltage Drop	V _{FM}	I _F =5A, T _J =25°C			0.55	V
		I _F =5A, T _J =125°C			0.52	V
Leakage Current (Note 1)	I DM	V _R =40V, T _J =25°C			500	μΑ
		V _R =40V, T _J =125°C			50	mA

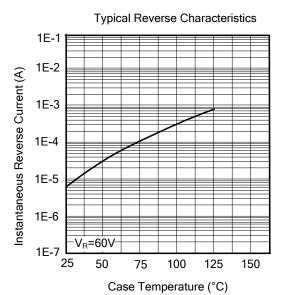
Notes: 1. Short duration pulse test used to minimize self-heating effect.

^{2.} Thermal resistance junction to case mounted on heatsink.

■ TYPICAL CHARACTERISTICS







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