

40V COMPLEMENTARY MEDIUM POWER TRANSISTOR IN SOT26

Features

- NPN + PNP combination
- BV_{CEO} > 40 (-40)V
- BV_{ECO} > 6 (-3)V
- I_{CM} = 9 (-9)A Peak Pulse Current
- V_{CE(sat)} < 60 (-90)mV @ 1A
- $R_{CE(sat)} = 38 (58) m\Omega$
- Totally Lead-Free & Fully RoHS compliant (Notes 1 & 2)
- Halogen and Antimony Free. "Green" Device (Note 3)
- Qualified to AEC-Q101 Standards for High Reliability

Description

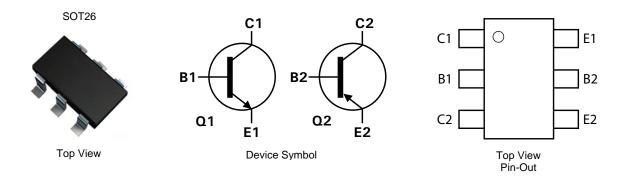
Advanced process capability has been used to achieve this high performance device. Combining NPN and PNP transistors in the SOT26 package provides a compact solution for the intended applications.

Mechanical Data

- Case: SOT26
- · Case Material: molded plastic, "Green" molding compound
- UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Finish Matte Tin Plated Leads, Solderable per MIL-STD-202, Method 208
- Weight: 0.015 grams (approximate)

Applications

- MOSFET and IGBT gate driving
- Motor drive



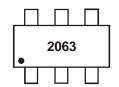
Ordering Information (Note 4)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
ZXTC2063E6TA	2063	7	8	3,000

Notes:

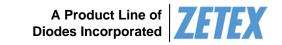
- 1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS) & 2011/65/EU (RoHS 2) compliant.
- 2. See http://www.diodes.com/ for more information about Diodes Incorporated's definitions of Halogen and Antimony free, "Green" and Lead-Free.
- 3. Halogen and Antimony free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
- 4. For packaging details, go to our website at http://www.diodes.com

Marking Information



2063 = Product Type Marking Code





Maximum Ratings - Q1 (NPN Transistor) (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	130	V
Collector-Emitter Voltage	V _{CEO}	40	V
Emitter-Collector Voltage (Reverse blocking)	V _{ECO}	6	V
Emitter-Base Voltage	V _{EBO}	7	V
Continuous Collector Current	Ic	3.5	Α
Peak Pulsed Collector Current	I _{CM}	9	Α
Base Current	I _B	1	Α

Maximum Ratings - Q2 (PNP Transistor) (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	-45	V
Collector-Emitter Voltage	V _{CEO}	-40	V
Emitter-Collector Voltage (Reverse blocking)	V _{ECO}	-3	V
Emitter-Base Voltage	V _{EBO}	-7	V
Continuous Collector Current	Ic	-3	Α
Peak Pulsed Collector Current	I _{CM}	-9	A
Base Current	I _B	-1	Α

Thermal Characteristics (@TA = +25°C, unless otherwise specified.)

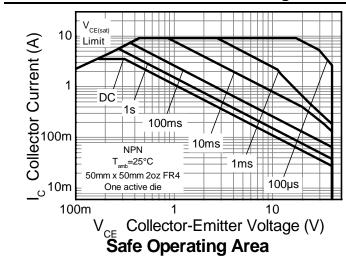
Characteristic		Symbol	Value	Unit	
	(Notes 5 & 9)		0.7 5.6		
	(Notes 6 & 9)	P _D	0.9 7.2	W mW/°C	
Power Dissipation Linear Derating Factor	(Notes 6 & 10)		1.1 8.8		
-	(Notes 7 & 9)		1.1 8.8		
	(Notes 8 & 9)]	1.7 13.6		
	(Notes 5 & 9)		179		
	(Notes 6 & 9)	$R_{ hetaJA}$	139	2004	
Thermal Resistance, Junction to Ambient	(Notes 6 & 10)		113		
	(Notes 7 & 9)		113	°C/W	
	(Notes 8 & 9)		73		
Thermal Resistance, Junction to Lead	(Note 11)	$R_{ heta JL}$	87.58		
Operating and Storage Temperature Range	T _J , T _{STG}	-55 to +150	°C		

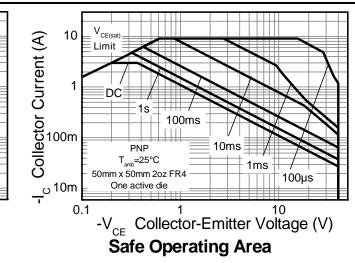
Notes:

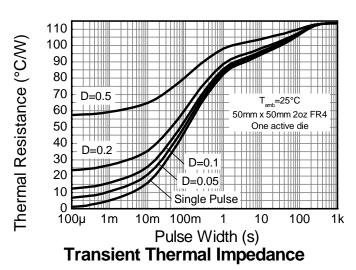
- 5. For a device surface mounted on 15mm x 15mm FR4 PCB with high coverage of single sided 1oz copper, in still air conditions; the device is measured when operating in a steady-state condition.
- 6. Same as note (5), except the device is surface mounted on 25mm x 25mm 1oz copper.
- 7. Same as note (5), except the device is surface mounted on 50mm x 50mm 2oz copper.
- 8. Same as note (7), except the device is measured at t < 5 seconds.
- 9. For device with one active die, both collectors attached to a common heatsink.
- 10. For device with two active dice running at equal power, split heatsink 50% to each collector.
- 11. Thermal resistance from junction to solder-point (at the end of the collector lead).

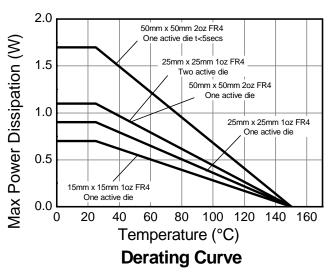


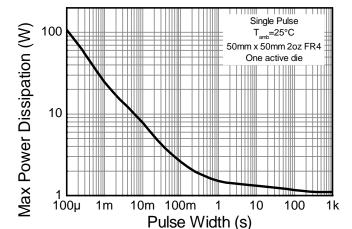
Thermal Characteristics and Derating Information











Pulse Power Dissipation



Electrical Characteristics – Q1 (NPN Transistor) (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV _{CBO}	130	170	_	V	$I_C = 100 \mu A, I_E = 0$
Collector-Emitter Breakdown Voltage (Note 12)	BV _{CEO}	40	63	_	V	$I_C = 10 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	BV _{EBO}	7	8.3	_	V	$I_E = 100 \mu A, I_C = 0$
Emitter-collector breakdown voltage (reverse blocking)	BV _{ECX}	6	7.4	_	V	I_E =100μA, R_{BC} < 1k Ω or 0.25V > V_{BC} > -0.25V
Emitter-collector breakdown voltage (base open)	BV _{ECO}	6	7.4	_	V	$I_E = 100 \mu A$
Collector Cutoff Current	I _{CBO}		<1	50 20	nA μA	V _{CB} = 100V V _{CB} = 100V, T _A = +100°C
Collector Cutoff Current	I _{EBO}	_	<1	50	nA	V _{EB} = 5.6V
ON CHARACTERISTICS (Note 12)						
DC Current Gain	h _{FE}	300 280 40	450 400 60	900	_	$I_C = 10 \text{mA}, V_{CE} = 2 \text{V}$ $I_C = 1.0 \text{A}, V_{CE} = 2 \text{V}$ $I_C = 3.5 \text{A}, V_{CE} = 2 \text{V}$
Collector-Emitter Saturation Voltage	V _{CE(sat)}	_	50 85 150 135	60 110 220 195	mV	$\begin{split} I_{C} &= 1.0\text{A}, \ I_{B} = 100\text{mA} \\ I_{C} &= 1.0\text{A}, \ I_{B} = 20\text{mA} \\ I_{C} &= 2.0\text{A}, \ I_{B} = 40\text{mA} \\ I_{C} &= 3.5\text{A}, \ I_{B} = 350\text{mA} \end{split}$
Base-Emitter Saturation Voltage	V _{BE(sat)}	_	960	1050	mV	$I_C = 3.5A$, $I_B = 350mA$
Base-Emitter Turn-On Voltage	V _{BE(on)}		860	950	mV	$I_C = 3.5A, V_{CE} = 2V$
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	C _{obo}	_	12	20	pF	V _{CB} = 10V, f = 1.0MHz
Current Gain-Bandwidth Product	f⊤	1	190	_	MHz	V _{CE} = 10V, I _C = 50mA, f = 100MHz
Delay Time	t _d		64	_	ns	
Rise Time	t _r		108	_	ns	Vcc = 10V. lc = 1A. l _{B1} = l _{B2} = 10mA
Storage Time	ts		428	_	ns	VCC = 10V, $IC = 1A$, $IB1 = IB2 = 10MA$
Fall Time	t _f		130	_	ns	

Notes: 12. Measured under pulsed conditions. Pulse width \leq 300 μ s. Duty cycle \leq 2%.



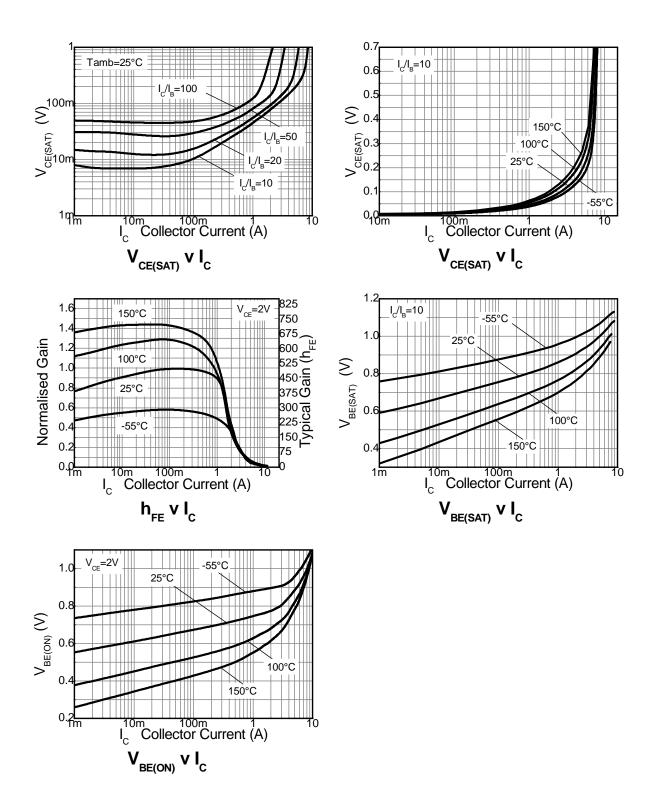
Electrical Characteristics – Q2 (PNP Transistor) (@TA = +25°C, unless otherwise specified.)

Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS						
Collector-Base Breakdown Voltage	BV _{CBO}	-45	-80	_	V	$I_C = -100 \mu A$, $I_E = 0$
Collector-Emitter Breakdown Voltage (Note 12)	BV _{CEO}	-40	-65	_	V	$I_C = -10 \text{mA}, I_B = 0$
Emitter-Base Breakdown Voltage	BV _{EBO}	-7	-8.3	_	V	$I_E = -100 \mu A, I_C = 0$
Emitter-collector breakdown voltage (reverse blocking)	BV _{ECX}	-6	-7.4		V	$-I_E = 100\mu A$, $R_{BC} < 1kΩ$ or $0.25V < V_{BC} < -0.25V$
Emitter-collector breakdown voltage (base open)	BV _{ECO}	-3	-8.7	_	V	I _E = -100μA
Collector Cutoff Current	I _{CBO}	_	<1	-50 -20	nA μA	V _{CB} = -36V V _{CB} = -36V, T _A = +100°C
Collector Cutoff Current	I _{EBO}	_	<1	-50	nA	V _{EB} = -5.6V
ON CHARACTERISTICS (Note 12)	•					
DC Current Gain	h _{FE}	300 200 20	450 280 50	900 — —	_	I _C = -10mA, V _{CE} = -2V I _C = -1.0A, V _{CE} = -2V I _C = -3.0A, V _{CE} = -2V
Collector-Emitter Saturation Voltage	V _{CE(sat)}		-70 -195 -175	-90 -290 -260	mV	I _C = -1.0A, I _B = -100mA I _C = -1.0A, I _B = -20mA I _C = -3.0A, I _B = -300mA
Base-Emitter Saturation Voltage	V _{BE(sat)}	_	-935	-1000	mV	$I_C = -3.0A$, $I_B = -300mA$
Base-Emitter Turn-On Voltage	V _{BE(on)}	_	-855	-950	mV	I _C = -3.0A, V _{CE} = -2V
SMALL SIGNAL CHARACTERISTICS						
Output Capacitance	C _{obo}	_	17	25	pF	V _{CB} = -10V, f = 1.0MHz
Current Gain-Bandwidth Product	f _T	_	270	_	MHz	$V_{CE} = -10V$, $I_{C} = -50mA$, $f = 100MHz$
Delay Time	t _d	_	57		ns	
Rise Time	t _r		69		ns	V 10V I 10 I I 10mA
Storage Time	ts	_	154	_	ns	$V_{CC} = -10V$, $I_{C} = -1A$, $I_{B1} = I_{B2} = -10mA$
Fall Time	t _f	_	60	_	ns	

Notes: 12. Measured under pulsed conditions. Pulse width \leq 300 μ s. Duty cycle \leq 2%.

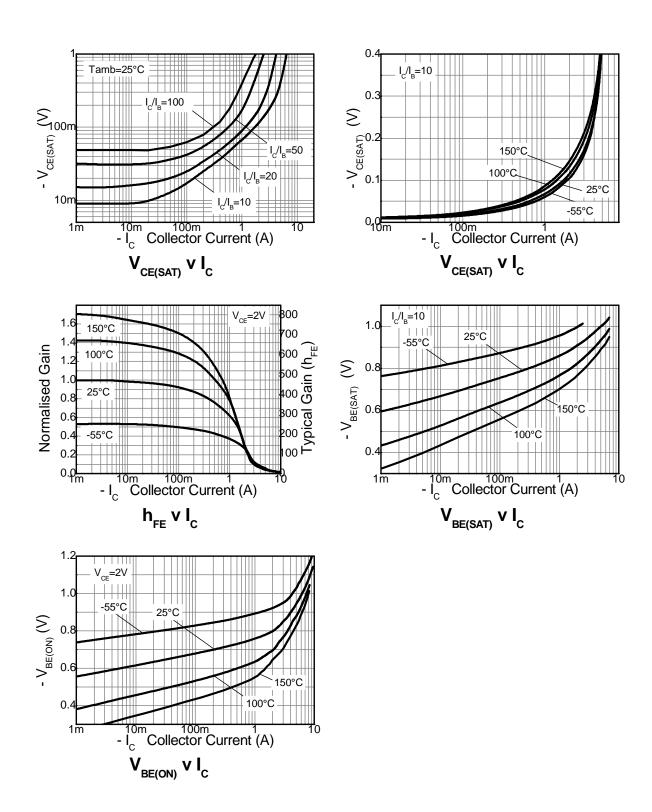


Typical Electrical Characteristics – Q1 (NPN Transistor) (@T_A = +25°C, unless otherwise specified.)





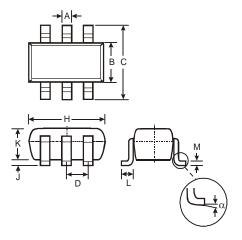
Typical Electrical Characteristics – Q2 (PNP Transistor) (@T_A = +25°C, unless otherwise specified.)





Package Outline Dimensions

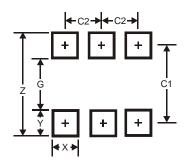
Please see AP02002 at http://www.diodes.com/datasheets/ap02002.pdf for latest version.



	SOT26						
Dim	Min	Max	Тур				
Α	0.35	0.50	0.38				
В	1.50	1.70	1.60				
С	2.70	3.00	2.80				
D	_		0.95				
Н	2.90	3.10	3.00				
J	0.013	0.10	0.05				
K	1.00	1.30	1.10				
L	0.35	0.55	0.40				
M	0.10	0.20	0.15				
α	0°	8°	ı				
AII D	All Dimensions in mm						

Suggested Pad Layout

Please see AP02001 at http://www.diodes.com/datasheets/ap02001.pdf for the latest version.



Dimensions	Value (in mm)
Z	3.20
G	1.60
Х	0.55
Y	0.80
C1	2.40
C2	0.95





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