LinearDimensions
SEMICONDUCTOR

The LND-PKD310

PEAK SAMPLE & HOLD WITH OFFSET + AMPLIFIER

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What is the LND-PKD310?
The LND-PKD310 is a versatile peak sample and hold IC with digitally programmable gain (1-16x), two DC level shifters, and very accurate peak detect circuit. All functions of this IC are controlled by microcontroller; this product is considered a microcontroller peripheral device.

How does it work?
·   Connect Vcc and Gnd, connect the clock signal.
·   Reset the device by holding M and D low for at least 64 clock cycles.
         This will clear all registers.
·   Load level shift registers A and B, and set the programmable gain on
         the opamp.
·   Connect input signal to In.
·   Initiate sample and hold with microcontroller, the resulting peak value
         will be on Out.
·   Ensure that the sample time is long enough to ensure that a full
   waveform passes through the device.

How Do I get samples?
The LND-PKD310 is available in 28 pin ceramic package. For volume orders, Linear Dimensions can package this device is industry standard packaging including a SOIC-28. Please contact your Linear Dimensions representative for more information or www.lineardimensions.com.

Features

- Accurate Peak Detect
- Programmable DC Gain
- Two DC Level Shifters
- All Functions Digitally
- Controlled Inclusing
- Clocking of Sample & Hold

Applications

- Sensor Arrays
- Security Monitors
- Input Devices
- EmbeddedControllers
- Microncontroller Peripheral
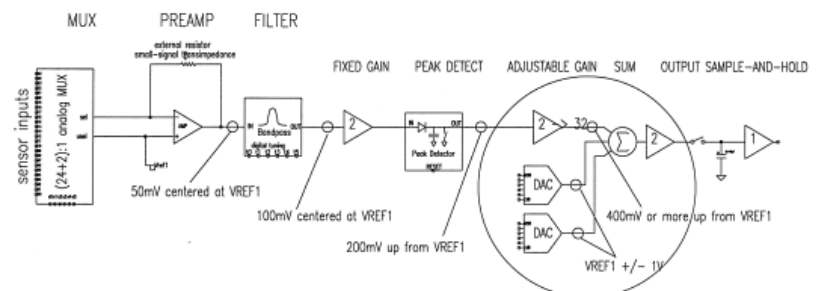
There are 5 internal registers to control MUX address, filter tuning, signal gain, and DAC settings.