TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SZ07AFS

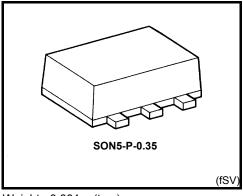
NON-Inverter (Open Drain)

Features

- High output current: 24 mA (min) at V_{CC} = 3 V
- Super high speed operation: t_{pZL} = 2.3 ns (typ.)

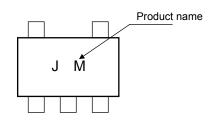
at $V_{CC} = 5 \text{ V}$, 50 pF

- Operation voltage range: V_{CC (opr.)} = 1.65 to 5.5 V
- 5.5-V tolerant input
- 5.5-V power down protection output

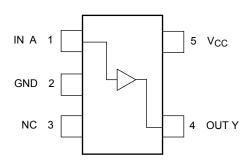


Weight: 0.001 g (typ.)

Marking



Pin Assignment (top view)



Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	−0.5 to 6	V
DC input voltage	V _{IN}	−0.5 to 6	٧
DC output voltage	V _{OUT}	-0.5 to 6 (Note 1)	٧
Input diode current	I _{IK}	-20	mA
Output diode current	lok	–20 (Note 2)	mA
DC output current	lout	50	mA
DC V _{CC} /ground current	Icc	±50	mA
Power dissipation	PD	50	mW
Storage temperature	T _{stg}	-65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

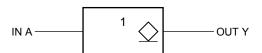
Note 1: Do not exceed I_{OUT} of absolute maximum ratings.

Note 2: VOUT < GND

Start of commercial production 2008-03



IEC Logic Symbol



Truth Table

Α	Y
L	L
Н	Z

Z: High Impedance

Operating Ranges

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	1.65 to 5.5	V
Supply voltage		1.5 to 5.5 (Note 3)	V
Input voltage	V _{IN}	0 to 5.5	٧
Output voltage	V _{OUT}	0 to 5.5	٧
Operating temperature	T _{opr}	-40 to 85	°C
	dt/dv	0 to 0 (V _{CC} = 1.80 V \pm 0.15 V, 2.5 V \pm 0.2 V)	
Input rise and fall time		0 to 10 (V _{CC} = 3.3 V \pm 0.3 V)	ns/V
		0 to 5 ($V_{CC} = 5.0 \text{ V} \pm 0.5 \text{ V}$)	

Note 3: Data retention only

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Electrical Characteristics

DC Characteristics

Characteristics Symbol Test Condition		Symbol Toot Condition		Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
		V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic			
Input voltage Low level	V _{IH}			1.65 to 1.95	V _{CC} × 0.75		_	V _{CC} × 0.75	١		
	TiigiTievei	VIН	_		2.3 to 5.5	V _{CC} × 0.7		_	V _{CC} × 0.7		V
	I ow level	.,			1.65 to 1.95			V _{CC} × 0.25	_	V _{CC} × 0.25	
	V _{IL}	_		2.3 to 5.5			V _{CC} × 0.3	_	V _{CC} × 0.3		
Z-state output leak	age current	ILKG	VIN = VIH VOUT = 0 to 5.5 V		1.65 to 5.5	_	_	±5	_	±10	μА
			V _{IN} = V _{IL}	I _{OL} = 100 μA	1.65	_	0	0.1	_	0.1	V
					2.3		0	0.1	_	0.1	
		Low level V _{OL}			3.0	_	0	0.1	_	0.1	
					4.5		0	0.1	_	0.1	
Output voltage Low level	Low level			$I_{OL} = 4 \text{ mA}$	1.65		0.08	0.24	_	0.24	
				$I_{OL} = 8 \text{ mA}$	2.3		0.1	0.3	_	0.3	
				I _{OL} = 16 mA	3.0	_	0.15	0.4	_	0.4	
				I _{OL} = 24 mA	3.0	_	0.22	0.55	_	0.55	
				$I_{OL} = 32 \text{ mA}$	4.5	_	0.22	0.55	_	0.55	
Input leakage current I_{IN} $V_{IN} = 5.5 \text{ V or GND}$		0 to 5.5	_	_	±1	_	±10	μА			
Power off leakage current I_{OFF} V_{IN} or $V_{OUT} = 5.5 \text{ V}$		0.0	_	_	1	_	10	μА			
Quiescent supply current I_{CC} $V_{IN} = V_{CC}$ o		or GND	5.5	_	_	2	_	20	μА		

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AC Characteristics (unless otherwise specified, Input: $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Condition		Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristics	Symbol	rest Condition	V _{CC} (V)	Min	Тур.	Max	Min	Max	Offic
	^t pZL	C_L = 50 pF, R_L = 500 Ω	1.80 ± 0.15	1.8	5.5	9.5	1.8	10.5	- ns
			2.5 ± 0.2	1.2	3.7	5.8	1.2	6.4	
			3.3 ± 0.3	0.8	2.9	4.4	0.8	4.8	
Propagation delay time			5.0 ± 0.5	0.5	2.3	3.5	0.5	3.9	
	^t pLZ	C_L = 50 pF, R_L = 500 Ω	1.80 ± 0.15	1.8	4.3	9.5	1.8	10.5	
			2.5 ± 0.2	1.2	2.8	5.8	1.2	6.4	
			3.3 ± 0.3	0.8	2.1	4.4	0.8	4.8	
			5.0 ± 0.5	0.5	1.4	3.5	0.5	3.9	
Input capacitance	C _{IN}		0 to 5.5		4		_	_	pF
Output capacitance	C _{OUT}		0 to 5.5		4		_	_	pF
Power dissipation capacitance	C	(Note 4)	3.3		4	_	_	_	pF
	C _{PD}	(140te 4)	5.5		10		_	_	þί

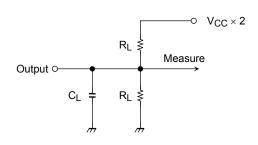
Note4: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

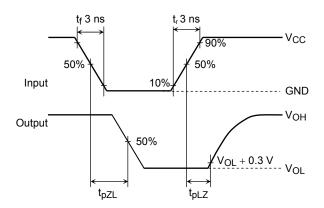
Average operating current can be obtained by the equation.

$$I_{CC (opr.)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

AC Characteristics Measurement Circuit

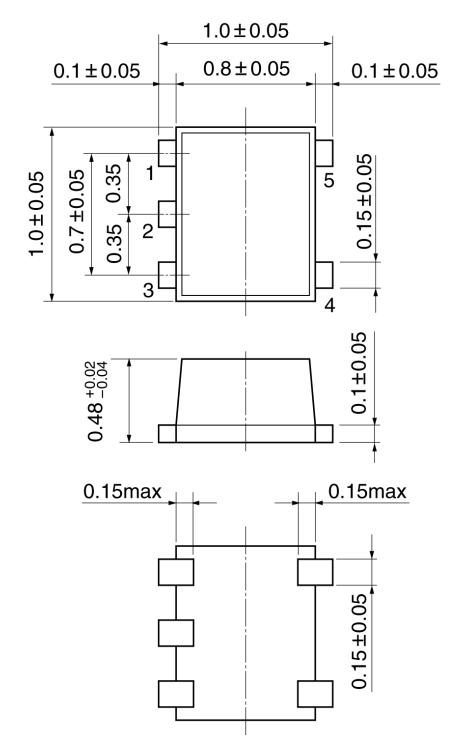
AC Waveforms





Package Dimensions

SON5-P-0.35 Unit: mm



Weight: 0.001 g (typ.)

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