PTC thermistors for overcurrent protection in telecom applications

Telecom Pair Protector (TPP)
for GR1089 Central Office, SMD

Series/Type: **B59970T1100A062**
Date: November 2009

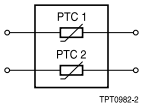
Telecom pair protector (TPP) for GR1089 Central Office T1970

SMD

Applications

- Overcurrent protection for telecom applications
- Suitable for line card applications e.g. POTS, access networks, customer premises equipment (CPE) or integrated voice data (IVD)

Internal circuit



Features

- Two resistance-matched PTCs in a plastic housing
- Compliant with GR1089, edition 3 Central Office requirements
- Compliant with GR1089, edition 3 (telecommunication ports)
  - Lightning surge, level 1 and 2
  - Lightning surge intra building
  - AC power fault, level 1 and 2
  - AC power fault, customer premises and intra building
- Compliant with ITU-T standards
  - basic-level lightning surges (10/700 µs)
  - basic-level power induction (600 V, 1 A, 0.2 s)
  - power contact criteria A/B (230 V, 15 min.)
- Suitable for continuous connection to mains voltages of 110/230 V AC in tripped (high-ohmic) condition
- Housing material according to UL94-V0
- UL approval to UL 1434 (file number E69802)
- Tight resistance matching maintained after switching
- Negligible resistance drift after reflow soldering or switching
- Marked with manufacturer's logo, type designation and date code
- RoHS-compatible

Options

- Alternative tolerances and resistances on request

Delivery mode

- Blister tape, 380-mm reel with 24-mm tape, taping to IEC 60286-3

General technical data

Maximum fault voltage		$V_{F,max}$	600	V AC
Max. operating voltage		V_{max}	245	V AC
Rated voltage		V_R	230	V AC
Operating temperature range	(V = 0)	T_{op}	−40/+125	°C
Operating temperature range	($V = V_{max}$)	T_{op}	−5/+75	°C
Resistance matching in one housing		$\lvert R_2 - R_1 \rvert$	2.0	Ω

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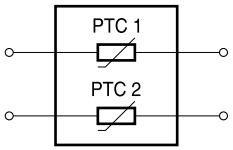
Electrical specifications and ordering code

Type	R_R	ΔR_R	I_R @ 25 °C	I_R @ 70 °C	I_S @ 25 °C	I_S @ 70 °C	Ordering code
	Ω	%	mA	mA	mA	mA	
T1970	70	±15	70	40	150	85	B59970T1100A062

Switching times and ordering code

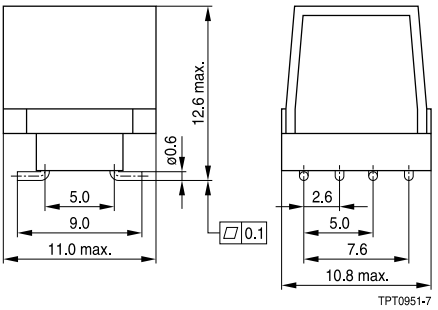
Type	t_S @ 0.15 A, 230 V AC	t_S @ 1 A, 230 V AC	t_S @ 2.5 A, 230 V AC	Ordering code
	s	s	s	
T1970	< 100	< 1	< 0.15	B59970T1100A062

Internal circuit

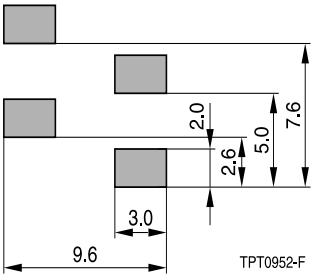


Dimensional drawings

T1970



Solder pad



Dimensions in mm

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Reliability data

Test	Standard	Test conditions	$\lvert\Delta R_{25}/R_{25}\rvert$
Electrical endurance, cycling	IEC 60738-1	Room temperature, I_{Smax}; V_{max} Number of cycles: 10	< 20%
Electrical endurance, constant	IEC 60738-1	Storage at V_{max} /$T_{op,max}$ (V_{max}) Test duration: 1000 h	< 25%
Damp heat	IEC 60738-1	Temperature of air: 40 °C Relative humidity of air: 93% Duration: 56 days Test according to IEC 60068-2-78	< 10%
Rapid change of temperature	IEC 60738-1	$T_1 = T_{op,min}$ (0 V), $T_2 = T_{op,max}$ (0 V) Number of cycles: 5 Test duration: 30 min Test according to IEC 60068-2-14, Test Na	< 10%
Vibration	IEC 60738-1	Frequency range: 10 to 55 Hz Displacement amplitude: 0.75 mm Test duration: 3 × 2 h Test according to IEC 60068-2-6, Test Fc	< 5%
Shock	IEC 60738-1	Acceleration: 390 m/s^2 Pulse duration: 6 ms; 6 × 4000 pulses	< 5%
Climatic sequence	IEC 60738-1	Dry heat: $T = T_{op,max}$ (0 V) Test duration: 16 h Damp heat first cycle Cold: $T = T_{op,min}$ (0 V) Test duration: 2 h Damp heat 5 cycles Tests performed according to IEC 60068-2-30	< 10%

Cautions and warnings

General

- EPCOS thermistors are designed for specific applications and should not be used for purposes not identified in our specifications, application notes and data books unless otherwise agreed with EPCOS during the design-in-phase.
- Ensure suitability of thermistor through reliability testing during the design-in phase. The thermistors should be evaluated taking into consideration worst-case conditions.

Storage

- Store thermistors only in original packaging. Do not open the package before storage.
- Storage conditions in original packaging: storage temperature −25 °C ... +45 °C, relative humidity ≤75% annual mean, maximum 95%, dew precipitation is inadmissible.
- Avoid contamination of thermistors surface during storage, handling and processing.
- Avoid storage of thermistor in harmful environment with effect on function on long-term operation (examples given under operation precautions).
- Use thermistor within the following period after delivery:
  - Through-hole devices (housed and leaded PTCs): 24 months
  - Motor protection sensors, glass-encapsulated sensors and probe assemblies: 24 months
  - Telecom pair and quattro protectors (TPP, TQP): 24 months
  - Leadless PTC thermistors for pressure contacting: 12 months
  - Leadless PTC thermistors for soldering: 6 months
  - SMDs in EIA sizes 3225 and 4032, and for PTCs with metal tags: 24 months
  - SMDs in EIA sizes 0402, 0603, 0805 and 1210: 12 months

Handling

- PTCs must not be dropped. Chip-offs must not be caused during handling of PTCs.
- Components must not be touched with bare hands. Gloves are recommended.
- Avoid contamination of thermistor surface during handling.

Soldering (where applicable)

- Use rosin-type flux or non-activated flux.
- Insufficient preheating may cause ceramic cracks.
- Rapid cooling by dipping in solvent is not recommended.
- Complete removal of flux is recommended.
- Standard PTC heaters are not suitable for soldering.

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Mounting

- Electrode must not be scratched before/during/after the mounting process.
- Contacts and housing used for assembly with thermistor have to be clean before mounting. Especially grease or oil must be removed.
- When PTC thermistors are encapsulated with sealing material, the precautions given in chapter "Mounting instructions", "Sealing and potting" must be observed.
- When the thermistor is mounted, there must not be any foreign body between the electrode of the thermistor and the clamping contact.
- The minimum force of the clamping contacts pressing against the PTC must be 10 N.
- During operation, the thermistor's surface temperature can be very high. Ensure that adjacent components are placed at a sufficient distance from the thermistor to allow for proper cooling at the thermistors.
- Ensure that adjacent materials are designed for operation at temperatures comparable to the surface temperature of thermistor. Be sure that surrounding parts and materials can withstand this temperature.
- Avoid contamination of thermistor surface during processing.

Operation

- Use thermistors only within the specified temperature operating range.
- Use thermistors only within the specified voltage and current ranges.
- Environmental conditions must not harm the thermistors. Use thermistors only in normal atmospheric conditions. Avoid use in deoxidizing gases (chlorine gas, hydrogen sulfide gas, ammonia gas, sulfuric acid gas etc), corrosive agents, humid or salty conditions. Contact with any liquids and solvents should be prevented.
- Be sure to provide an appropriate fail-safe function to prevent secondary product damage caused by abnormal function (e.g. use VDR for limitation of overvoltage condition).

Symbols and terms

A	Area
C_{th}	Heat capacity
f	Frequency
I	Current
I_{max}	Maximum current
I_R	Rated current
I_{PTC}	PTC current
I_r	Residual currrent
$I_{r,oil}$	Residual currrent in oil (for level sensors)
$I_{r,air}$	Residual currrent in air (for level sensors)
I_{RMS}	Root-mean-square value of current
I_S	Switching current
I_{Smax}	Maximum switching current
LCT	Lower category temperature
N	Number (integer)
N_c	Operating cycles at V_{max}, charging of capacitor
N_f	Switching cycles at V_{max}, failure mode
P	Power
P_{25}	Maximum power at 25 °C
P_{el}	Electrical power
P_{diss}	Dissipation power
R_{min}	Minimum resistance
R_R	Rated resistance
ΔR_R	Tolerance of R_R
R_P	Parallel resistance
R_{PTC}	PTC resistance
R_{ref}	Reference resistance
R_S	Series resistance
R_{25}	Resistance at 25 °C
$R_{25,match}$	Resistance matching per reel/ packing unit at 25 °C
ΔR_{25}	Tolerance of R_{25}
T	Temperature
t	Time
T_A	Ambient temperature
t_a	Thermal threshold time
T_C	Ferroelectric Curie temperature

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t_E	Settling time (for level sensors)
T_R	Rated temperature
T_{sense}	Sensing temperature
T_{op}	Operating temperature
T_{PTC}	PTC temperature
t_R	Response time
T_{ref}	Reference temperature
T_{Rmin}	Temperature at minimum resistance
t_S	Switching time
T_{surf}	Surface temperature
UCT	Upper category temperature
V or V_{el}	Voltage (with subscript only for distinction from volume)
V_{RMS}	Root-mean-square value of voltage
V_{BD}	Breakdown voltage
V_{ins}	Insulation test voltage
$V_{link,max}$	Maximum link voltage
V_{max}	Maximum operating voltage
$V_{max,dyn}$	Maximum dynamic (short-time) operating voltage
V_{meas}	Measuring voltage
$V_{meas,max}$	Maximum measuring voltage
V_R	Rated voltage
V_{PTC}	Voltage drop across a PTC thermistor
α	Temperature coefficient
Δ	Tolerance, change
δ_{th}	Dissipation factor
τ_{th}	Thermal cooling time constant
λ	Failure rate
e	Lead spacing (in mm)

Abbreviations / Notes

SMD Surface-mount devices

* To be replaced by a number in ordering codes, type designations etc.

+ To be replaced by a letter

All dimensions are given in mm.

The commas used in numerical values denote decimal points.

Important notes

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