CMLT5551

SURFACE MOUNT SILICON DUAL, HIGH VOLTAGE NPN TRANSISTOR



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DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMLT5551 consists of two individual, isolated NPN silicon transistors manufactured by the epitaxial planar process and epoxy molded in an SOT-563 suface mount package. This device has been designed for high voltage amplifier applications.

MARKING CODE: 5C5



MAXIMUM RATINGS: (T _A =25°C)	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	180	V
Collector-Emitter Voltage	V _{CEO}	160	V
Emitter-Base Voltage	V_{EBO}	6.0	V
Continuous Collector Current	l _C	600	mA
Power Dissipation	P_{D}	350	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	Θ_{JA}	357	°C/W
ELECTRICAL CHARACTERISTICS PER TRAN	ISISTOR: (T _A =25°C	unless otherwise noted	d)
SYMBOL TEST CONDITIONS	MIN	MAX	UNITS

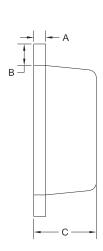
	5/A		
L CHARACTERISTICS PER TRANS TEST CONDITIONS	SISTOR: (T _A =25° MIN	C unless otherwise noted MAX	UNITS
V _{CB} =120V		50	nA
V _{CB} =120V, T _A =100°C		50	μΑ
I _C =100μA	180		V
I _C =1.0mA	160		V
I _E =10μA	6.0		V
I _C =10mA, I _B =1.0mA		0.15	V
I _C =50mA, I _B =5.0mA		0.20	V
I _C =10mA, I _B =1.0mA		1.00	V
I _C =50mA, I _B =5.0mA		1.00	V
V _{CE} =5.0V, I _C =1.0mA	80		
V_{CE} =5.0V, I_{C} =10mA	80	250	
V_{CE} =5.0V, I_{C} =50mA	30		
V_{CE} =10V, I_{C} =10mA, f=100MHz	100	300	MHz
V_{CB} =10V, I_E =0, f=1.0MHz		6.0	pF
V_{CE} =10V, I_{C} =1.0mA, f=1.0kHz	50	200	
V_{CE} =5.0V, I_{C} =200 μ A, R_{S} =10 Ω ,			
f=10Hz to 15.7kHz		8.0	dB
	TEST CONDITIONS $V_{CB}=120V$ $V_{CB}=120V, T_{A}=100^{\circ}C$ $I_{C}=100\mu A$ $I_{C}=1.0mA$ $I_{E}=10\mu A$ $I_{C}=10mA, I_{B}=1.0mA$ $I_{C}=50mA, I_{B}=5.0mA$ $I_{C}=50mA, I_{B}=5.0mA$ $I_{C}=50mA, I_{B}=5.0mA$ $V_{CE}=5.0V, I_{C}=1.0mA$ $V_{CE}=5.0V, I_{C}=10mA$ $V_{CE}=5.0V, I_{C}=10mA$ $V_{CE}=10V, I_{C}=10mA, f=100MHz$ $V_{CB}=10V, I_{C}=1.0mA, f=1.0kHz$ $V_{CE}=10V, I_{C}=1.0mA, f=1.0kHz$ $V_{CE}=5.0V, I_{C}=200\mu A, R_{S}=10\Omega,$	TEST CONDITIONS $V_{CB}=120V$ $V_{CB}=120V$, $V_{CB}=100V$, $V_{CB}=1.00V$, $V_{CB}=$	$\begin{array}{cccccccccccccccccccccccccccccccccccc$

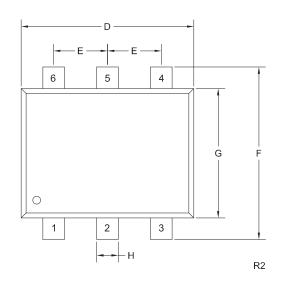
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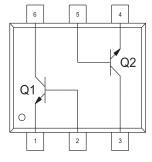


SOT-563 CASE - MECHANICAL OUTLINE





PIN CONFIGURATION



DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
Α	0.0027	0.007	0.07	0.18		
В	0.008		0.20			
С	0.017	0.024	0.45	0.60		
D	0.059	0.067	1.50	1.70		
E	0.020		0.50			
F	0.059	0.067	1.50	1.70		
G	0.043	0.051	1.10	1.30		
Н	0.006	0.012	0.15	0.30		
SOT-563 (REV: R2)						

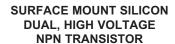
LEAD CODE:

- 1) Emitter Q1
- 2) Base Q1
- 3) Collector Q2
- 4) Emitter Q2
- 5) Base Q2
- 6) Collector Q1

MARKING CODE: 5C5

R3 (29-June 2015)

CMLT5551





SERVICES

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R3 (29-June 2015)