Silicon NPN Epitaxial

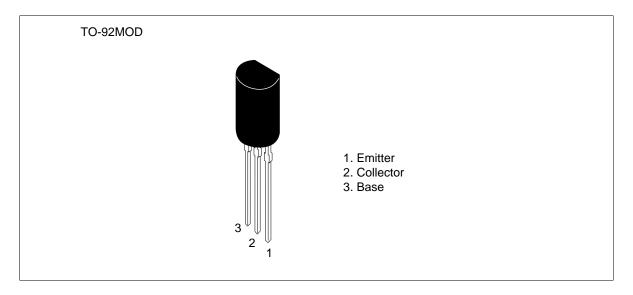
HITACHI

ADE-208-1141 (Z) 1st. Edition Mar. 2001

Application

- Power switching
- TV horizontal deflection output

Outline





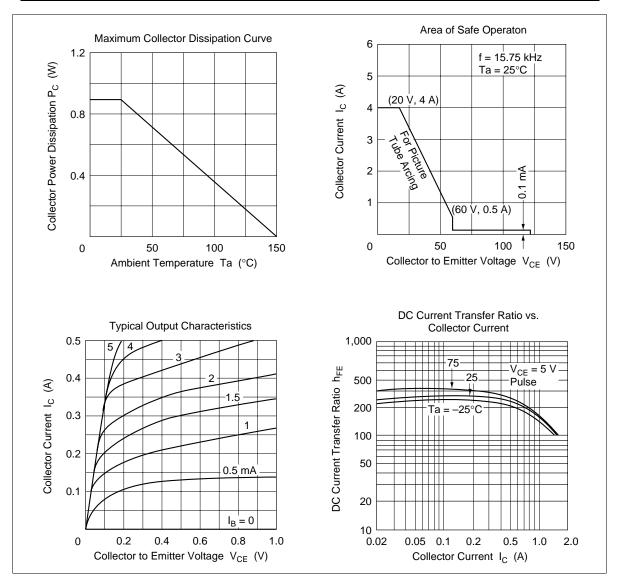
Absolute Maximum Ratings (Ta = 25° C)

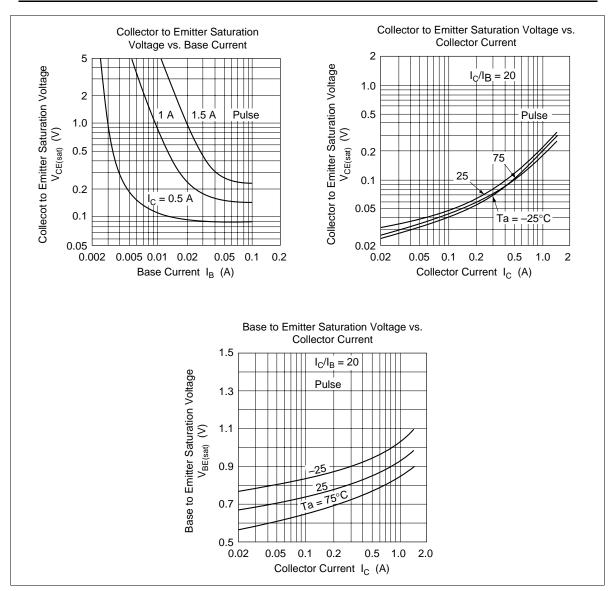
Item	Symbol	Ratings	Unit
Collector to base voltage	V _{CBO}	120	V
Collector to emitter voltage	V _{CEO}	60	V
Emitter to base voltage	V _{EBO}	5	V
Collector current	Ι _c	1	A
Collector peak current	i _{C(peak)}	1.5	A
Surge collector current	I _{C(surge)}	4	A
Collector power dissipation	Pc	0.9	W
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Electrical Characteristics (Ta = 25°C)

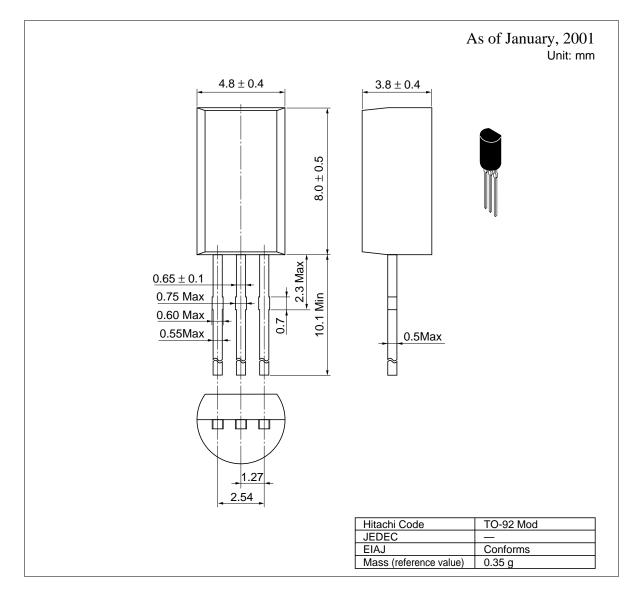
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{(BR)CBO}$	120	_	_	V	$I_{c} = 10 \ \mu A, \ I_{E} = 0$
Collector to emitter breakdown voltage	$V_{(BR)CEO}$	60	_	—	V	$I_c = 1 \text{ mA}, R_{BE} = \infty$
Emitter to base breakdown voltage	$V_{(BR)EBO}$	5	_	_	V	$I_{\rm E} = 10 \ \mu A, \ I_{\rm C} = 0$
Collector cutoff current	I _{CBO}		_	1.0	μΑ	$V_{CB} = 100 \text{ V}, \text{ I}_{E} = 0$
DC current transfer ratio	h _{FE}	150		_		$V_{ce} = 5 V, I_c = 1 A^{*1}$
Collector to emitter saturation voltage	$V_{\text{CE(sat)}}$	—	—	0.3	V	$I_{\rm C} = 1 \text{ A}, I_{\rm B} = 0.05 \text{ A}^{*1}$
Base to emitter saturation voltage	$V_{\text{BE(sat)}}$		—	1.2	MHz	_
Fall time	t _f		0.4		pF	$I_{CP} = 1 \text{ A}, I_{B1} = -I_{B2} = 50 \text{ mA}^{*1}$
Noto: 1 Dulas test						

Note: 1. Pulse test





Package Dimensions



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