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NCE6058K

NCE N-Channel Enhancement Mode Power MOSFET

Description

The NCE6058K uses advanced trench technology and design to provide excellent R_{DS(ON)} with low gate charge. It can be used in a wide variety of applications.

General Features

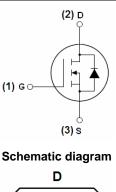
- V_{DS} =60V,I_D =58A $R_{DS(ON)}$ <16m Ω @ V_{GS} =10V (Typ:13m Ω)
- High density cell design for ultra low Rdson
- Fully characterized avalanche voltage and current
- Good stability and uniformity with high EAS
- Excellent package for good heat dissipation
- Special process technology for high ESD capability

Application

- Power switching application
- LED backlighting
- Uninterruptible power supply

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100% AVds TESTED!





Marking and pin assignment



TO-252-2L top view

Package Marking and Ordering Information

-						
	Device Marking	Device	Device Package	Reel Size	Tape width	Quantity
ſ	NCE6058K	NCE6058K	TO-252-2L	-	-	-

Absolute Maximum Ratings (T_C=25 ℃unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V _{DS}	60	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous	I _D	58	Α
Drain Current-Continuous(T _C =100 °C)	I _D (100℃)	41	Α
Pulsed Drain Current	I _{DM}	120	Α
Maximum Power Dissipation	P _D	85	W
Debating factor		0.57	W/℃
Single pulse avalanche energy (Note 5)	E _{AS}	290	mJ
Operating Junction and Storage Temperature Range	T_{J} , T_{STG}	-55 To 175	$^{\circ}$



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Thermal Characteristic

Thermal Resistance, Junction-to-Case ^(Note 2)	$R_{ heta JC}$	1.76	°C/W	
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Electrical Characteristics (T_C=25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Тур	Max	Unit
Off Characteristics						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V I _D =250μA	60	-	-	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =60V,V _{GS} =0V	-	-	1	μA
Gate-Body Leakage Current	I _{GSS}	V _{GS} =±20V,V _{DS} =0V	-	-	±100	nA
On Characteristics (Note 3)	<u>.</u>					
Gate Threshold Voltage	V _{GS(th)}	$V_{DS}=V_{GS}$, $I_{D}=250\mu A$	2	3	4	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =30A	-	13	16	mΩ
Forward Transconductance	g FS	V _{DS} =5V,I _D =30A	30	-	-	S
Dynamic Characteristics (Note4)			•			
Input Capacitance	C _{lss}		-	2498	-	PF
Output Capacitance	Coss	V_{DS} =25V, V_{GS} =0V,	-	185	-	PF
Reverse Transfer Capacitance	C _{rss}	F=1.0MHz	-	80	-	PF
Switching Characteristics (Note 4)			•			
Turn-on Delay Time	t _{d(on)}		-	12	-	nS
Turn-on Rise Time	t _r	V_{DD} =30V, I_D =2A, R_L =1 Ω	-	5.2	-	nS
Turn-Off Delay Time	$t_{d(off)}$	V_{GS} =10 V , R_{GEN} =3 Ω	-	38	-	nS
Turn-Off Fall Time	t _f		-	27	-	nS
Total Gate Charge	Qg	V 20VI 20A	-	36	-	nC
Gate-Source Charge	Q _{gs}	$V_{DS}=30V,I_{D}=30A,$ $V_{GS}=10V$	-	9.9	-	nC
Gate-Drain Charge	Q_{gd}	V _{GS} =10V	-	6.6	-	nC
Drain-Source Diode Characteristics	<u> </u>		•			
Diode Forward Voltage (Note 3)	V_{SD}	V _{GS} =0V,I _S =30A	-	-	1.2	V
Diode Forward Current (Note 2)	Is		-	-	58	Α
Reverse Recovery Time	t _{rr}	TJ = 25°C, IF =30A	-	35		nS
Reverse Recovery Charge	Qrr	di/dt = 100A/µs ^(Note3)	-	47		nC
Forward Turn-On Time	t _{on}	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LE			y LS+LD)	
		•				

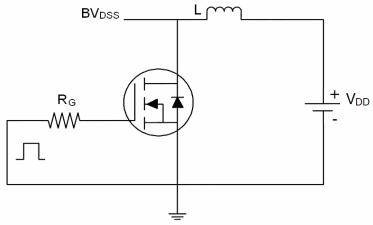
Notes:

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature.
- **2.** Surface Mounted on FR4 Board, $t \le 10$ sec.
- 3. Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2%.
- 4. Guaranteed by design, not subject to production
- **5.** E_{AS} condition: Tj=25 $^{\circ}\text{C}$,V_{DD}=30V,V_G=10V,L=0.5mH,Rg=25 Ω

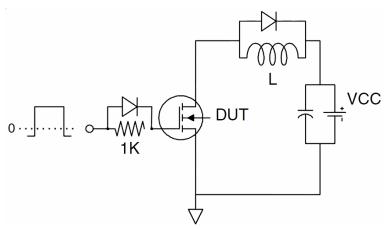


Test circuit

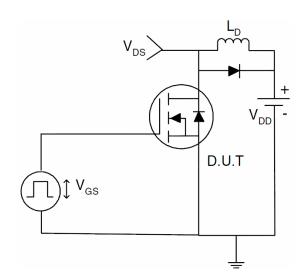
1) E_{AS} test Circuits



2) Gate charge test Circuit



3) Switch Time Test Circuit



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Typical Electrical and Thermal Characteristics (Curves)

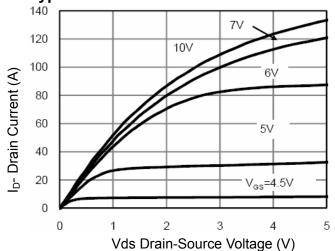


Figure 1 Output Characteristics

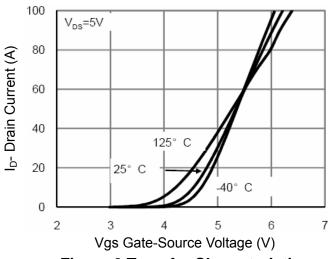


Figure 2 Transfer Characteristics

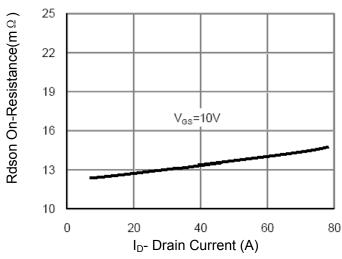


Figure 3 Rdson- Drain Current

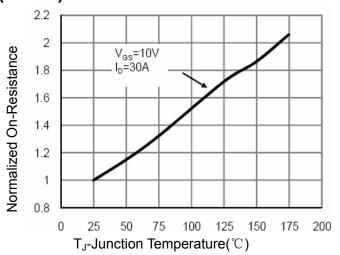


Figure 4 Rdson-JunctionTemperature

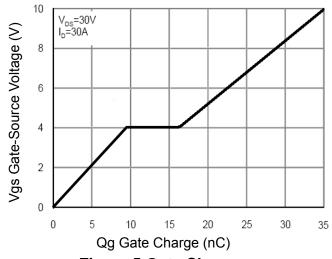


Figure 5 Gate Charge

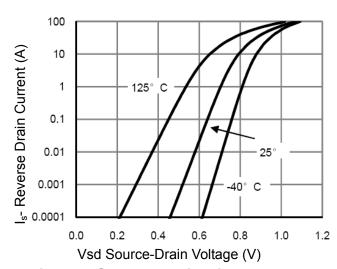
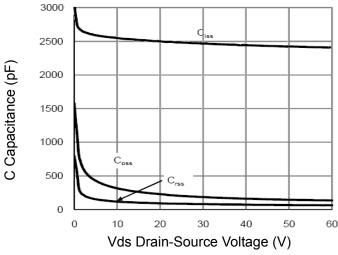


Figure 6 Source- Drain Diode Forward

120

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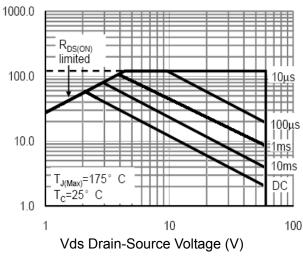




90 Power Dissipation (W) 60 30 0 0 25 75 100 125 150 175 T_J -Junction Temperature($^{\circ}$ C)

Figure 7 Capacitance vs Vds

Figure 9 Power De-rating



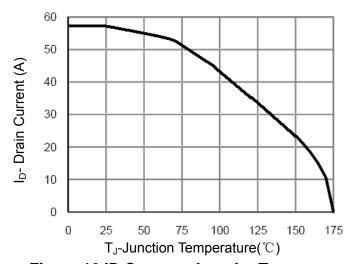


Figure 8 Safe Operation Area

Figure 10 ID Current- JunctionTemperature

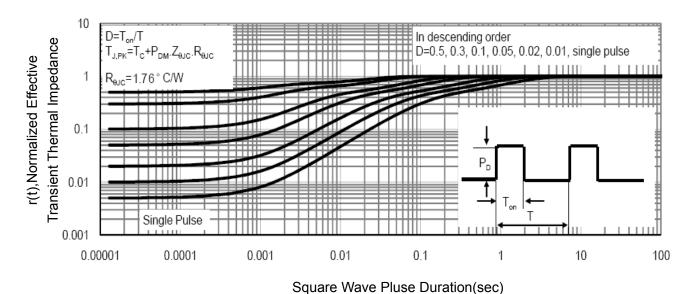


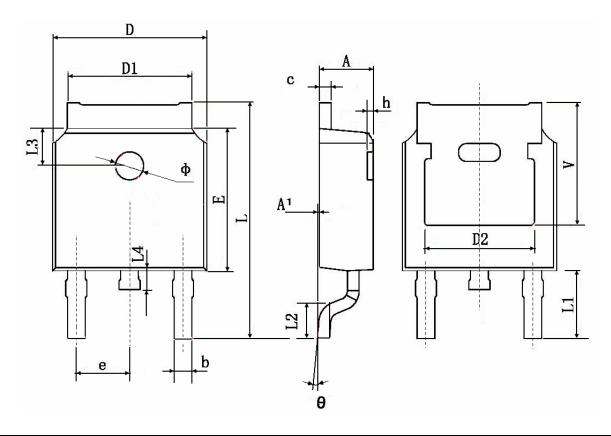
Figure 11 Normalized Maximum Transient Thermal Impedance

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TO-252 Package Information



Symbol	Dimensions	In Millimeters	Dimensions In Inches			
Symbol	Min.	Max.	Min.	Max.		
А	2.200	2.400	0.087	0.094		
A1	0.000	0.127	0.000	0.005		
b	0.660	0.860	0.026	0.034		
С	0.460	0.580	0.018	0.023		
D	6.500	6.700	0.256	0.264		
D1	5.100	5.460	0.201	0.215		
D2	4.830 TYP.		0.190 TYP.			
Е	6.000	6.200	0.236	0.244		
е	2.186	2.386	0.086	0.094		
L	9.800	10.400	0.386	0.409		
L1	2.900 TYP.		0.114	0.114 TYP.		
L2	1.400	1.700	0.055	0.067		
L3	1.600	1.600 TYP. 0.063 TYP.		1.600 TYP. 0.063 TY		TYP.
L4	0.600	1.000	0.024	0.039		
Ф	1.100	1.300	0.043	0.051		
θ	0°	8°	0°	8°		
h	0.000	0.300	0.000	0.012		
V	5.350	TYP.	0.211 TYP.			



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