

SANYO Semiconductors DATA SHEET

An ON Semiconductor Company

ExPD(Excellent Power Device) TND027MP— Lowside Power Switch Lamp-, Solenoid-, and Motor-Driving Applications

Features

- · N-channel MOSFET built in
- · Overheat protection. (Self recovery type)
- · Overcurrent protection. (Self recovery type current limiting function)
- · Overvoltage protection

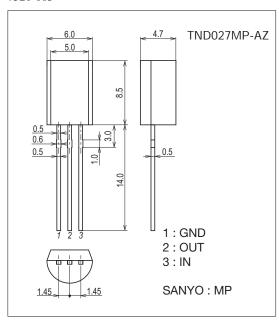
Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V _{DS}		60	V
Output Current	I _O (DC)		1.5	А
Input Voltage	VIN		-0.3 to +10	V
Allowable Power Dissipation	PD		1.0	W
Operating Supply Voltage	V _{DS} (opr)		40	V
Operating Temperature	Topr		-40 to +85	°C
Junction Temperature	Tj		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Package Dimensions

unit: mm (typ) 7520-003

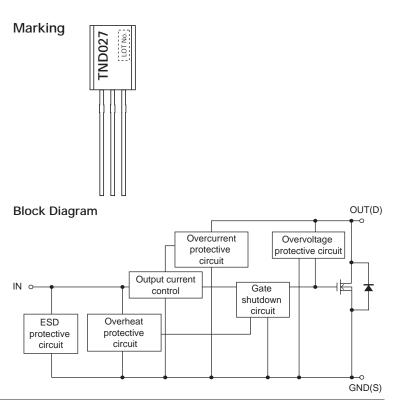


Product & Package Information

• Package

• JEITA, JEDEC : SC-51, TO-92(1-WATT), TO-226AE

• Minimum Packing Quantity: 1,000 pcs./box



SANYO Semiconductor Co., Ltd.

http://www.sanyosemi.com/en/network/

TND027MP

Electrical Characteristics at Ta=25°C

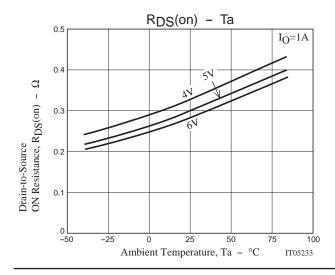
Parameter	Cymphol	Conditions		Unit			
Parameter	Symbol	Conditions	min	typ	max	Uill	
Drain-to-Source Clamp Voltage	V _{DS} , clamp	V _{IN} =0V, I _O =1mA	60			V	
Output-OFF Current	I _{DSS} (1)	V _{IN} =0V, V _{DS} =50V			10	μΑ	
Output-OFF Current	IDSS(2)	V _{IN} =0V, V _{DS} =12V			5	μΑ	
Input Threshold Voltage	V _{IN} (th)	V _{DS} =5V, I _O =1mA	1.0	1.5	2.0	V	
Protection Circuit Operating Input Voltage	VIN(opr)		4		10	V	
Drain-to-Source ON Resistance	R _{DS} (on)	V _{IN} =5V, I _O =1A		0.3	0.4	Ω	
Input Current (Output On)	IIN	V _{IN} =5V			0.6	mA	
Over-Heat Detecting Temperature	Tj(sd)	V _{IN} =5V, I _O =1A	120	150	190	°C	
Over-Current Detecting Current	Is	V _{IN} =5V	3.0	6.0	9.0	Α	
Over-Current Limit (Peak)	I _{LMT}	V _{IN} =5V	3.0	6.0	9.0	Α	
Input Clamp Voltage	V _{IN} , clamp	I _{IN} =1mA	10			V	

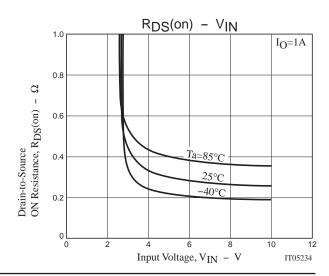
Notes: 1. Overcurrent protection circuit limits the output current to the range of overcurrent limit value.

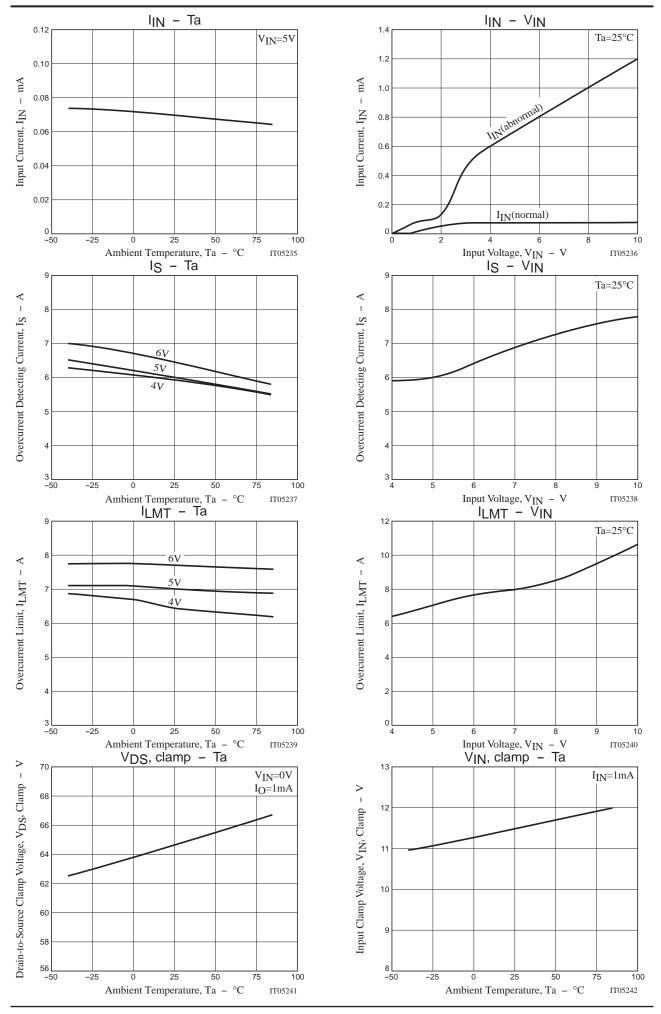
2. During overheat protecting operation, output current is turned off.

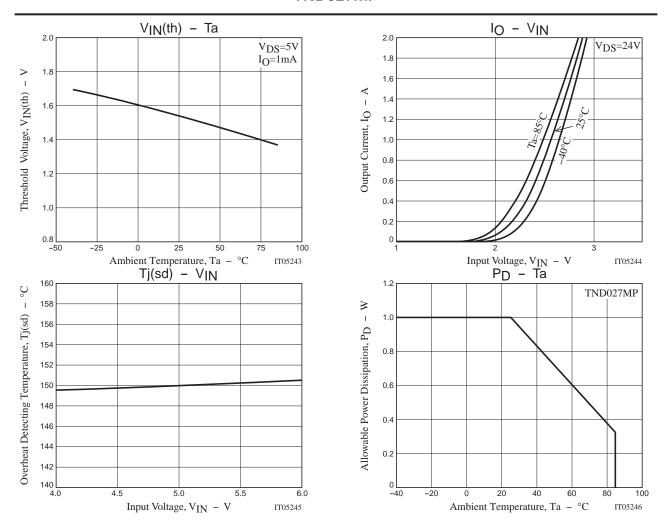
Ordering Information

Device Package		Shipping	memo	
TND027MP-AZ	IND027MP-AZ I MP I		Pb Free	

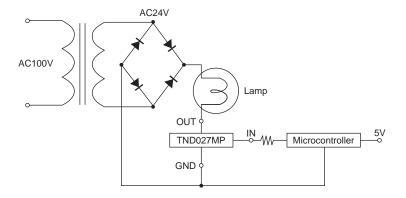




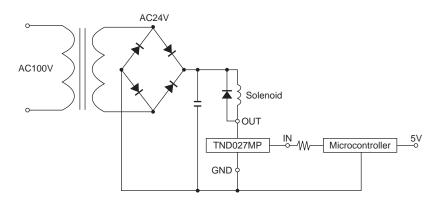




Sample Application Circuit



Another Sample Application Circuit (Solenoid drive)



TND027MP

Operation Description

- The output power MOSFET will be turned on when the input voltage exceeds the input threshold voltage (4 to 6V is recommended), and then the lamp will be turned on by the current flowing to the lamp. Conversely, the output power MOSFET will be turned off when the input voltage goes below the input threshold voltage, and the lamp will be turned off.
- The inrush current that occurs during normal lamp operation is limited to a preset value by the built-in overcurrent protecting circuit, which makes the lamp life longer.
- The internal overcurrent protection function limits the current of output power MOSFET when output current of at least the overcurrent detecting current value flows at load short. Besides, if the device temperature exceeds the allowable power dissipation, overheat protection function protects the power switch from being broken down by turning off the current of output power MOSFET when Tj comes to 150°C (typical).
- · As an example of application circuit, DC voltage can also be controlled as a solenoid drive.

Addition

- The diode between OUT and GND in the block diagram is parasitic diode of the MOSFET.
- Not apply a voltage on IN terminal during the period when OUT voltage is lower then GND voltage when driving a solenoid or a motor.
- Be sure connect a diode between OUT terminal and GND terminal when you want to apply a voltage on IN terminal under the above-stated state (that is, OUT Voltage < GND Voltage).

Taping Specification

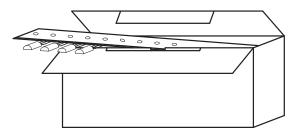
TND027MP-AZ

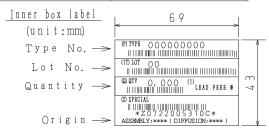
1. Packing Format

Packing Packing		Maximum Number of devices contained (pcs)		Packing format			
Lackage Mame	Туре	Inner BOX	number of contained	Outer Box (C-14)	Outer Box (C-15)		
AE/AZ		C-3 Dimensions:mm (external) 330×45×125	1,000	16inner Box contained (16,000pcs) Dimensions:mm (external) 500×345×195	Binner Box contained (8, 000pcs) Dimensions:mm (external) 3 4 5 × 2 6 0 × 1 9 5		
	A J	C-5 Dimensions:mm (external) 330×45×245	2, 000	8inner Box contained (16,000pcs) Dimensions:mm (external) 500×345×195	4inner Box contained (8,000pcs) Dimensions:mm (external) 345×260×195		

Packing method

Put zigzag folding in an inner box.



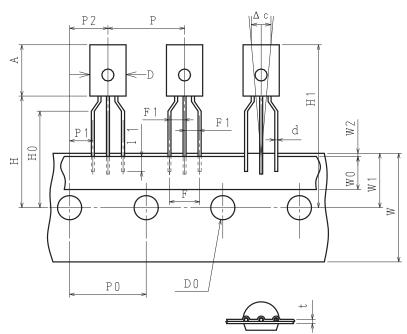


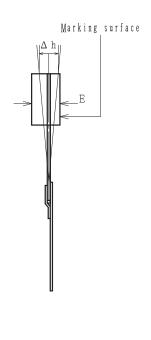
NOTE (1)
The LEAD FREE * description shows that the surface treatment of the terminal is lead free.

Label	JEITA Phase
LEAD FREE 3	JEITA Phase 3A
LEAD FREE 4	JEITA Phase 3

2. Taping specifications

2-1. Carrier tape size

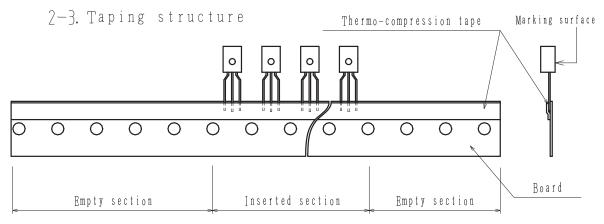




2-2. Taping size standard

H	1		mm

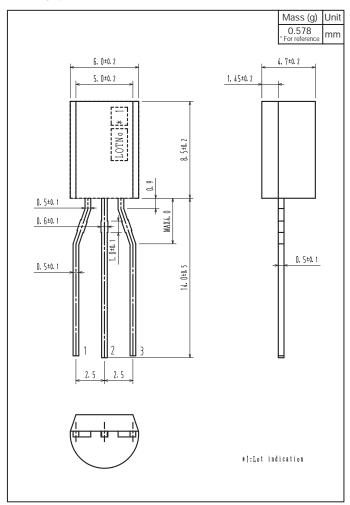
I t em	Symbol	Standard	Tolerance		I t em	Symbol	Standard	Tolerance
	D	6. 0	±0.2		Tape width	W	18. 0	+1.0 -0.5
Work piece outside diameter	E	4. 7	±0.2		Adhesive tape	WO	6. 0	±1.5
Work piece height	Α	8, 5	±0.2		Displacement of perforations	W 1	9. 0	±0.5
Lead wire diameter	d	0.5×0.5t	±0.1		Work piece bottom surface position	Н	18. 5	± 1. 0
Bonded lead wire	1 1	2. 5MIN			Insert stopper position	Н0	16.0	±0.5
Pitch between products	P	12. 7	± 1. 0		Work piece upper limit position	H 1	27. 0	± 1. 5
Pitch between perforations	P 0	12. 7	±0.2		Perforations diameter	D 0	φ4. O	±0.2
Accumulation Pitch	P0×20	254.0	± 1. 0		Tape thickness	t	0. 7	±0.2
Distance between lead wire	F	5. 0	+0.8 -0.2		Product inclination	△c	0	±1.5
Lead wire pitch distance	F 1	2, 5	+ 0. 4 - 0. 1					
Product inclination	∆h	0	±2.0					
Displacement of perforations	P 1	3. 85	±0.3	M	easurement position is he bottom of the clinch			
Dishigooment of heriorgrious	P 2	6. 35	±0.3					
Displacement of tape	W2	0.5MAX			ot to be displaced to he outside of the board			



 \cdot Provide 3~5 empty sections in the leading and end portions of the tape.

Outline Drawing

TND027MP-AZ



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