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**Advanced Power
Electronics Corp.**

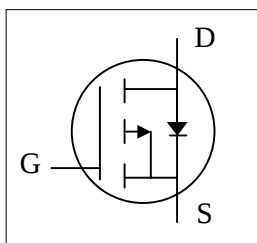
AP40P03GH/J

Pb Free Plating Product

P-CHANNEL ENHANCEMENT MODE

POWER MOSFET

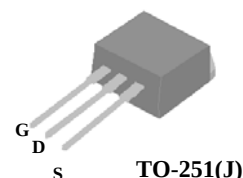
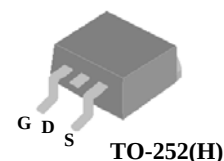
- ▼ Lower On-resistance
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic
- ▼ RoHS Compliant



BV_{DS}	-30V
$R_{DS(ON)}$	28m Ω
I_D	-30A

Description

The TO-252 package is universally preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters. The through-hole version (AP40P03GJ) is available for low-profile applications.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-30	V
V_{GS}	Gate-Source Voltage	± 20	V
$I_D@T_C=25^\circ\text{C}$	Continuous Drain Current, V_{GS} @ 10V	-30	A
$I_D@T_C=100^\circ\text{C}$	Continuous Drain Current, V_{GS} @ 10V	-18	A
I_{DM}	Pulsed Drain Current ¹	-120	A
$P_D@T_C=25^\circ\text{C}$	Total Power Dissipation	31.3	W
	Linear Derating Factor	0.25	W/ $^\circ\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ\text{C}$

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Thermal Resistance Junction-case Max.	4	$^\circ\text{C}/\text{W}$
Rthj-a	Thermal Resistance Junction-ambient Max.	110	$^\circ\text{C}/\text{W}$



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Electrical Characteristics@T_j=25°C(unless otherwise specified)

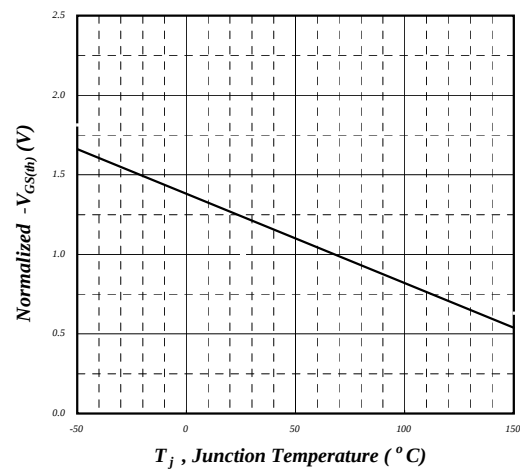
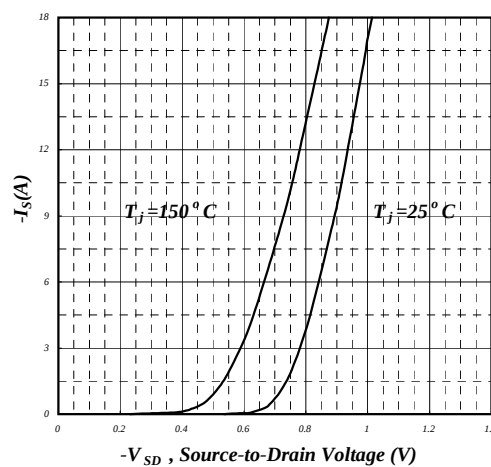
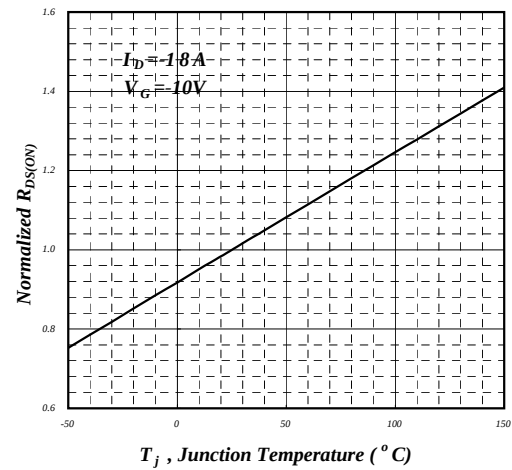
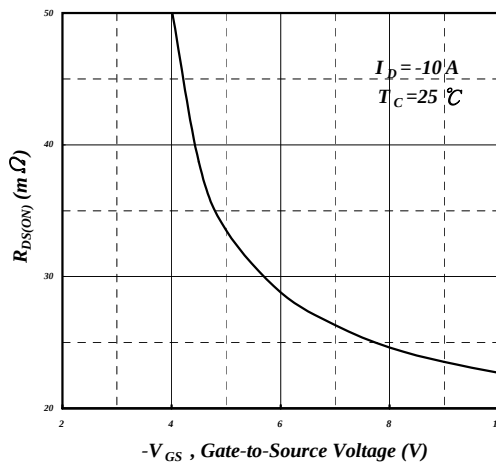
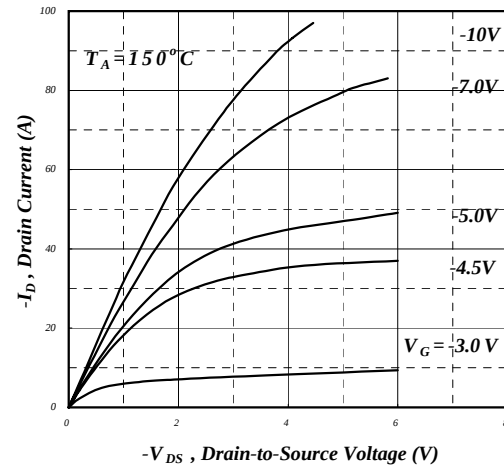
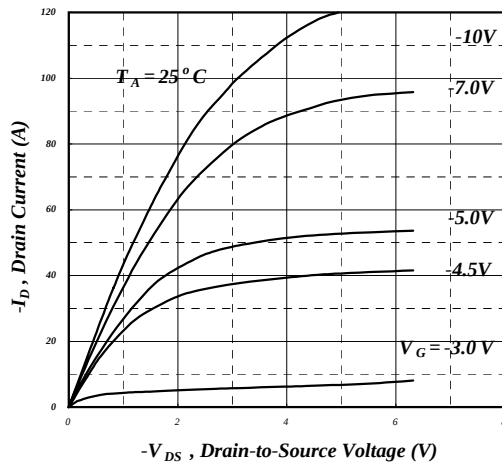
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-30	-	-	V
ΔBV _{DSS} /ΔT _j	Breakdown Voltage Temperature Coefficient	Reference to 25°C, I _D =-1mA	-	-0.02	-	V/°C
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-10V, I _D =-18A	-	-	28	mΩ
		V _{GS} =-4.5V, I _D =-10A	-	-	50	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250uA	-1	-	-3	V
g _{fs}	Forward Transconductance	V _{DS} =-10V, I _D =-18A	-	20	-	S
I _{DSS}	Drain-Source Leakage Current (T _j =25°C)	V _{DS} =-30V, V _{GS} =0V	-	-	-1	uA
	Drain-Source Leakage Current (T _j =150°C)	V _{DS} =-24V, V _{GS} =0V	-	-	-25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} = ±20V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =-18A	-	14	22	nC
Q _{gs}	Gate-Source Charge	V _{DS} =-24V	-	3	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =-4.5V	-	9	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =-15V	-	12	-	ns
t _r	Rise Time	I _D =-18A	-	56	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω, V _{GS} =-10V	-	30	-	ns
t _f	Fall Time	R _D =0.8Ω	-	57	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	915	1465	pF
C _{oss}	Output Capacitance	V _{DS} =-25V	-	280	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	195	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =-18A, V _{GS} =0V	-	-	-1.2	V
t _{rr}	Reverse Recovery Time ²	I _S =-18A, V _{GS} =0V,	-	30	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=-100A/μs	-	21	-	nC

Notes:

- 1.Pulse width limited by safe operating area.
- 2.Pulse width ≤300us , duty cycle ≤2%.





AP40P03GH/J

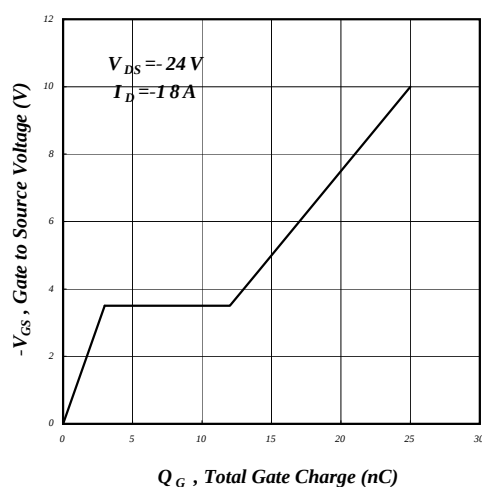


Fig 7. Gate Charge Characteristics

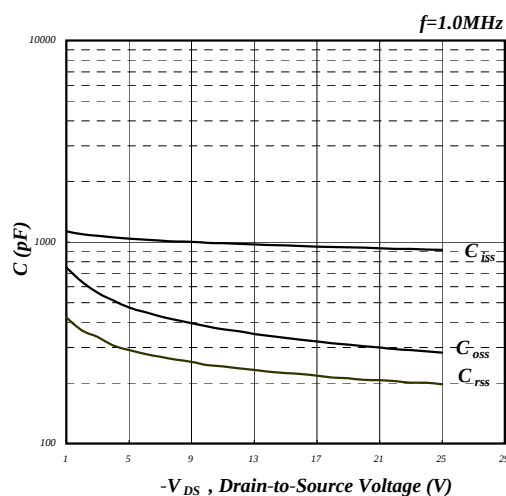


Fig 8. Typical Capacitance Characteristics

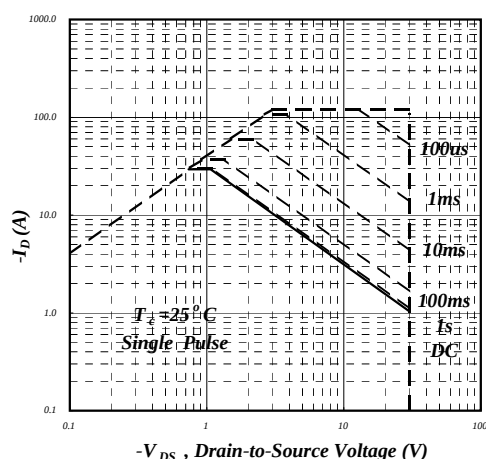


Fig 9. Maximum Safe Operating Area

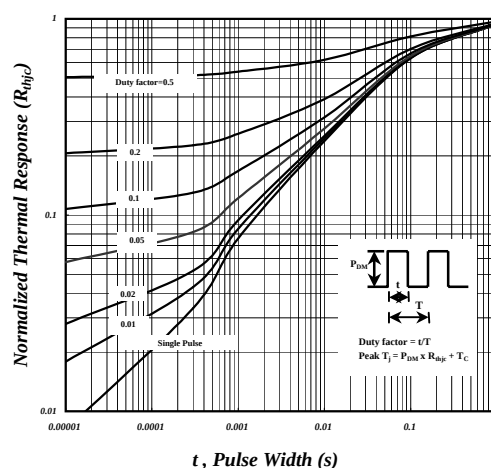


Fig 10. Effective Transient Thermal Impedance

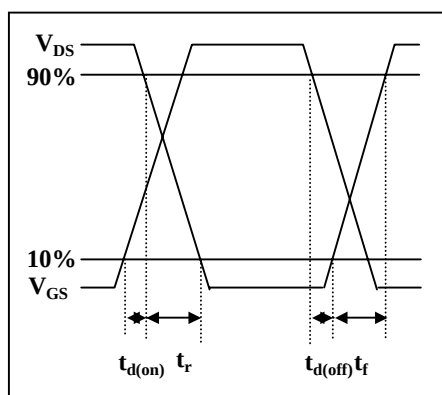


Fig 11. Switching Time Waveform

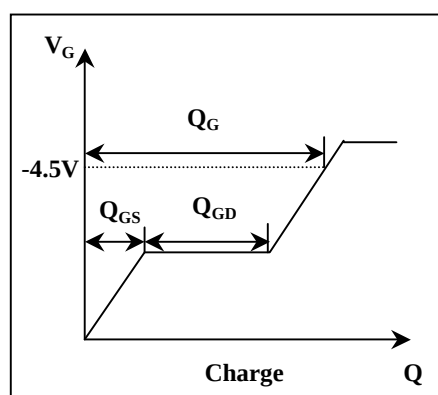


Fig 12. Gate Charge Waveform