

N-Channel Enhancement Mode Power MOSFET

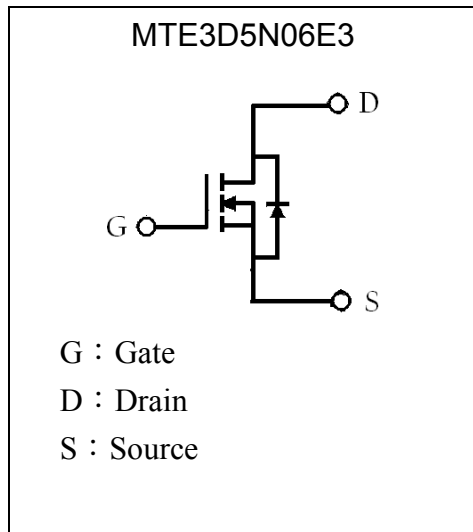
MTE3D5N06E3

BV _{DSS}	60V
I _D @V _{GS} =10V, T _C =25°C	80A
R _{DS(on)(TYP)} @ V _{GS} =10V, I _D =30A	3.6mΩ
R _{DS(on)(TYP)} @ V _{GS} =7V, I _D =20A	3.8mΩ

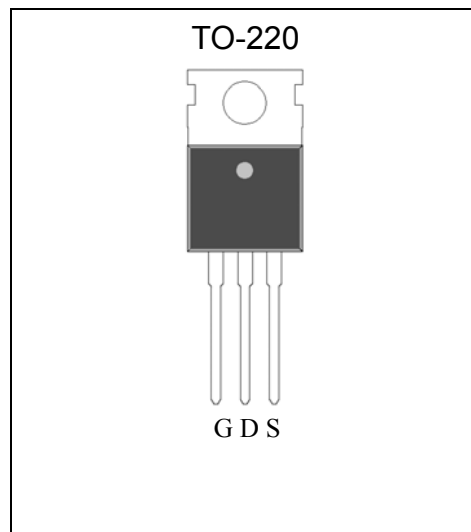
Features

- Simple Drive Requirement
- Fast Switching Characteristic
- RoHS compliant package

Symbol

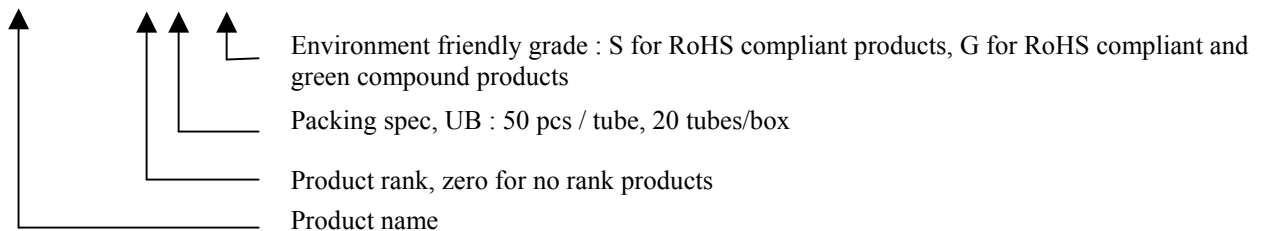


Outline



Ordering Information

Device	Package	Shipping
MTE3D5N06E3-0-UB-S	TO-220 (Pb-free lead plating package)	50 pcs/tube, 20 tubes/box, 4 boxes / carton



**Absolute Maximum Ratings** ($T_C=25^\circ\text{C}$, unless otherwise noted)

Parameter	Symbol	Limits	Unit
Drain-Source Voltage	V_{DS}	60	V
Gate-Source Voltage	V_{GS}	± 30	
Continuous Drain Current @ $T_C=25^\circ\text{C}$ (silicon limit)	I_D	143	A
Continuous Drain Current @ $T_C=100^\circ\text{C}$ (silicon limit)		101	
Continuous Drain Current @ $T_C=25^\circ\text{C}$ (package limit) (Note 1)		80	
Pulsed Drain Current (Note 3)	I_{DM}	480	
Continuous Drain Current @ $T_A=25^\circ\text{C}$ (Note 2)	I_{DSM}	15.4	
Continuous Drain Current @ $T_A=70^\circ\text{C}$ (Note 2)		12.3	
Avalanche Current (Note 3)	I_{AS}	30	
Avalanche Energy @ $L=100\mu\text{H}$, $I_D=30\text{A}$, $R_G=25\Omega$ (Note 2)	E_{AS}	45	mJ
Power Dissipation	P_D	$T_C=25^\circ\text{C}$ (Note 1)	188
		$T_C=100^\circ\text{C}$ (Note 1)	94
Power Dissipation	P_{DSM}	$T_A=25^\circ\text{C}$ (Note 2)	2
		$T_A=70^\circ\text{C}$ (Note 2)	1.3
Operating Junction and Storage Temperature	T_j, T_{stg}	-55~+175	$^\circ\text{C}$

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-case, max	$R_{th,j-c}$	0.8	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-ambient, max, (Note 2)	$R_{th,j-a}$	62.5	

- Note : 1. The power dissipation P_D is based on $T_{J(MAX)}=175^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
2. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in² FR-4 board with 2 oz. copper, in a still air environment with $T_A=25^\circ\text{C}$. The power dissipation P_{DSM} is based on $R_{\theta JA}$ and the maximum allowed junction temperature of 150°C . The value in any given application depends on the user's specific board design, and the maximum temperature of 175°C may be used if the PCB allows it.
3. Pulse width limited by junction temperature $T_{J(MAX)}=175^\circ\text{C}$. Ratings are based on low frequency and low duty cycles to keep initial $T_J=25^\circ\text{C}$.
4. The static characteristics are obtained using $<300\mu\text{s}$ pulses, duty cycle 0.5% maximum.
5. The $R_{\theta JA}$ is the sum of thermal resistance from junction to case $R_{\theta JC}$ and case to ambient.



Characteristics (Tc=25°C, unless otherwise specified)

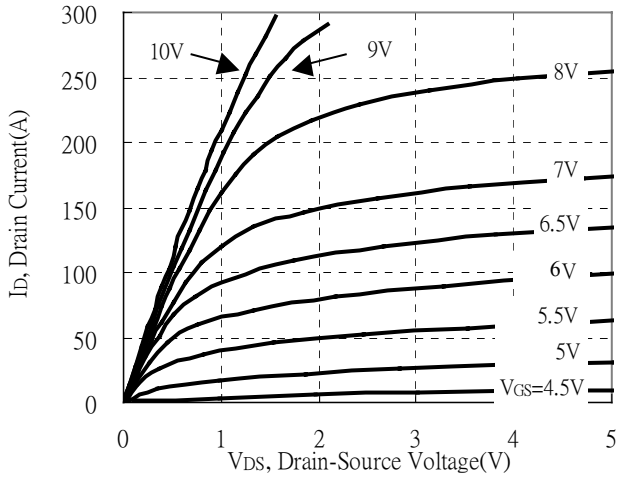
Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static					
BV _{DSS}	60	-	-	V	V _{GS} =0V, I _D =250μA
V _{GS(th)}	2.0	-	4.0		V _{DS} = V _{GS} , I _D =250μA
G _{FS}	-	46	-	S	V _{DS} =5V, I _D =20A
I _{GSS}	-	-	±100	nA	V _{GS} =±30V
I _{DSS}	-	-	1	μA	V _{DS} =60V, V _{GS} =0V
	-	-	25		V _{DS} =60V, V _{GS} =0V, T _j =125°C
*R _{DS(ON)}	-	3.6	4.8	mΩ	V _{GS} =10V, I _D =30A
	-	3.8	5.0		V _{GS} =7V, I _D =20A
Dynamic					
*Q _g	-	68.3	-	nC	I _D =30A, V _{DS} =30V, V _{GS} =10V
*Q _{gs}	-	11	-		
*Q _{gd}	-	28	-		
*t _{d(ON)}	-	24	-	ns	V _{DS} =30V, I _D =30A, V _{GS} =10V, R _G =1Ω
*t _r	-	25.2	-		
*t _{d(OFF)}	-	56.2	-		
*t _f	-	17.8	-		
C _{iss}	-	2919	-	pF	V _{GS} =0V, V _{DS} =25V, f=1MHz
C _{oss}	-	585	-		
C _{rss}	-	270	-		
R _g	-	1.1	-	Ω	f=1MHz
Source-Drain Diode					
*I _S	-	-	80	A	
*I _{SM}	-	-	480		
*V _{SD}	-	0.81	1.2	V	I _S =30A, V _{GS} =0V
*t _{rr}	-	28.7	-	ns	I _F =30A, V _{GS} =0V, dI _F /dt=100A/μs
*Q _{rr}	-	26.2	-	nC	

*Pulse Test : Pulse Width ≤300μs, Duty Cycle≤2%

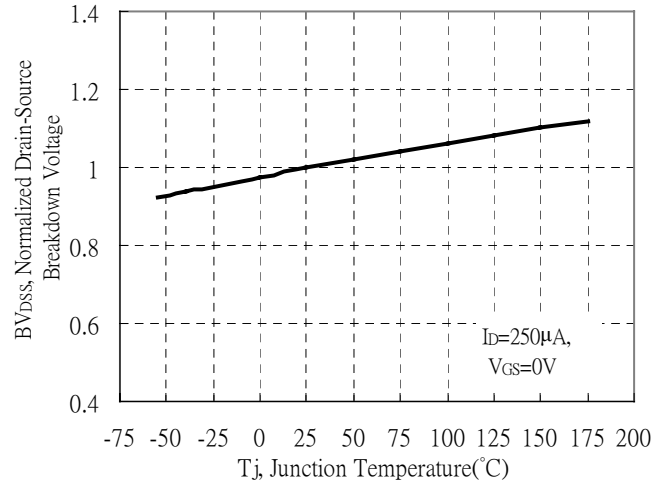


Typical Characteristics

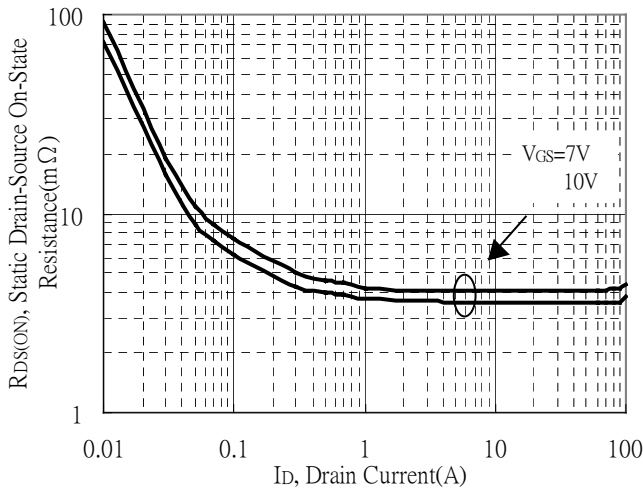
Typical Output Characteristics



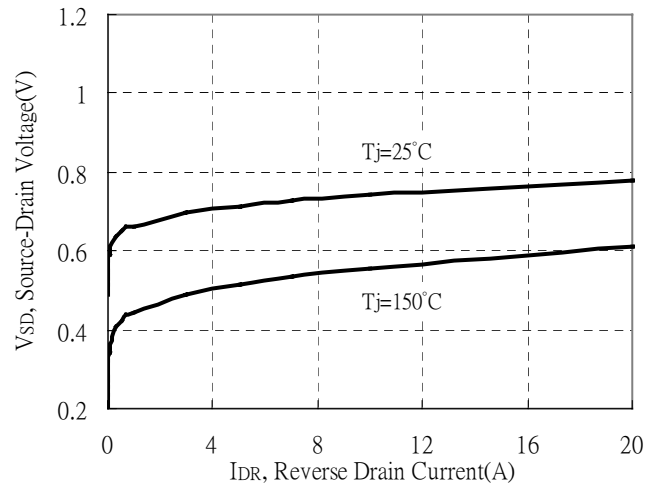
Brekdown Voltage vs Ambient Temperature



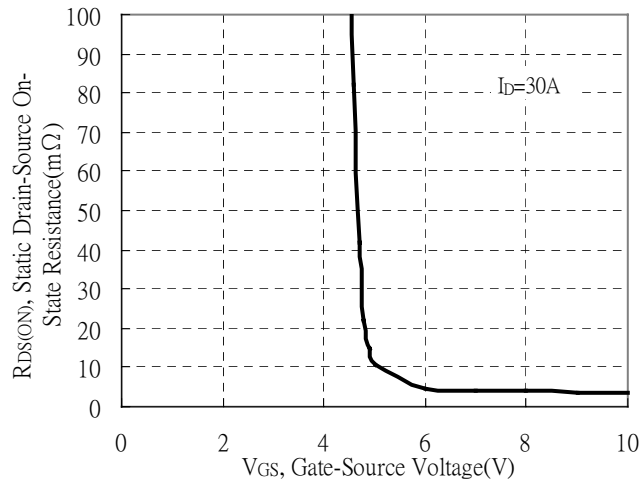
Static Drain-Source On-State resistance vs Drain Current



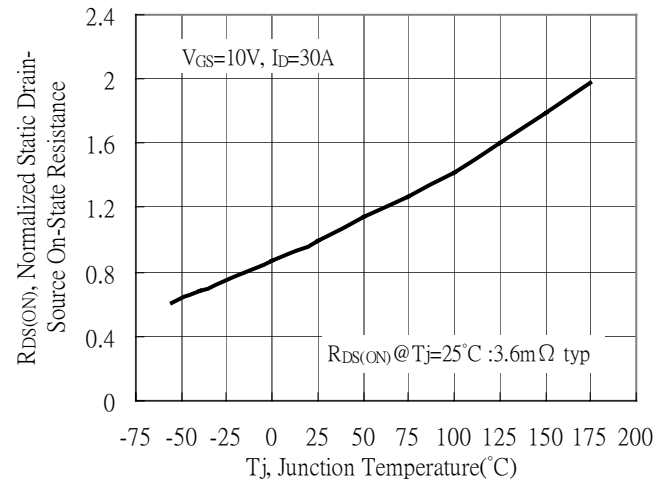
Reverse Drain Current vs Source-Drain Voltage



Static Drain-Source On-State Resistance vs Gate-Source Voltage

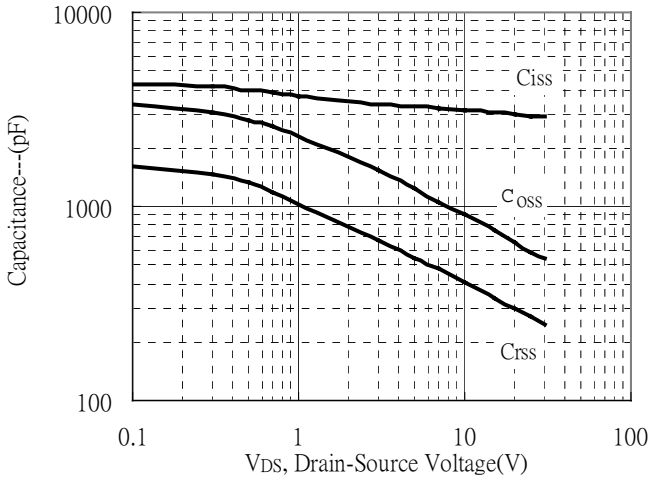


Drain-Source On-State Resistance vs Junction Temperature

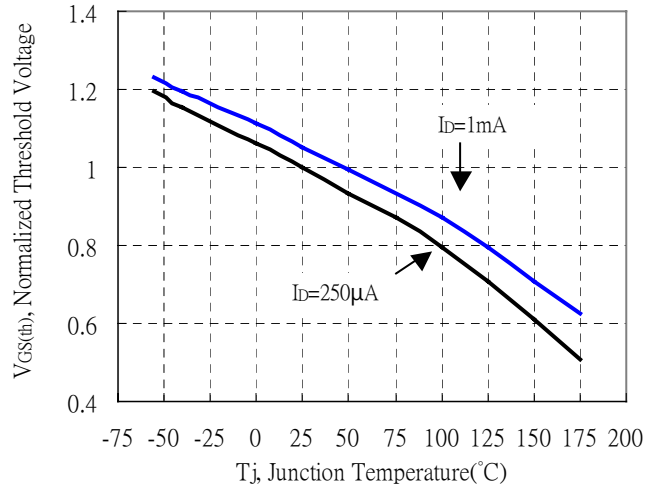


Typical Characteristics(Cont.)

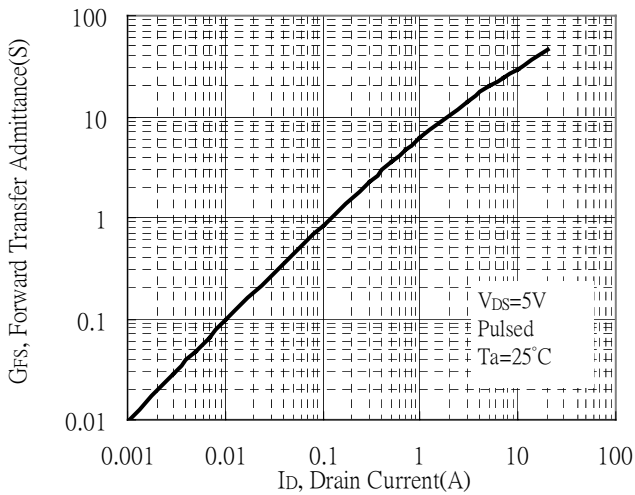
Capacitance vs Drain-to-Source Voltage



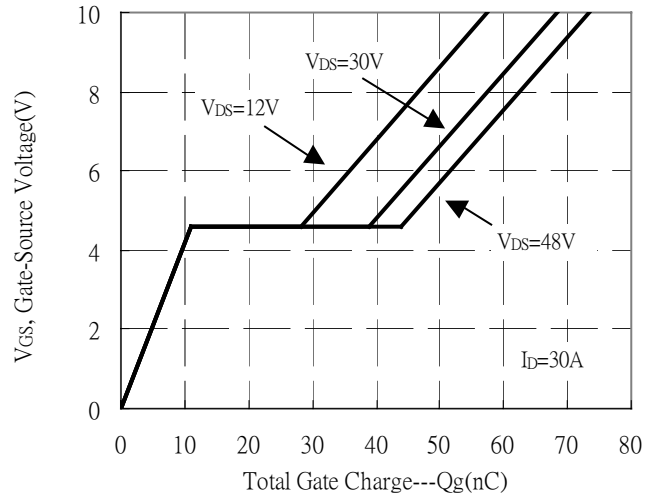
Normalized Threshold Voltage vs Junction Temperature



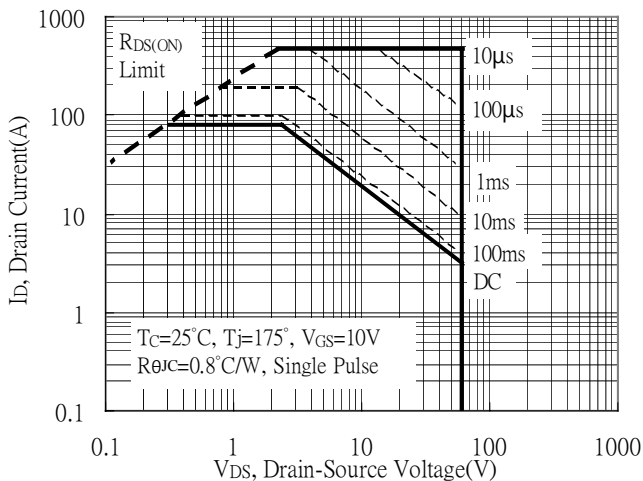
Forward Transfer Admittance vs Drain Current



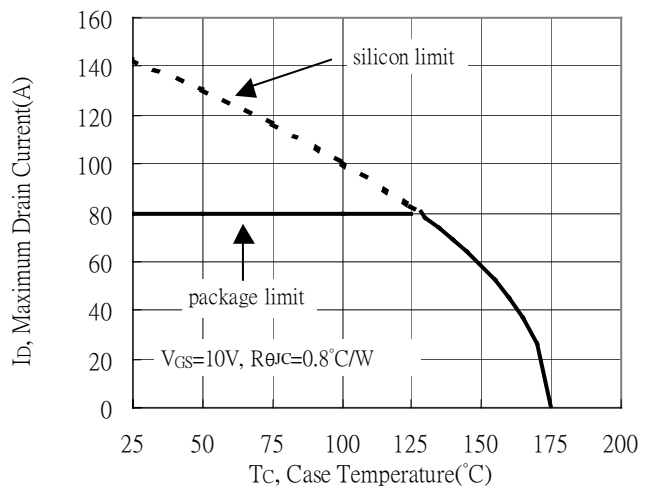
Gate Charge Characteristics



Maximum Safe Operating Area

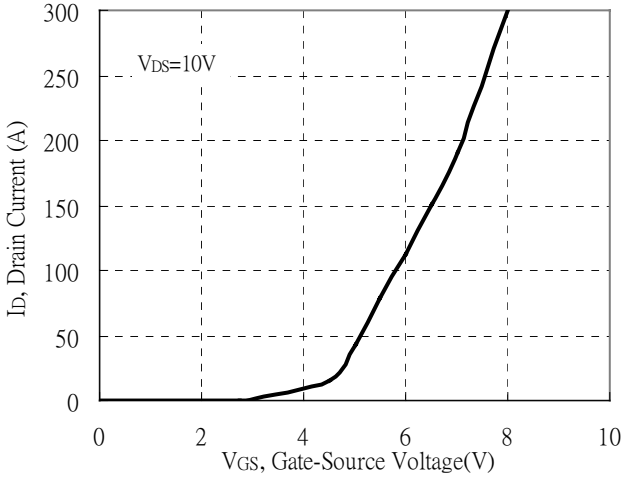


Maximum Drain Current vs Case Temperature

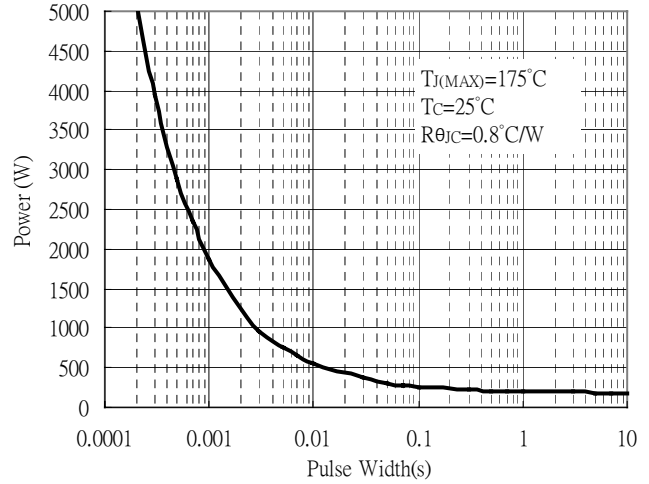


Typical Characteristics(Cont.)

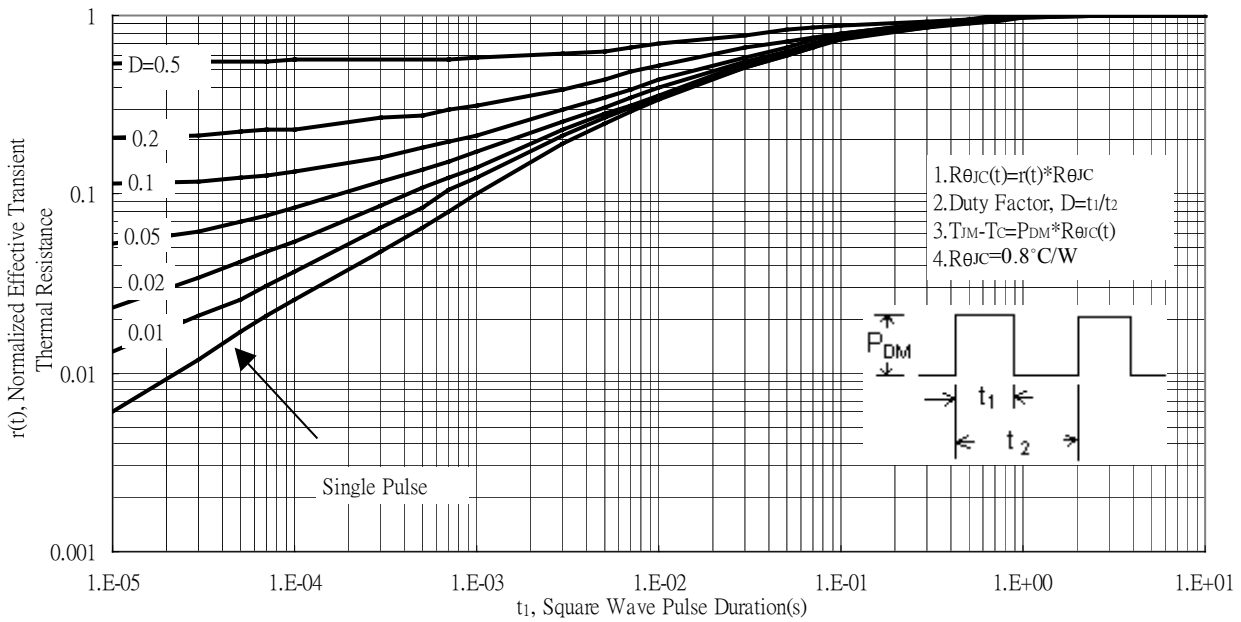
Typical Transfer Characteristics



Single Pulse Maximum Power Dissipation



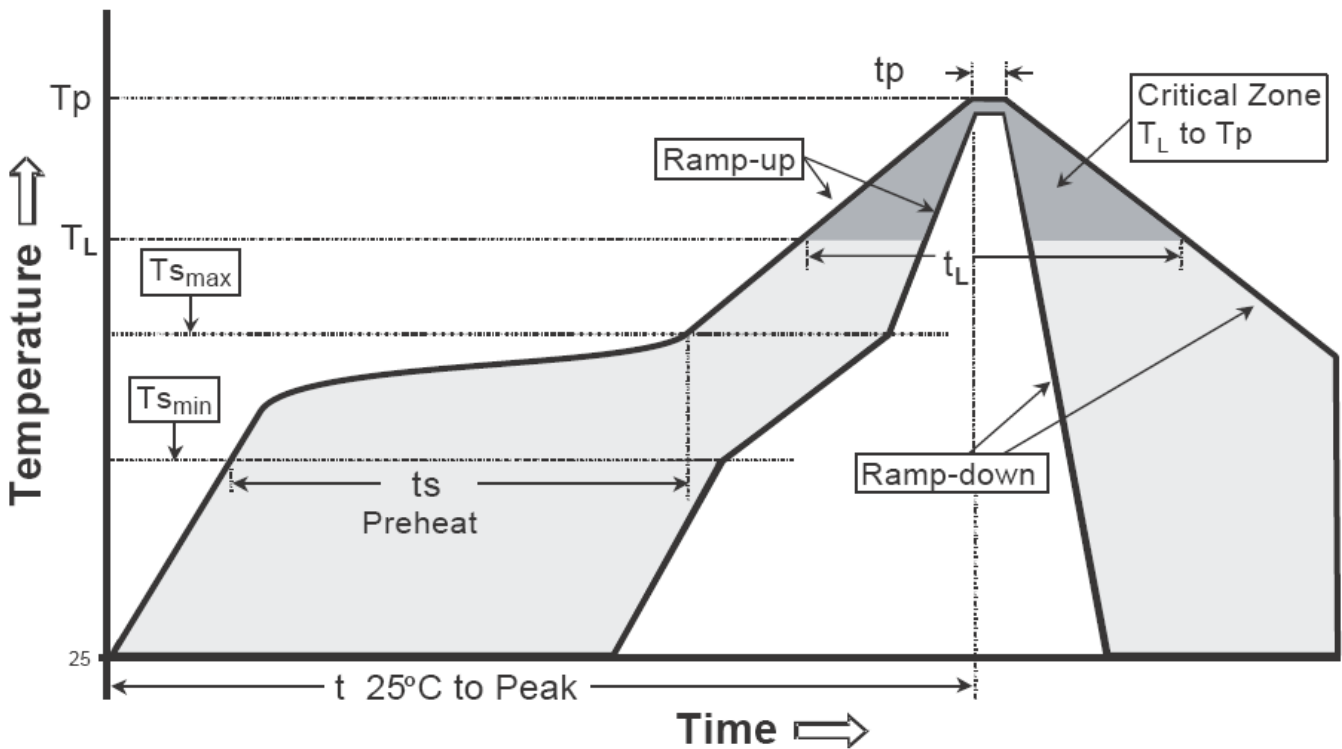
Transient Thermal Response Curves



Recommended wave soldering condition

Product	Peak Temperature	Soldering Time
Pb-free devices	260 +0/-5 °C	5 +1/-1 seconds

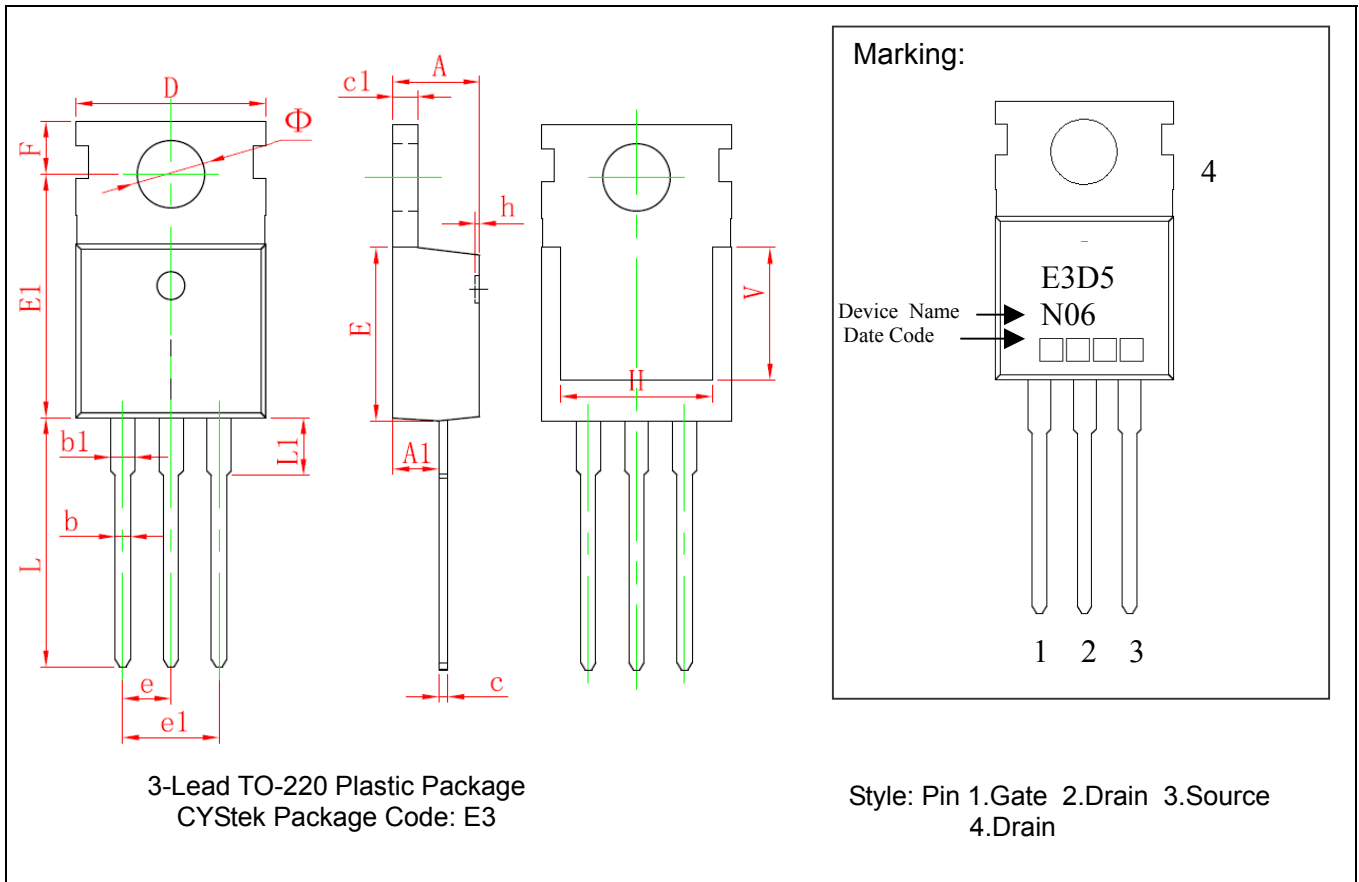
Recommended temperature profile for IR reflow



Profile feature	Sn-Pb eutectic Assembly	Pb-free Assembly
Average ramp-up rate (Tsmax to Tp)	3°C/second max.	3°C/second max.
Preheat		
-Temperature Min(T_s min)	100°C	150°C
-Temperature Max(T_s max)	150°C	200°C
-Time($t_{s min}$ to $t_{s max}$)	60-120 seconds	60-180 seconds
Time maintained above:		
-Temperature (T_L)	183°C	217°C
- Time (t_L)	60-150 seconds	60-150 seconds
Peak Temperature(T_p)	240 +0/-5 °C	260 +0/-5 °C
Time within 5°C of actual peak temperature(t_p)	10-30 seconds	20-40 seconds
Ramp down rate	6°C/second max.	6°C/second max.
Time 25 °C to peak temperature	6 minutes max.	8 minutes max.

Note : All temperatures refer to topside of the package, measured on the package body surface.

TO-220 Dimension



*: Typical

DIM	Millimeters		Inches		DIM	Millimeters		Inches	
	Min.	Max.	Min.	Max.		Min.	Max.	Min.	Max.
A	4.400	4.600	0.173	0.181	e	2.540*		0.100*	
A1	2.250	2.550	0.089	0.100	e1	4.980	5.180	0.196	0.204
b	0.710	0.910	0.028	0.036	F	2.650	2.950	0.104	0.116
b1	1.170	1.370	0.046	0.054	H	7.900	8.100	0.311	0.319
c	0.330	0.650	0.013	0.026	h	0.000	0.300	0.000	0.012
c1	1.200	1.400	0.047	0.055	L	12.900	13.400	0.508	0.528
D	9.910	10.250	0.390	0.404	L1	2.850	3.250	0.112	0.128
E	8.950	9.750	0.352	0.384	V	7/500	REF	0.295	REF
E1	12.650	12.950	0.498	0.510	Φ	3.400	3.800	0.134	0.150

Notes: 1.Controlling dimension: millimeters.
 2.Maximum lead thickness includes lead finish thickness, and minimum lead thickness is the minimum thickness of base material.
 3.If there is any question with packing specification or packing method, please contact your local CYStek sales office.

Material:

- Lead: Pure tin plated.
- Mold Compound: Epoxy resin family, flammability solid burning class: UL94V-0.

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