

CPW2-1200S050-Silicon Carbide Schottky Diode Chip

ZERO RECOVERY® RECTIFIER

 $\mathbf{V}_{\mathsf{RRM}} = 1200 \ \mathsf{V}$

 $\mathbf{I}_{\mathsf{F}(\mathsf{AVG})} = 50 \, \mathsf{A}$

 $Q_c = 305 \text{ nC}$

Features

- 1200-Volt Schottky Rectifier
- Zero Reverse Recovery
- Zero Forward Recovery
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on V_F



Chip Outline

Part Number	Anode	Cathode	Package	Marking
CPW2-1200S050B	Al	NiV/Ag	Sawn on Foil	Wafer # on Foil

Maximum Ratings

Symbol	Parameter	Value	Unit	Test Conditions	Note
V _{RRM}	Repetitive Peak Reverse Voltage	1200	V		
V_{RSM}	Surge Peak Reverse Voltage	1200	V		
V _{DC}	DC Blocking Voltage	1200	V		
$\boldsymbol{I}_{\text{F(AVG)}}$	Average Forward Current	50	Α	T ₃ =175°C	
\mathbf{I}_{FRM}	Repetitive Peak Forward Surge Current	TBD	А	$T_c=25$ °C, $t_p=8.3$ ms, Half Sine Wave	1
\mathbf{I}_{FSM}	Non-Repetitive Peak Forward Surge Current	TBD	А	T _c =25°C, t _p =10 μs, Pulse	1
$T_{_{\rm J}}$, $T_{\rm stg}$	Operating Junction and Storage Temperature	-55 to +175	°C		



Electrical Characteristics

Symbol	Parameter	Тур.	Max.	Unit	Test Conditions	Note
V _F	Forward Voltage	1.65 2.6	2.0 3.0	V	I _F = 50 A T _J =25°C I _F = 50 A T _J =175°C	
I _R	Reverse Current	10 50	200 1000	μΑ	$V_R = 1200 \text{ V } T_J = 25^{\circ}\text{C}$ $V_R = 1200 \text{ V } T_J = 175^{\circ}\text{C}$	
Q _c	Total Capacitive Charge	305		nC	$V_R = 500 \text{ V, } I_F = 50 \text{ A}$ $di/dt = 500 \text{ A/}\mu\text{s}$ $T_J = 25^{\circ}\text{C}$	
С	Total Capacitance	4500 396 325		pF	$V_R = 0 \text{ V}, T_J = 25^{\circ}\text{C}, f = 1 \text{ MHz}$ $V_R = 200 \text{ V}, T_J = 25^{\circ}\text{C}, f = 1 \text{ MHz}$ $V_R = 400 \text{ V}, T_J = 25^{\circ}\text{C}, f = 1 \text{ MHz}$	

Note:

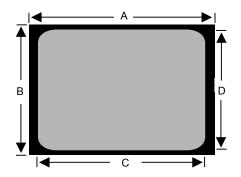
1. Assumes $\theta_{\text{J-C}}$ Thermal Resistance of $0.5\,^{\circ}\text{C/W}$ or less.

Mechanical Parameters

Mechanical Farameters						
Parameter	Тур.	Unit				
Die Size	4.02 x 8.23	mm				
Anode Pad Size	3.58 x 7.77	mm				
Anode Pad Opening	3.22 x 7.41	mm				
Thickness	387 ± 10%	μm				
Wafer Size	100	mm				
Anode Metalization (AI)	4	μm				
Cathode Metalization (Ni/Ag)	1.8	μm				
Frontside Passivation	Nitride					



Chip Dimensions



Symbol	Dimension				
	mm	inch			
А	8.23	0.324			
В	4.02	0.158			
С	7.41	0.292			
D	3.22	0.127			

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The die-on-tape method of delivering these SiC die may be considered a means of temporary storage only. Due to an increase in adhesion over time, die stored for an extended period may affix too strongly to the tape. These die should be stored in a temperature-controlled, nitrogen dry box soon after receipt. Cree will further recommend that all die be removed from tape to a waffle pack, to a similar storage medium, or used in production within 2 - 3 weeks of delivery to assure 100% release of all die without issues.