

RQJ0201UGDQA

Silicon P Channel MOS FET **Power Switching**

R07DS0290EJ0400 (Previous: REJ03G1317-0300) Rev.4.00 Mar 28, 2011

Features

• Low on-resistance

 $R_{DS(on)} = 53 \text{ m}\Omega \text{ typ } (V_{GS} = -4.5 \text{ V}, I_D = -1.8 \text{ A})$

- Low drive current
- High speed switching
- 2.5 V gate drive

Outline

RENESAS Package code: PLSP0003ZB-A (Package name: MPAK)



3 ⊖D

1. Source

2. Gate

3. Drain

Note: Marking is "UG".

Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	-20	V
Gate to source voltage	V_{GSS}	+8 / -12	V
Drain current	I _D	-3.4	A
Drain peak current	I _{D(pulse)} Note1	-10	A
Body - drain diode reverse drain current	I _{DR}	-3.4	Α
Channel dissipation	Pch _(pulse) Note2	0.8	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. When using the glass epoxy board (FR-4: 40 x 40 x 1 mm)

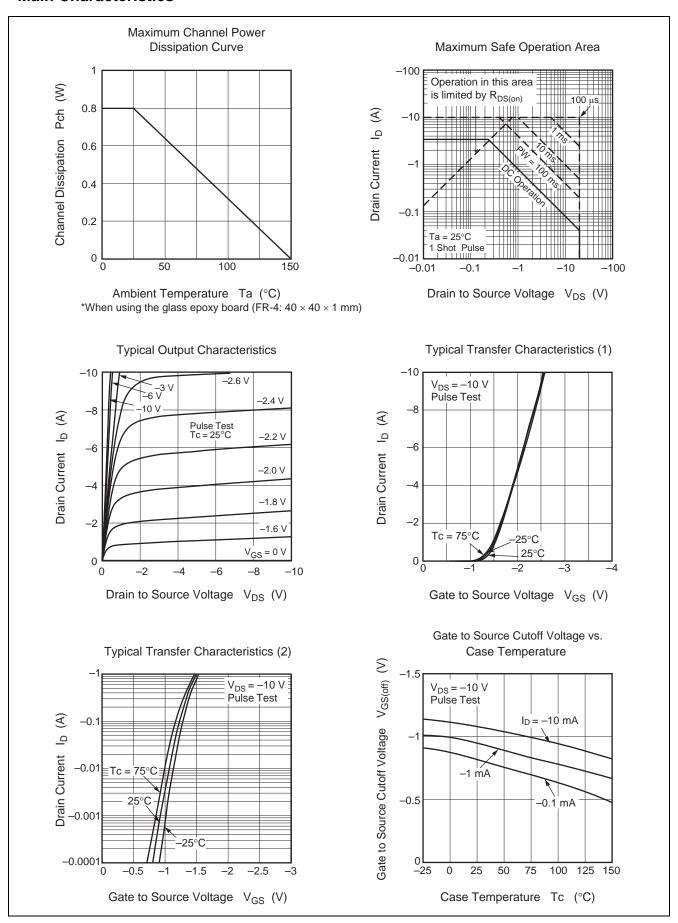
Electrical Characteristics

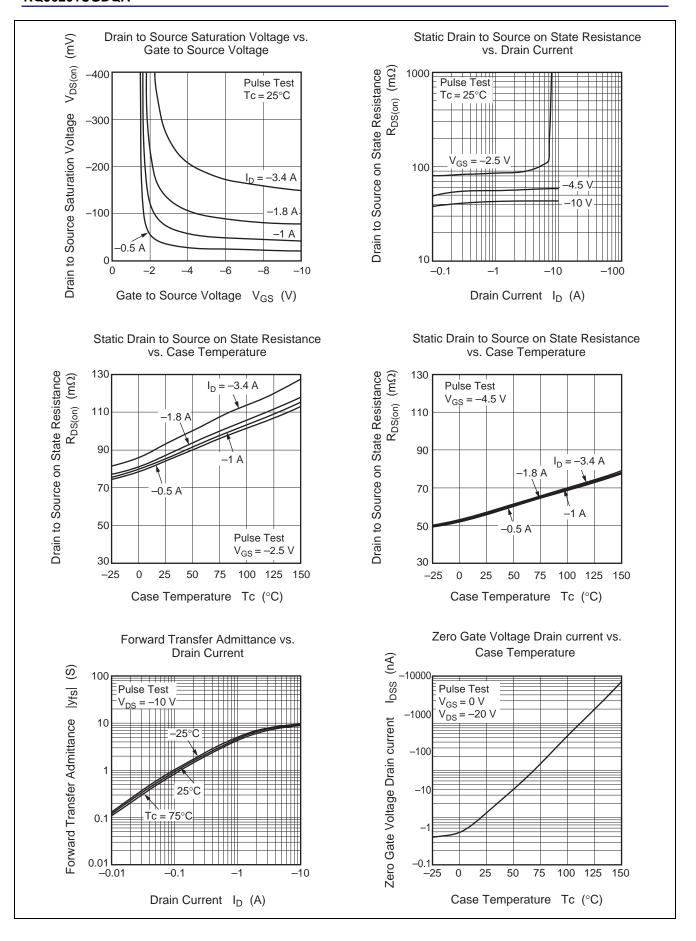
 $(Ta = 25^{\circ}C)$

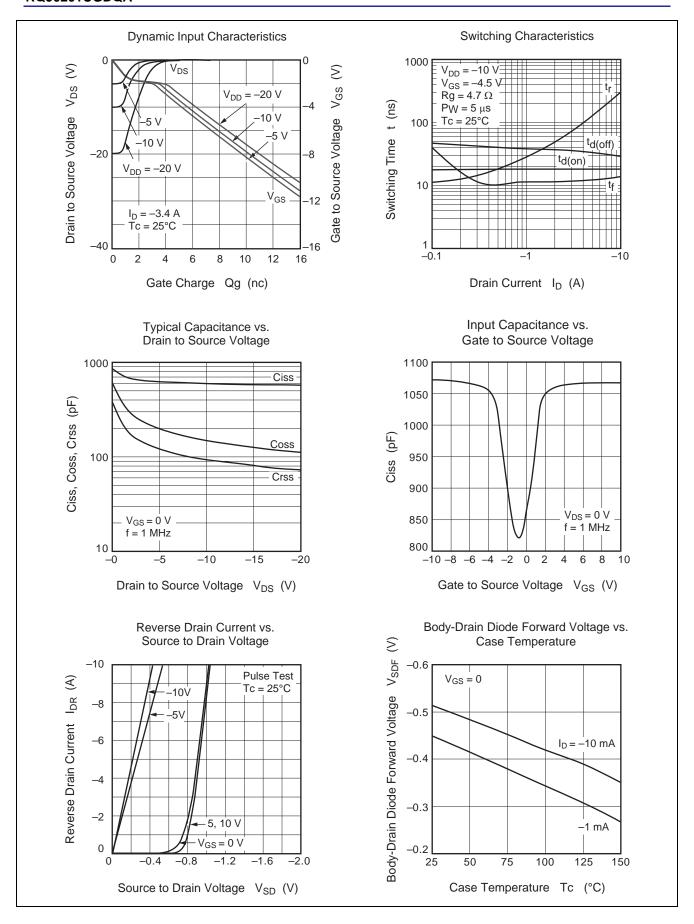
Item	Symbol	Min	Тур	Max	Unit	Test conditions	
Drain to source breakdown voltage	$V_{(BR)DSS}$	-20	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$	
Gate to source breakdown voltage	$V_{(BR)GSS}$	+8	_	_	V	$I_G = +100 \mu A, V_{DS} = 0$	
	V _{(BR)GSS}	-12		_	V	$I_G = -100 \mu A, V_{DS} = 0$	
Gate to source leak current	I _{GSS}			+10	μΑ	$V_{GS} = +6 \text{ V}, V_{DS} = 0$	
	I _{GSS}			-10	μΑ	$V_{GS} = -10 \text{ V}, V_{DS} = 0$	
Drain to source leak current	I _{DSS}			-1	μΑ	$V_{DS} = -20 \text{ V}, V_{GS} = 0$	
Gate to source cutoff voltage	$V_{GS(off)}$	-0.4		-1.4	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$	
Drain to source on state resistance	R _{DS(on)}	_	53	69	mΩ	$I_D = -1.8 \text{ A}, V_{GS} = -4.5 \text{ V}^{\text{Note3}}$	
	R _{DS(on)}	_	80	112	mΩ	$I_D = -1.8 \text{ A}, V_{GS} = -2.5 \text{ V}^{\text{Note3}}$	
Forward transfer admittance	y _{fs}	4.5	6.5	_	S	$I_D = -1.8 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$	
Input capacitance	Ciss	_	597	_	pF	V _{DS} = −10 V	
Output capacitance	Coss	_	149	_	pF	$V_{GS} = 0$	
Reverse transfer capacitance	Crss	_	93	_	pF	f = 1 MHz	
Turn - on delay time	t _{d(on)}	_	18	_	ns	$I_D = -1.8 \text{ A}$	
Rise time	t _r	_	43	_	ns	$V_{GS} = -4.5 \text{ V}$ $R_L = 5.5 \Omega$ $Rg = 4.7 \Omega$	
Turn - off delay time	$t_{d(off)}$	_	37	_	ns		
Fall time	t _f	_	12	_	ns		
Total gate charge	Qg		6.3		nC	V _{DD} = -10 V	
Gate to source charge	Qgs		1.1		nC	$V_{GS} = -4.5 \text{ V}$	
Gate to drain charge	Qgd	_	2.5	_	nC	$I_D = -3.4 \text{ A}$	
Body - drain diode forward voltage	V_{DF}	_	-0.85	-1.1	V	$I_F = -3.4 \text{ A}, V_{GS} = 0^{\text{Note3}}$	

Notes: 3. Pulse test

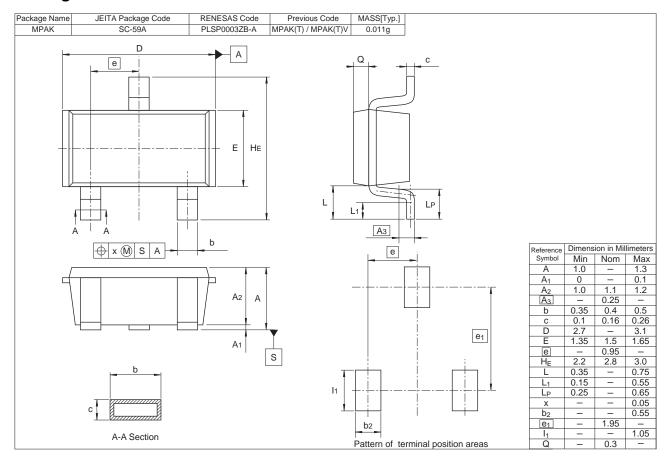
Main Characteristics







Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
RQJ0201UGDQATL-H	3000 pcs.	φ178 mm reel, 8 mm Emboss taping

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Renesas Electronics America Inc. 2880 Scott Boulevard Santa Clara, CA 95050-2554, U.S.A. Tel: +1-408-588-6000, Fax: +1-408-588-6130

Renesas Electronics Canada Limited 1101 Nicholson Road, Newmarket, Ontario L3Y 9C3, Canada Tel: +1-905-898-5441, Fax: +1-905-898-3220

Renesas Electronics Europe Limited Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K Tel: +444-1628-585-100, Fax: +444-1628-585-900 Renesas Electronics Europe GmbH

Arcadiastrasse 10, 40472 Düsseldorf, Germany Tel: +49-211-65030, Fax: +49-211-6503-1327

Renesas Electronics (China) Co., Ltd.
7th Floor, Quantum Plaza, No.27 ZhiChunLu Haidian District, Beijing 100083, P.R.China
Tel: +86-10-2353-1155, Fax: +86-10-8235-7679

Renesas Electronics Hong Kong Limited
Unit 1601-1613, 161F., Tower 2, Grand Century Place, 193 Prince Edward Road West, Mongkok, Kowloon, Hong Kong
Tel: +852-2868-9318, Fax: +852-2886-9022/9044

Renesas Electronics Taiwan Co., Ltd. 13F, No. 363, Fu Shing North Road, Taipei, Taiv Tel: +886-2-8175-9600, Fax: +886 2-8175-9670

Renesas Electronics Singapore Pte. Ltd. 1 harbourFront Avenue, #06-10, keppel Bay Tower, Singapore 098632 Tel: +65-6213-0200, Fax: +65-6278-8001

Renesas Electronics Malaysia Sdn.Bhd.
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No. 18, Jln Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: +60-3-7955-9390, Fax: +60-3-7955-9510

Renesas Electronics Korea Co., Ltd. 11F., Samik Lavied' or Bidg., 720-2 Yeoksam-Dong, Kangnam-Ku, Seoul 135-080, Korea Tel: 482-2-558-3737, Fax: 482-2-558-5141

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