

PSMN013-100XS

N-channel 100V 13 m Ω standard level MOSFET in TO220F (SOT186A)

Rev. 2 — 6 March 2012

Product data sheet

1. Product profile

1.1 General description

Standard level N-channel MOSFET in TO220F (SOT186A) package qualified to 175C. This product is designed and qualified for use in a wide range of industrial, communications and domestic equipment.

1.2 Features and benefits

- High efficiency due to low switching and conduction losses
- Isolated package
- Suitable for standard level gate drive

1.3 Applications

- AC-to-DC power supply equipment
- Motor control

- Server power supplies
- Synchronous rectification

1.4 Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	-	100	V
I _D	drain current	$T_{mb} = 25 ^{\circ}\text{C}; V_{GS} = 10 \text{V}; \text{see} \frac{\text{Figure 1}}{}$	-	-	35.2	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	-	48.4	W
Static charact	teristics					
R _{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 10 \text{ A}; T_j = 25 ^{\circ}\text{C};$ see <u>Figure 12</u> ; see <u>Figure 13</u>	-	10.8	13.9	mΩ
Dynamic char	racteristics					
Q_{GD}	gate-drain charge	$V_{GS} = 10 \text{ V}; I_D = 10 \text{ A}; V_{DS} = 50 \text{ V};$	-	17.5	-	nC
Q _{G(tot)}	total gate charge	see Figure 14; see Figure 15	-	57.5	-	nC
Avalanche ru	ggedness					
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_D = 35.2 A; $V_{sup} \le$ 100 V; unclamped; R_{GS} = 50 Ω ; see Figure 3	-	-	180	mJ



2. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate		_
2	D	drain	mb	D
3	S	source		_G (巨本)
mb		mounting base; isolated		mbb076 S
			SOT186A (TO-220F)	

3. Ordering information

Table 3. Ordering information

Type number	Package			
	Name	Description	Version	
PSMN013-100XS	TO-220F	plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220 "full pack"	SOT186A	

4. Limiting values

Table 4. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{DS}	drain-source voltage	T _j ≥ 25 °C; T _j ≤ 175 °C	-	100	V
V_{DGR}	drain-gate voltage	$T_j \ge 25 \text{ °C}; T_j \le 175 \text{ °C}; R_{GS} = 20 \text{ k}\Omega$	-	100	V
V_{GS}	gate-source voltage		-20	20	V
I_{D}	drain current	$V_{GS} = 10 \text{ V}; T_{mb} = 25 \text{ °C}; \text{ see } \frac{\text{Figure 1}}{\text{M}}$	-	35.2	Α
		$V_{GS} = 10 \text{ V}; T_{mb} = 100 \text{ °C}; \text{ see } \frac{\text{Figure 1}}{\text{Model}}$	-	24.9	Α
I _{DM}	peak drain current	pulsed; $t_p \le 10 \mu s$; $T_{mb} = 25 \text{ °C}$; see Figure 4	-	141	Α
P _{tot}	total power dissipation	T _{mb} = 25 °C; see <u>Figure 2</u>	-	48.4	W
T _{stg}	storage temperature		-55	175	°C
Tj	junction temperature		-55	175	°C
$T_{sld(M)}$	peak soldering temperature		-	260	°C

Table 4. Limiting values ...continued

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
Source-dra	in diode				
Is	source current	T _{mb} = 25 °C	-	40.3	Α
I _{SM}	peak source current	pulsed; $t_p \le 10 \ \mu s$; $T_{mb} = 25 \ ^{\circ}C$	-	141	Α
Avalanche	ruggedness				
E _{DS(AL)S}	non-repetitive drain-source avalanche energy	V_{GS} = 10 V; $T_{j(init)}$ = 25 °C; I_D = 35.2 A; $V_{sup} \le$ 100 V; unclamped; R_{GS} = 50 Ω ; see Figure 3	-	180	mJ

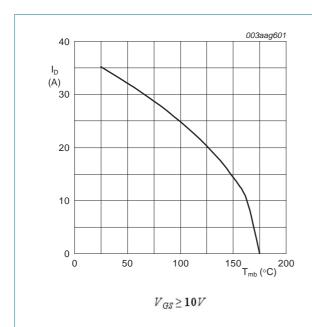


Fig 1. Continuous drain current as a function of mounting base temperature

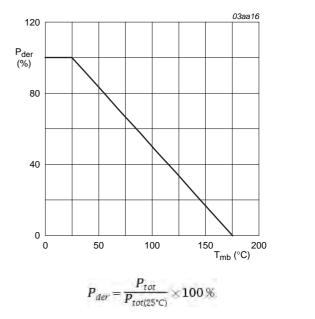


Fig 2. Normalized total power dissipation as a function of mounting base temperature

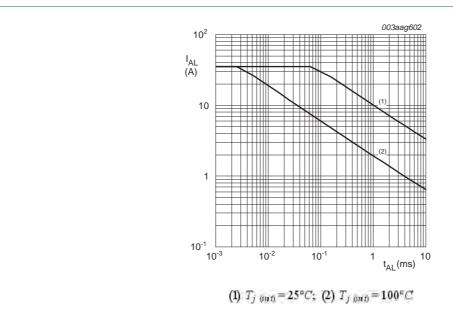
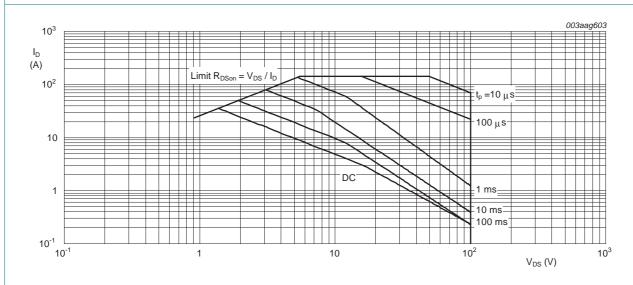


Fig 3. Single pulse avalanche rating; avalanche current as a function of avalanche time



 $T_{mb} = 25^{\circ}C$; I_{DM} is a single pulse

Fig 4. Safe operating area; continuous and peak drain currents as a function of drain-source voltage

5. Thermal characteristics

Table 5. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
$R_{th(j-mb)}$	thermal resistance from junction to mounting base	see Figure 5	-	2.85	3.1	K/W
$R_{th(j-a)}$	thermal resistance from junction to ambient	vertical in free air	-	55	-	K/W

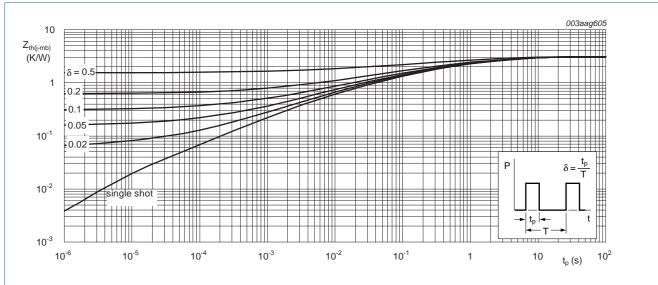


Fig 5. Transient thermal impedance from junction to mounting base as a function of pulse duration

6. Isolation characteristics

Table 6. Isolation characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
C _{isol}	isolation capacitance	f = 1 MHz	-	10	-	pF
V _{isol(RMS)}	RMS isolation voltage	50 Hz ≤ f ≤ 60 Hz; RH ≤ 65 %; sinusoidal waveform; clean and dust free	-	-	2500	V

7. Characteristics

Table 7. Characteristics

Table 7.	Characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static cha	racteristics					
$V_{(BR)DSS}$	drain-source	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	100	-	-	V
	breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = -55 °C$	90	-	-	V
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = 25 \text{ °C}$; see <u>Figure 10</u> ; see <u>Figure 11</u>	2	3	4	V
		$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = 175 \text{ °C}$; see Figure 10	1	-	-	V
		$I_D = 1 \text{ mA}$; $V_{DS} = V_{GS}$; $T_j = -55 \text{ °C}$; see Figure 10	-	-	4.6	V
I_{DSS}	drain leakage current	$V_{DS} = 100 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	-	2	μΑ
		$V_{DS} = 100 \text{ V}; V_{GS} = 0 \text{ V}; T_j = 100 ^{\circ}\text{C}$	-	-	40	μΑ
I_{GSS}	gate leakage current	$V_{GS} = 20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nA
		$V_{GS} = -20 \text{ V}; V_{DS} = 0 \text{ V}; T_j = 25 \text{ °C}$	-	2	100	nA
R_{DSon}	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 10 \text{ A}; T_j = 25 \text{ °C};$ see <u>Figure 12</u> ; see <u>Figure 13</u>	-	10.8	13.9	mΩ
		$V_{GS} = 10 \text{ V}; I_D = 10 \text{ A}; T_j = 100 \text{ °C};$ see Figure 13	-	18.9	24.3	mΩ
		$V_{GS} = 10 \text{ V; } I_D = 10 \text{ A; } T_j = 175 \text{ °C;}$ see Figure 13	-	30.2	38.9	mΩ
R_G	internal gate resistance (AC)	f = 1 MHz	-	1	-	Ω
Dynamic	characteristics					
Q _{G(tot)}	total gate charge	$I_D = 10 \text{ A}; V_{DS} = 50 \text{ V}; V_{GS} = 10 \text{ V};$	-	57.5	-	nC
Q_{GS}	gate-source charge	see Figure 14; see Figure 15	-	13.1	-	nC
Q _{GS(th)}	pre-threshold gate-source charge		-	8.8	-	nC
Q _{GS(th-pl)}	post-threshold gate-source charge		-	4.3	-	nC
Q_{GD}	gate-drain charge		-	17.5	-	nC
$V_{GS(pl)}$	gate-source plateau voltage	$I_D = 10 \text{ A}$; $V_{DS} = 50 \text{ V}$; see <u>Figure 14</u>	-	4.5	-	V
C _{iss}	input capacitance	$V_{DS} = 50 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 16}}{\text{Figure 17}}$	-	3195	-	pF
C _{oss}	output capacitance	$V_{DS} = 50 \text{ V}; V_{GS} = 0 \text{ V}; f = 1 \text{ MHz};$ $T_j = 25 \text{ °C}; \text{ see } \frac{\text{Figure 16}}{\text{ Company 100}}$	-	221	-	pF
C _{rss}	reverse transfer capacitance	$V_{DS} = 50 \text{ V}$; $V_{GS} = 0 \text{ V}$; $f = 1 \text{ MHz}$; $T_j = 25 \text{ °C}$; see Figure 16; see Figure 17	-	136	-	pF
t _{d(on)}	turn-on delay time	$V_{DS} = 50 \text{ V}; R_L = 5 \Omega; V_{GS} = 10 \text{ V};$	-	18	-	ns
t _r	rise time	$R_{G(ext)} = 4.7 \Omega; T_j = 25 °C$	-	18	-	ns
t _{d(off)}	turn-off delay time		-	46	-	ns
t _f	fall time		-	21	-	ns

Table 7. Characteristics ... continued

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Source-drai	n diode					
V _{SD}	source-drain voltage	$I_S = 10 \text{ A}; V_{GS} = 0 \text{ V}; T_j = 25 ^{\circ}\text{C};$ see <u>Figure 18</u>	-	0.8	1.2	V
t _{rr}	reverse recovery time	$I_S = 10 \text{ A}$; $dI_S/dt = -100 \text{ A/}\mu\text{s}$; $V_{GS} = 0 \text{ V}$;	-	52	-	ns
Qr	recovered charge	$V_{DS} = 50 \text{ V}$	-	109	-	nC

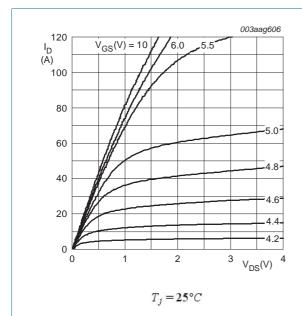


Fig 6. Output characteristics; drain current as a function of drain-source voltage; typical values

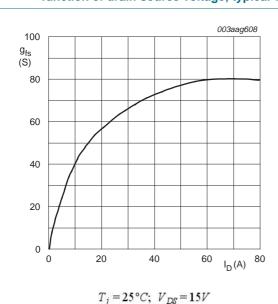


Fig 8. Forward transconductance as a function of drain current; typical values

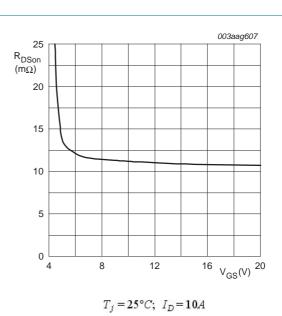


Fig 7. Drain-source on-state resistance as a function of gate-source voltage; typical values

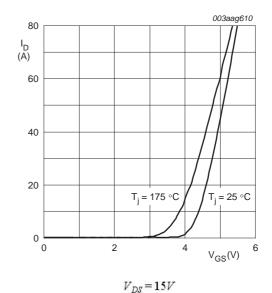


Fig 9. Transfer characteristics; drain current as a function of gate-source voltage; typical values

PSMN013-100XS

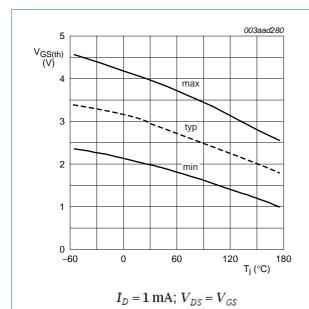


Fig 10. Gate-source threshold voltage as a function of junction temperature

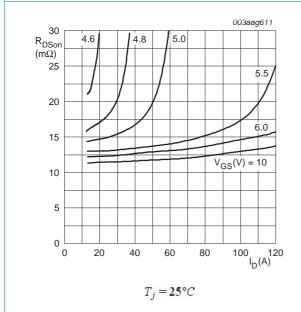
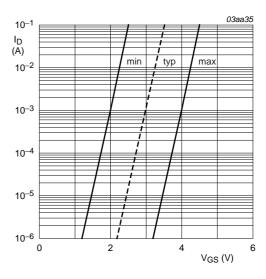
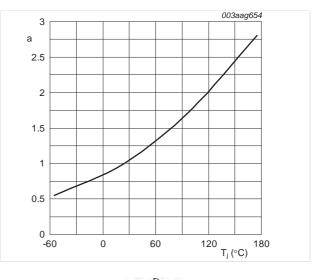


Fig 12. Drain-source on-state resistance as a function of drain current; typical values



 $T_j = 25 \,^{\circ}C; V_{DS} = 5V$

Fig 11. Sub-threshold drain current as a function of gate-source voltage



 $a = \frac{R_{DSon}}{R_{DSon(25^{\circ}C)}}$

Fig 13. Normalized drain-source on-state resistance factor as a function of junction temperature

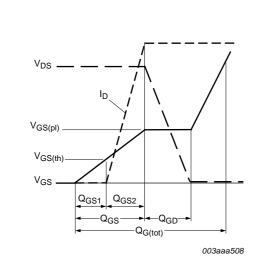


Fig 14. Gate charge waveform definitions

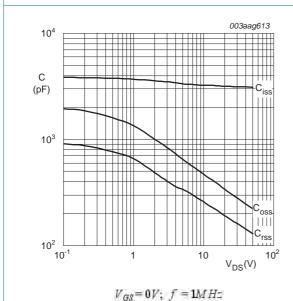
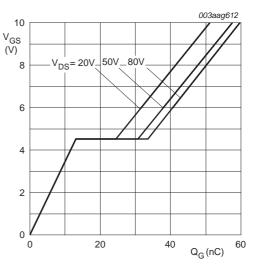


Fig 16. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values



 $T_i = 25^{\circ}C; I_D = 10A$

Fig 15. Gate-source voltage as a function of gate charge; typical values

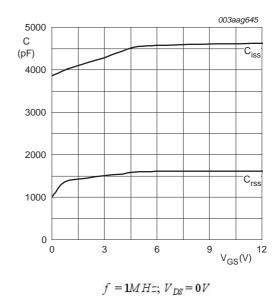
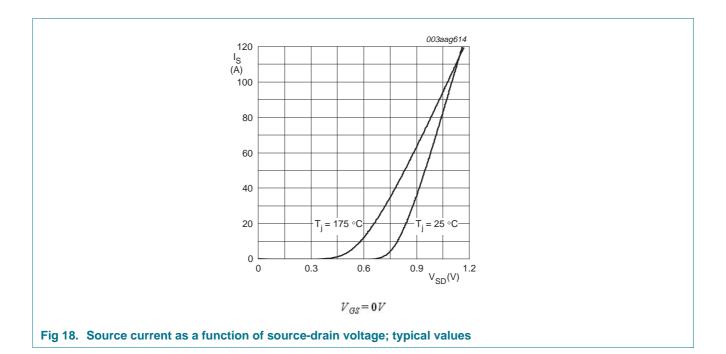


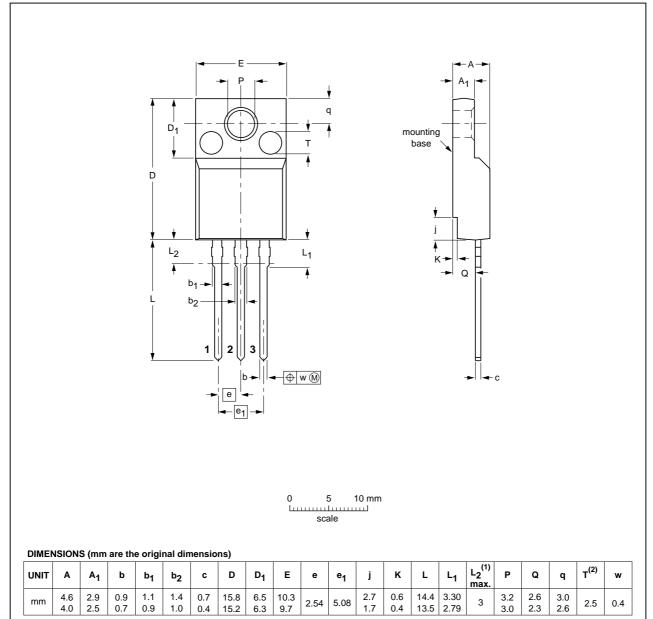
Fig 17. Input and reverse transfer capacitances as a function of gate-source voltage, typical values



8. Package outline

Plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220 'full pack'

SOT186A



Notes

- 1. Terminal dimensions within this zone are uncontrolled.
- 2. Both recesses are \varnothing 2.5 \times 0.8 max. depth

	OUTLINE		REFER	ENCES	EUROPEAN ISSUE DATE		
	VERSION	IEC	JEDEC	JEITA	PROJECTION	1330E DATE	
	SOT186A		3-lead TO-220F			-02-04-09- 06-02-14	
_							

Fig 19. Package outline SOT186A (TO-220F)

PSMN013-100XS

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9. Revision history

Table 8. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes		
PSMN013-100XS v.2	20120306	Product data sheet	-	PSMN013-100XS v.1		
Modifications:	Status changed from preliminary to product.					
	 Various changes to content. 					
PSMN013-100XS v.1	20111213	Preliminary data sheet	-	-		

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10.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

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N-channel 100V 13 mΩ standard level MOSFET in TO220F (SOT186A)

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