

HSD88

Silicon Schottky Barrier Diode for Various Detector, Mixer

HITACHI

ADE-208-1386 (Z)

Rev.0
Jun. 2001

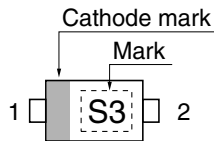
Features

- Low capacitance. ($C = 0.8 \text{ pF max}$)
- Low forward voltage.
- Super small Flat Package (SFP) is suitable for surface mount design.

Ordering Information

Type No.	Laser Mark	Package Code
HSD88	S3	SFP

Outline



1. Cathode
2. Anode

Absolute Maximum Ratings

($T_a = 25^\circ\text{C}$)

Item	Symbol	Value	Unit
Reverse voltage	V_R	10	V
Average rectified current	I_o	15	mA
Junction temperature	T_j	125	$^\circ\text{C}$
Storage temperature	T_{stg}	-55 to +125	$^\circ\text{C}$

Electrical Characteristics

($T_a = 25^\circ\text{C}$)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Forward voltage	V_{F1}	350	—	420	mV	$I_F = 1 \text{ mA}$
	V_{F2}	500	—	580		$I_F = 10 \text{ mA}$
Reverse current	I_{R1}	—	—	0.2	μA	$V_R = 2 \text{ V}$
	I_{R2}	—	—	10		$V_R = 10 \text{ V}$
Capacitance	C	—	—	0.8	pF	$V_R = 0 \text{ V}$, $f = 1 \text{ MHz}$
ESD-Capability * ¹	—	30	—	—	V	C = 200 pF, R = 0 Ω , Both forward and reverse direction 1 pulse.

Notes: 1. Failure criterion ; $I_R > 0.4 \mu\text{A}$ at $V_R = 2 \text{ V}$

2. Please do not use the soldering iron due to avoid high stress to the SFP package.

Main Characteristic

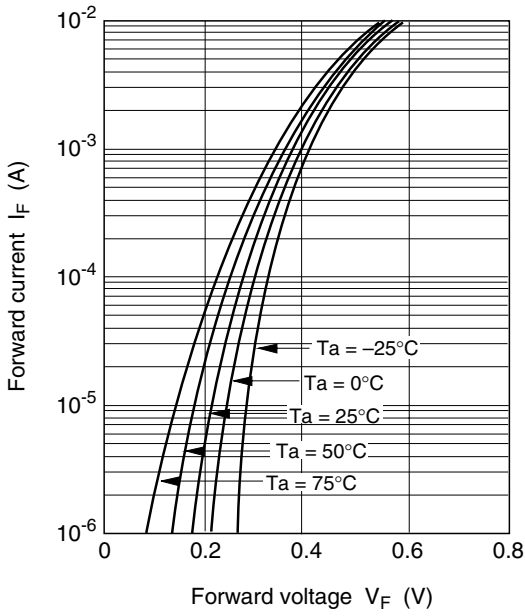


Fig.1 Forward current vs. Forward voltage

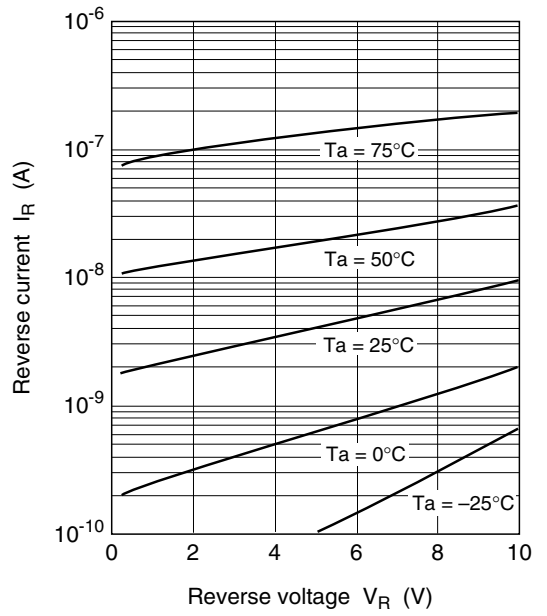


Fig.2 Reverse current vs. Reverse voltage

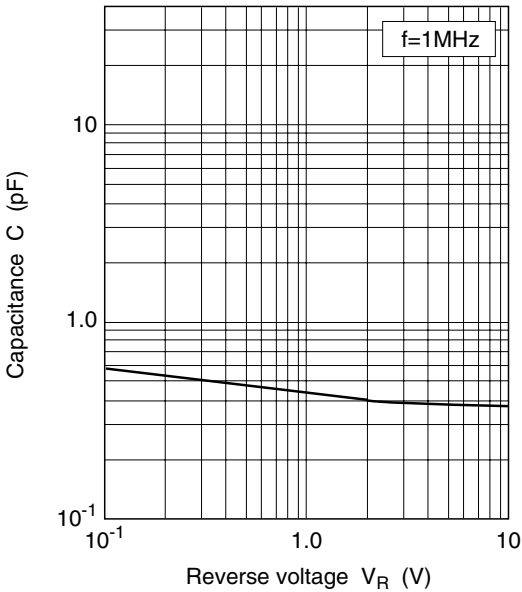
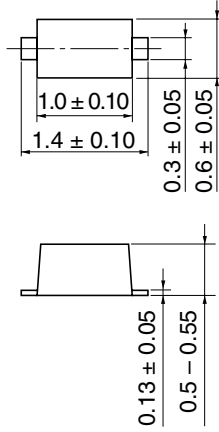


Fig.3 Capacitance vs. Reverse voltage

Package Dimensions

As of January, 2001
Unit: mm



Hitachi Code	SFP
JEDEC	—
EIAJ	—
Mass (reference value)	0.0010 g

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Sales Offices

HITACHI

Hitachi, Ltd.

Semiconductor & Integrated Circuits
Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan
Tel: (03) 3270-2111 Fax: (03) 3270-5109

URL	NorthAmerica	: http://semiconductor.hitachi.com/
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For further information write to:

Hitachi Semiconductor (America) Inc. 179 East Tasman Drive San Jose, CA 95134 Tel: <1>(408) 433-1990 Fax: <1>(408) 433-0223	Hitachi Europe Ltd. Electronic Components Group Whitebrook Park Lower Cookham Road Maidenhead Berkshire SL6 8YA, United Kingdom Tel: <44> (1628) 585000 Fax: <44> (1628) 585200
	Hitachi Europe GmbH Electronic Components Group Domacher StraÙe 3 D-85622 Feldkirchen, Munich Germany Tel: <49> (89) 9 9180-0 Fax: <49> (89) 9 29 30 00

Hitachi Asia Ltd. Hitachi Tower 16 Collyer Quay #20-00 Singapore 049318 Tel: <65>-538-6533/538-8577 Fax: <65>-538-6933/538-3877 URL: http://www.hitachi.com.sg	Hitachi Asia Ltd. (Taipei Branch Office) 4/F, No. 167, Tun Hwa North Road Hung-Kuo Building Taipei (105), Taiwan Tel: <886>-(2)-2718-3666 Fax: <886>-(2)-2718-8180 Telex: 23222 HAS-TP URL: http://www.hitachi.com.tw
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Hitachi Asia (Hong Kong) Ltd. Group III (Electronic Components) 7/F., North Tower World Finance Centre, Harbour City, Canton Road Tsim Sha Tsui, Kowloon Hong Kong Tel: <852>-(2)-735-9218 Fax: <852>-(2)-730-0281 URL: http://semiconductor.hitachi.com.hk
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