SURFACE MOUNT SILICON N-CHANNEL AND P-CHANNEL ENHANCEMENT-MODE COMPLEMENTARY MOSFET



### SOT-563 CASE

### **APPLICATIONS:**

- Load/Power switches
- · Power supply converter circuits
- · Battery powered portable devices

# MAXIMUM RATINGS: (T<sub>A</sub>=25°C)

Drain-Source Voltage

Gate-Source Voltage

Continuous Drain Current (Steady State)

Maximum Pulsed Drain Current (tp=10µs)

Power Dissipation (Note 1)

Power Dissipation (Note 2)

Power Dissipation (Note 3)

Operating and Storage Junction Temperature

Thermal Resistance (Note 1)

<b>Central</b>
Semiconductor Corp.

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## **DESCRIPTION:**

The CENTRAL SEMICONDUCTOR CMLDM3757 consists of complementary silicon N-Channel and P-Channel enhancement-mode MOSFETs designed for high speed pulsed amplifier and driver applications. These MOSFETs offer very low rDS(ON) and low threshold voltage.

### **MARKING CODE: 3C7**

#### **FEATURES:**

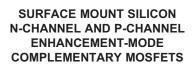
- ESD protection up to 1800V (Human Body Model)
- 350mW power dissipation
- Very low rDS(ON)
- · Low threshold voltage
- · Logic level compatible
- Small, SOT-563 surface mount package

SYMBOL	N-CH (Q1)	P-CH (Q2)	UNITS
$V_{DS}$	2	20	V
$V_{GS}$	8	.0	V
$I_{D}$	540	430	mA
$I_{DM}$	1500	750	mA
$P_{D}$	3	50	mW
$P_{D}$	3	00	mW
$P_{D}$	1	50	mW
T <sub>J</sub> , T <sub>stg</sub>	-65 to	+150	°C
$\Theta_{JA}$	3	57	°C/W

<b>ELECTRICAL CHARACTERISTICS</b> : (T <sub>A</sub> =25°C)		N-CH (Q1)			<u>P</u> -	P-CH (Q2)		
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	MIN	TYP	MAX	UNITS
IGSSF, IGSSR	$V_{GS}$ =4.5V, $V_{DS}$ =0	-	-	5.0	-	-	2.0	μΑ
IDSS	$V_{DS}$ =16V, $V_{GS}$ =0	-	-	1.0	-	-	1.0	μΑ
BV <sub>DSS</sub>	$V_{GS}=0, I_{D}=250\mu A$	20	-	-	20	-	-	V
V <sub>GS(th)</sub>	$V_{DS}=V_{GS}$ , $I_{D}=250\mu A$	0.45	-	1.0	0.45	-	1.0	V
$V_{SD}$	$V_{GS}$ =0, $I_{S}$ =350mA	-	-	1.2	-	-	1.2	V
rDS(ON)	$V_{GS}$ =4.5V, $I_D$ =540mA	-	0.35	0.55	-	-	-	Ω
rDS(ON)	$V_{GS}$ =4.5V, $I_D$ =430mA	-	-	-	-	0.4	0.9	Ω
rDS(ON)	$V_{GS}$ =2.5V, $I_D$ =500mA	-	0.5	0.7	-	-	-	Ω
rDS(ON)	$V_{GS}$ =2.5V, $I_D$ =300mA	-	-	-	-	0.55	1.2	Ω
rDS(ON)	$V_{GS}$ =1.8V, $I_D$ =350mA	-	0.7	0.9	-	-	-	Ω
rDS(ON)	$V_{GS}$ =1.8V, $I_D$ =150mA	-	-	-	-	0.75	2.0	Ω

Notes: (1) Ceramic or aluminum core PC Board with copper mounting pad area of 4.0mm²

- (2) FR-4 Epoxy PC Board with copper mounting pad area of 4.0mm<sup>2</sup>
- (3) FR-4 Epoxy PC Board with copper mounting pad area of 1.4mm<sup>2</sup>

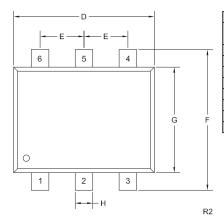




ELECTRICA	L CHARACTERISTICS - Continued: (T <sub>A</sub> =25°C)	N-CH	I (Q1)	P-CH	(Q2)	
SYMBOL	TEST CONDITIONS	TYP	MAX	TYP	MAX	UNITS
C <sub>rss</sub>	$V_{DS}$ =16V, $V_{GS}$ =0, f=1.0MHz	-	20	-	20	pF
C <sub>iss</sub>	$V_{DS}$ =16V, $V_{GS}$ =0, f=1.0MHz	-	150	-	175	pF
Coss	$V_{DS}$ =16V, $V_{GS}$ =0, f=1.0MHz	-	25	-	30	pF
Q <sub>g(tot)</sub>	$V_{DS}$ =10V, $V_{GS}$ =4.5V, $I_{D}$ =500mA	1.58	-	-	-	nC
Q <sub>g(tot)</sub>	$V_{DS}$ =10V, $V_{GS}$ =4.5V, $I_{D}$ =200mA	-	-	1.2	-	nC
Qgs	$V_{DS}$ =10V, $V_{GS}$ =4.5V, $I_{D}$ =500mA	0.17	-	-	-	nC
$Q_{gs}$	$V_{DS}$ =10V, $V_{GS}$ =4.5V, $I_{D}$ =200mA	-	-	0.24	-	nC
$Q_{gd}$	$V_{DS}$ =10V, $V_{GS}$ =4.5V, $I_{D}$ =500mA	0.24	-	-	-	nC
$Q_{qd}$	$V_{DS}$ =10V, $V_{GS}$ =4.5V, $I_{D}$ =200mA	-	-	0.36	-	nC
ton	$V_{DD}$ =10V, $V_{GS}$ =4.5V, $I_{D}$ =540mA, $R_{G}$ =10 $\Omega$	10	-	-	-	ns
<sup>t</sup> off	$V_{DD}$ =10V, $V_{GS}$ =4.5V, $I_{D}$ =540mA, $R_{G}$ =10 $\Omega$	25	-	-	-	ns
ton	$V_{DD}$ =10V, $V_{GS}$ =4.5V, $I_{D}$ =215mA, $R_{G}$ =10 $\Omega$	-	-	38	-	ns
t <sub>off</sub>	$V_{DD}$ =10V, $V_{GS}$ =4.5V, $I_{D}$ =215mA, $R_{G}$ =10 $\Omega$	-	-	48	-	ns

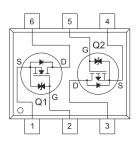
## **SOT-563 CASE - MECHANICAL OUTLINE**





DIMENSIONS							
	INC	HES	MILLIM	ETERS			
SYMBOL	MIN	MAX	MIN	MAX			
Α	0.0027	0.007	0.07	0.18			
В	0.0	800	0.20				
С	0.017	0.024	0.45	0.60			
D	0.059	0.067	1.50	1.70			
E	0.0	20	0.	50			
F	0.059	0.067	1.50	1.70			
G	0.043	0.051	1.10	1.30			
Н	0.006	0.012	0.15	0.30			
SOT-563 (REV: R2)							

## PIN CONFIGURATION



## LEAD CODE:

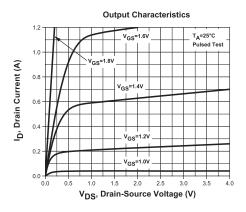
- 1) Source Q1
- 2) Gate Q1
- 3) Drain Q2
- 4) Source Q2
- 5) Gate Q2 6) Drain Q1
- MARKING CODE: 3C7

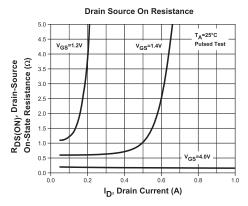
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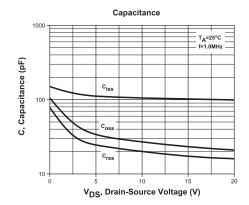


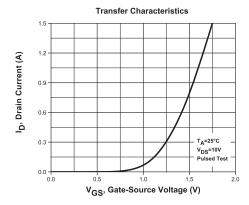
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### N-CHANNEL TYPICAL ELECTRICAL CHARACTERISTICS





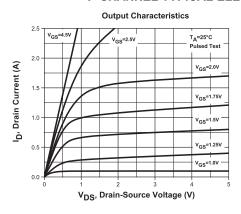


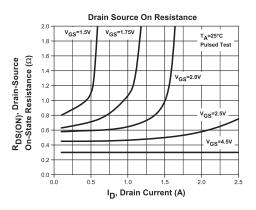


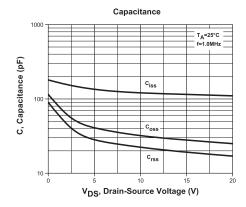
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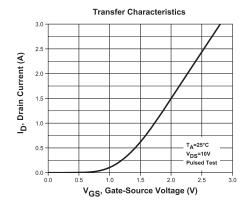


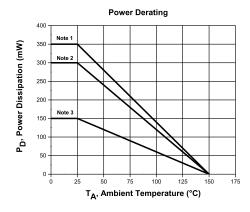
### P-CHANNEL TYPICAL ELECTRICAL CHARACTERISTICS



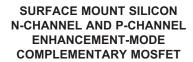








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