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April 1st, 2010 Renesas Electronics Corporation

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RENESAS

MOS FIELD EFFECT TRANSISTOR 2SK3812

SWITCHING N-CHANNEL POWER MOS FET

DESCRIPTION

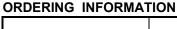
The 2SK3812 is N-channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

- Super low on-state resistance
- $R_{DS(on)1}$ = 2.8 m Ω MAX. (V_{GS} = 10 V, I_D = 55 A)
- $R_{DS(on)2}$ = 3.7 m Ω MAX. (V_{GS} = 4.5 V, I_D = 55 A)
- High current rating: ID(DC) = ±110 A

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Drain to Source Voltage (VGS = 0 V)	VDSS	60	V
Gate to Source Voltage (VDS = 0 V)	Vgss	±20	V
Drain Current (DC) (Tc = 25°C)	ID(DC)	±110	Α
Drain Current (pulse) Note1	D(pulse)	±440	А
Total Power Dissipation (Tc = 25°C)	Pt1	213	W
Total Power Dissipation (T _A = 25°C)	Pt2	1.5	W
Channel Temperature	Tch	150	°C
Storage Temperature	Tstg	–55 to +150	°C
Single Avalanche Energy Note2	Eas	397	mJ
Repetitive Avalanche Current Note3	lar	63	Α
Repetitive Avalanche Energy Note3	Ear	397	mJ



PART NUMBER	PACKAGE
2SK3812-ZP	TO-263 (MP-25ZP)

(TO-263)



Notes 1. PW \leq 10 μ s, Duty Cycle \leq 1%

- **2.** Starting T_{ch} = 25°C, V_{DD} = 30 V, R_G = 25 Ω , V_{GS} = 20 \rightarrow 0 V, L = 100 μ H
- **3.** $T_{ch(peak)} \le 150^{\circ}C$, RG = 25 Ω

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ELECTRICAL CHARACTERISTICS (T_A = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Zero Gate Voltage Drain Current	IDSS	V _{DS} = 60 V, V _{GS} = 0 V			10	μA
Gate Leakage Current	lgss	V _{GS} = ±20 V, V _{DS} = 0 V			±100	nA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	1.5	2.0	2.5	V
Forward Transfer Admittance Note	y fs	V _{DS} = 10 V, I _D = 55 A	50	110		S
Drain to Source On-state Resistance Note	RDS(on)1	V _{GS} = 10 V, I _D = 55 A		2.3	2.8	mΩ
	RDS(on)2	V _{GS} = 4.5 V, I _D = 55 A		2.6	3.7	mΩ
Input Capacitance	Ciss	V _{DS} = 10 V		16800		pF
Output Capacitance	Coss	V _{GS} = 0 V		1600		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		1000		pF
Turn-on Delay Time	td(on)	V _{DD} = 30 V, I _D = 55 A		42		ns
Rise Time	tr	V _{GS} = 10 V		160		ns
Turn-off Delay Time	td(off)	R _G = 0 Ω		140		ns
Fall Time	tr			15		ns
Total Gate Charge	QG	V _{DD} = 48 V		250		nC
Gate to Source Charge	QGS	V _{GS} = 10 V		41		nC
Gate to Drain Charge	Qgd	I _D = 110 A		66		nC
Body Diode Forward Voltage Note	VF(S-D)	I⊧ = 110 A, V₀s = 0 V		0.87	1.5	V
Reverse Recovery Time	trr	I⊧ = 110 A, V₀s = 0 V		53		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		74		nC

Note Pulsed

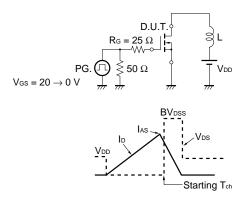
TEST CIRCUIT 1 AVALANCHE CAPABILITY

TEST CIRCUIT 2 SWITCHING TIME

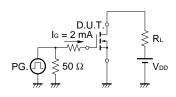
5

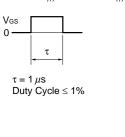
D.U.T.

∕W∕⊸∘ Rg

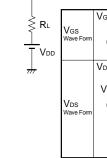


TEST CIRCUIT 3 GATE CHARGE



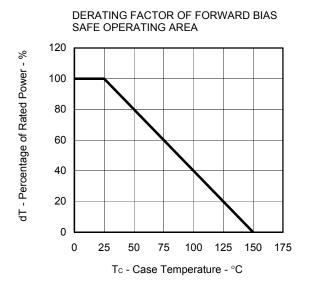


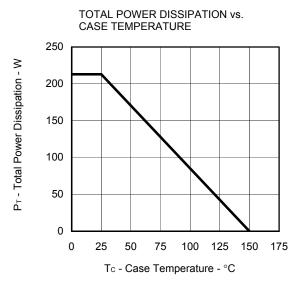
PG.



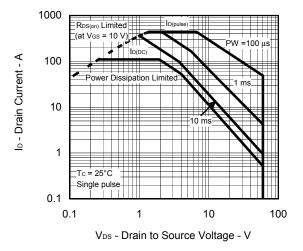
VGS Wave Form	Vgs 0 <u>10%</u> 7	[]v	GS	- 90%
VDS Wave Form	VDS VDS 0 td(on)	tr ton	10% td(off)	190%

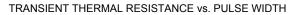
TYPICAL CHARACTERISTICS ($T_A = 25^{\circ}C$)

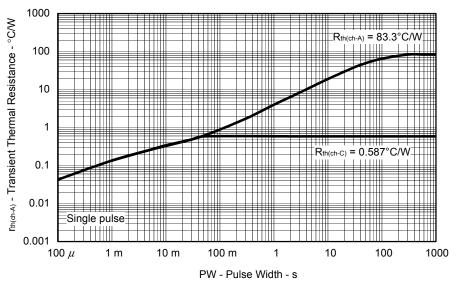




FORWARD BIAS SAFE OPERATING AREA







3.0

2.5

2.0

1.5

1.0

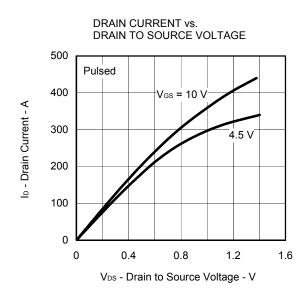
0.5

0

-75

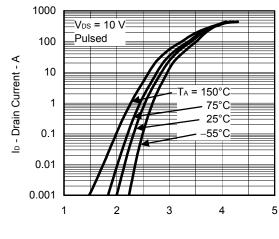
-25

V_{GS(off)} - Gate Cut-off Voltage - V



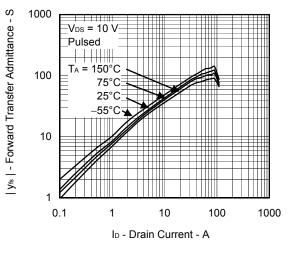
GATE CUT-OFF VOLTAGE vs.

CHANNEL TEMPERATURE



 $V_{\mbox{\scriptsize GS}}$ - Gate to Source Voltage - V

FORWARD TRANSFER ADMITTANCE vs. DRAIN CURRENT

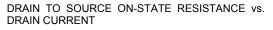


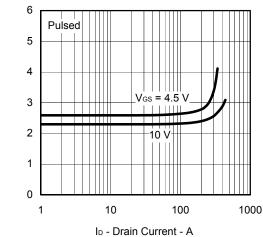


V_{DS} = 10 V

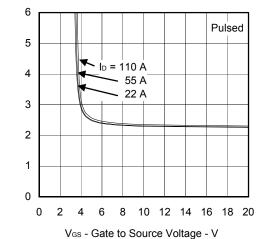
Tch - Channel Temperature - °C

25





DRAIN TO SOURCE ON-STATE RESISTANCE vs. GATE TO SOURCE VOLTAGE



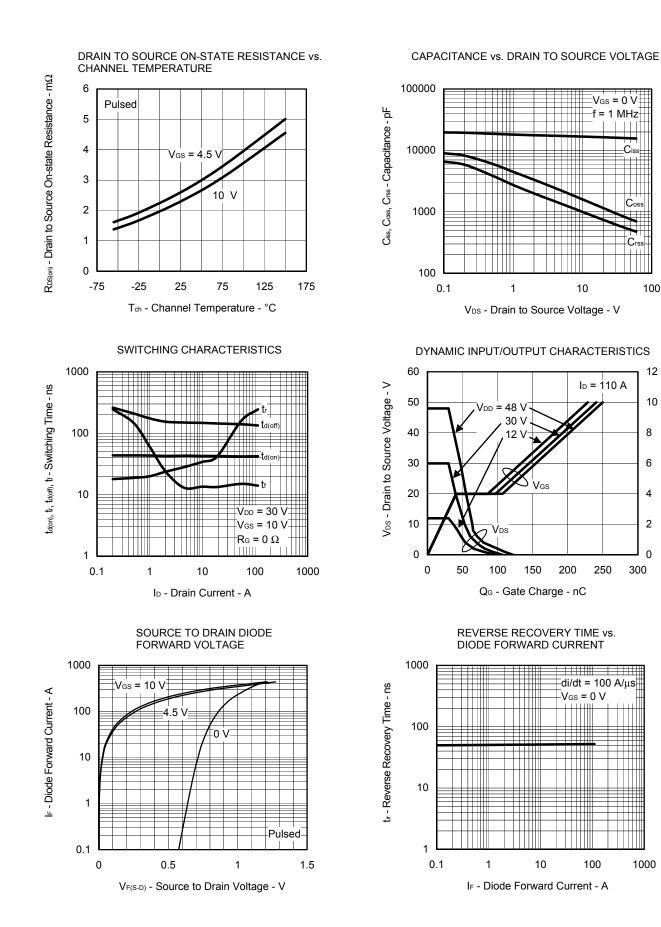
FORWARD TRANSFER CHARACTERISTICS

 $R_{DS(m)}$ - Drain to Source On-state Resistance - $m\Omega$

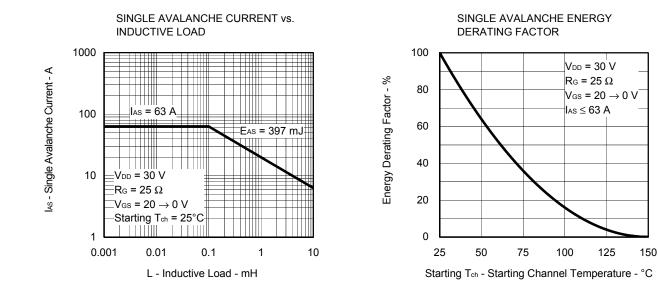
 $R_{DS(m)}$ - Drain to Source On-state Resistance - $m\Omega$

n

V_{GS} - Gate to Source Voltage - V



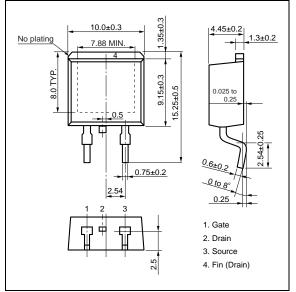
Data Sheet D16738EJ1V0DS



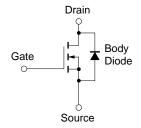
NEC

PACKAGE DRAWING (Unit: mm)

TO-263 (MP-25ZP)



EQUIVALENT CIRCUIT



Remark Strong electric field, when exposed to this device, can cause destruction of the gate oxide and ultimately degrade the device operation. Steps must be taken to stop generation of static electricity as much as possible, and quickly dissipate it once, when it has occurred.

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