TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

# TC7SG00FE

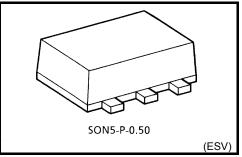
## 2-Input NAND Gate

#### Features

- High output current : ±8 mA (min) at V<sub>CC</sub> = 3.0 V
- Super high speed operation : t<sub>pd</sub> = 2.5 ns (typ.)

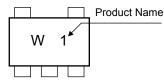
at V<sub>CC</sub> = 3.3 V,15pF nge : V<sub>CC</sub> = 0.9 to 3.6 V

- Operating voltage range
- 5.5-V tolerant inputs.
- 3.6-V power down protection output.

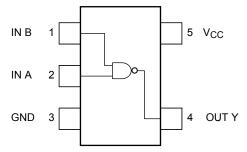


#### Weight: 0.003 g (typ.)

#### Marking



## Pin Assignment (top view)



## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage	V <sub>CC</sub>	-0.5 to 4.6	V
DC input voltage	V <sub>IN</sub>	-0.5 to 7.0	V
DC output voltage	Vaur	-0.5 to 4.6 (Note 1)	V
	Vout	–0.5 to $V_{CC}$ +0.5 (Note 2)	
Input diode current	I <sub>IK</sub>	-20	mA
Output diode current	I <sub>OK</sub>	-20 (Note 3)	mA
DC output current	IOUT	±25	mA
DC V <sub>CC</sub> /ground current	ICC	±50	mA
Power dissipation	PD	150	mW
Storage temperature	T <sub>stg</sub>	−65 to 150	°C

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: 
$$V_{CC} = 0V$$

Note 2: High or Low state. Do not exceed  $I_{\mbox{OUT}}$  of absolute maximum ratings.

Note 3: V<sub>OUT</sub> < GND

Start of commercial production 2005-02

# <u>TOSHIBA</u>

## IEC Logic Symbol



Truth	Table

А	В	Y
L	L	Н
L	Н	Н
Н	L	Н
Н	Н	L

## **Operating Ranges**

Characteristics	Symbol	Rating	Unit	
Supply voltage	V <sub>CC</sub>	0.9 to 3.6	V	
Input voltage	V <sub>IN</sub>	0 to 5.5	V	
Output voltage		0 to 3.6 (Note 4)	V	
	Vout	0 to V <sub>CC</sub> (Note 5)	v	
Output Current		± 8.0 (Note 6)		
		± 4.0 (Note 7)		
	1 /1	± 3.0 (Note 8)		
	IOH/IOL	± 1.7 (Note 9)	mA	
		± 0.3 (Note 10)		
		± 0.02 (Note 11)		
Operating temperature	T <sub>opr</sub>	-40 to 85	°C	
Input rise and fall time	dt/dv	0 to 10 (Note 12)	ns/V	

## **Electrical Characteristics**

## **DC Characteristics**

Characteristics	Symbol	Test Condition			Ta = 25°C			Ta = -40 to 85°C		Unit
Characteristics	Symbol	Test	Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Onit
				0.9	V <sub>CC</sub>		_	V <sub>CC</sub>		
				1.1 to 1.3	$V_{CC} \times 0.7$		_	$V_{CC} \times 0.7$		
High-level input voltage	VIH			1.4 to 1.6	V <sub>CC</sub> × 0.65	_	_	V <sub>CC</sub> × 0.65		v
vollage					V <sub>CC</sub> × 0.65	_	_	V <sub>CC</sub> × 0.65		
				2.3 to 2.7	1.7		_	1.7		
				3.0 to 3.6	2.0	_	_	2.0	_	
				0.9	_		GND	_	GND	
				1.1 to 1.3	_		$\begin{array}{c} V_{CC} \\ \times \ 0.3 \end{array}$	_	$V_{CC} \times 0.3$	
Low-level input	VIL		_	1.4 to 1.6	_		V <sub>CC</sub> × 0.35	_	V <sub>CC</sub> × 0.35	v
voltage				1.65 to 1.95	_	_	V <sub>CC</sub> × 0.35	_	V <sub>CC</sub> × 0.35	
				2.3 to 2.7	_	_	0.7	_	0.7	
				3.0 to 3.6	_	_	0.8	_	0.8	
		VIN = VIH or VIL	I <sub>OH</sub> =-0.02 mA	0.9	0.75	_	_	0.75	_	V
			I <sub>OH</sub> = -0.3 mA	1.1 to 1.3	V <sub>CC</sub> × 0.75		_	V <sub>CC</sub> × 0.75		
High-level output	V <sub>OH</sub>		I <sub>OH</sub> = -1.7 mA	1.4 to 1.6	V <sub>CC</sub> × 0.75	_	_	V <sub>CC</sub> × 0.75	_	
voltage			I <sub>OH</sub> = -3.0 mA	1.65 to 1.95	V <sub>CC</sub> -0.45		_	V <sub>CC</sub> -0.45		
			I <sub>OH</sub> = -4.0 mA	2.3 to 2.7	2.0	_	_	2.0	_	
			I <sub>OH</sub> = -8.0 mA	3.0 to 3.6	2.48	_		2.48	_	
		V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OL</sub> = 0.02 mA	0.9	_		0.1	_	0.1	V
			I <sub>OL</sub> = 0.3 mA	1.1 to 1.3	_	_	V <sub>CC</sub> × 0.25		$\begin{array}{c} V_{CC} \\ \times \ 0.25 \end{array}$	
Low-level output voltage	V <sub>OL</sub>		I <sub>OL</sub> = 1.7 mA	1.4 to 1.6			V <sub>CC</sub> × 0.25	_	V <sub>CC</sub> × 0.25	
			I <sub>OL</sub> = 3.0 mA	1.65 to 1.95	—	_	0.45	—	0.45	
			I <sub>OL</sub> = 4.0 mA	2.3 to 2.7	_	_	0.4	_	0.4	
			I <sub>OL</sub> = 8.0 mA	3.0 to 3.6	_	_	0.4	_	0.4	
Input leakage current	I <sub>IN</sub>	V <sub>IN</sub> = 0 to 5.5V		0 to 3.6	_	_	±0.1	_	±1.0	μA
Power off leakage current	I <sub>OFF</sub>		V <sub>IN</sub> = 0 to 5.5V V <sub>OUT</sub> = 0 to 3.6V		_		1.0		10.0	μA
Quiescent supply current	Icc	V <sub>IN</sub> = V <sub>CC</sub>	$V_{IN} = V_{CC}$ or GND				1.0		10.0	μΑ

## AC Electrical Characteristics (unless otherwise specified, Input $t_r = t_f = 3$ ns)

Characteristics	Symbol	Test Canditian		Ta = 25°C			Ta = -40 to 85°C		Linit
Characteristics		Test Condition	V <sub>CC</sub> (V)	Min	Тур.	Max	Min	Max	Unit
		C <sub>L</sub> = 10 pF,	0.9	_	26.9	_	_	—	ns
			1.1 to 1.3		10.9	20.7	1.0	38.6	
			1.4 to 1.6		5.9	9.6	1.0	11.3	
		$R_{L} = 1 M\Omega$	1.65 to 1.95		4.5	7.0	1.0	7.5	
			2.3 to 2.7		2.9	4.4	1.0	4.9	
			3.0 to 3.6		2.2	3.5	1.0	4.1	
	tplh tphl		0.9		30.0	_	_	—	
		$C_L = 15 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	1.1 to 1.3		12.0	24.2	1.0	42.0	
Drana setien delevitime			1.4 to 1.6		6.5	10.5	1.0	12.6	
Propagation delay time			1.65 to 1.95		5.0	7.7	1.0	8.0	
			2.3 to 2.7		3.2	4.9	1.0	5.6	
			3.0 to 3.6		2.5	3.8	1.0	4.4	
		$C_L = 30 \text{ pF},$ $R_L = 1 \text{ M}\Omega$	0.9		45.0	_	_	—	
			1.1 to 1.3		18.0	33.4	1.0	63.2	
			1.4 to 1.6		8.9	14.8	1.0	17.9	
			1.65 to 1.95		6.9	10.3	1.0	10.8	
			2.3 to 2.7		4.4	6.4	1.0	6.8	
			3.0 to 3.6		3.5	4.9	1.0	5.4	
Input capacitance	C <sub>IN</sub>	—	3.6	_	3			—	pF
Power dissipation capacitance	C <sub>PD</sub>	(Note 13)	0.9 to 3.6		6	_	_	—	pF

Note 13: C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

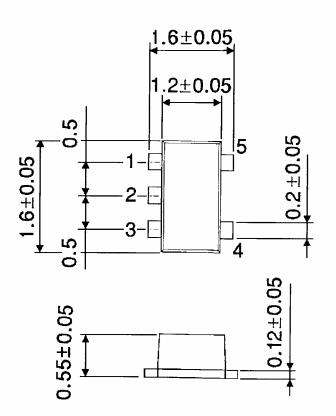
 $I_{CC (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$ 

## **TOSHIBA**

## Package Dimensions

SON5-P-0.50

Unit : mm



Weight: 0.003 g (typ.)

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