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April 1st, 2010 Renesas Electronics Corporation

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RD74LVC04B

Hex Inverters

REJ03D0217-0100Z Rev.1.00 Apr.08.2004

Description

The RD74LVC04B has six inverters in a 14-pin package. Low voltage and high-speed operation is suitable at the battery drive product (note type personal computer) and low power consumption extends the life of a battery for long time operation.

Features

- $V_{CC} = 1.65 \text{ V to } 5.5 \text{ V}$
- All inputs V_{IH} (Max.) = 5.5 V (@ V_{CC} = 0 V to 5.5 V)
- Typical V_{OL} ground bounce < 0.8 V (@ V_{CC} = 3.3 V, Ta = 25°C)
- Typical V_{OH} undershoot > 2.0 V (@ V_{CC} = 3.3 V, Ta = 25°C)
- High output current ± 4 mA (@V_{CC} = 1.65 V) ± 8 mA (@V_{CC} = 2.3 V) ± 12 mA (@V_{CC} = 2.7 V) ± 24 mA (@V_{CC} = 3.0 V to 5.5 V)
- Ordering Information

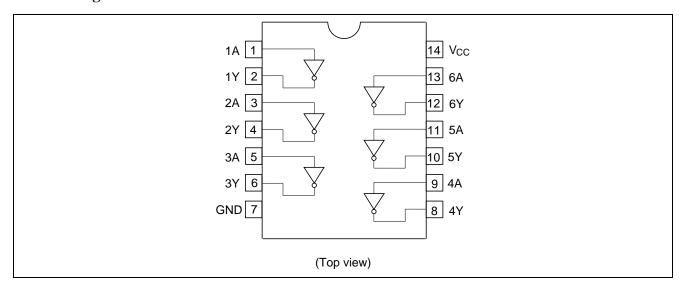
Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
RD74LVC04BFPEL	SOP-14 pin (JEITA)	FP-14DAV	FP	EL (2,000 pcs/reel)
RD74LVC04BTELL	TSSOP-14 pin	TTP-14DV	Т	ELL (2,000 pcs/reel)

Function Table

Input A	Output Y
Н	L
L	Н

H: High level L: Low level

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol Ratings Unit		Unit	Conditions
Supply voltage range	Vcc	-0.5 to 7.0	V	
Input diode current	I _{IK}	- 50	mA	$V_1 = -0.5 \text{ V}$
Input voltage	Vı	-0.5 to 7.0	V	
Output diode current	lok	- 50	mA	$V_0 = -0.5 \text{ V}$
		50	mA	$V_O = V_{CC} + 0.5 V$
Output voltage	Vo	-0.5 to V _{CC} +0.5	V	
Output current	lο	±50	mA	
V _{CC} , GND current / pin	I _{CC} or I _{GND}	100	mA	
Storage temperature	Tstg	-65 to +150	°C	

Note: The absolute maximum ratings are values, which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Conditions
Supply voltage	V _{cc}	1.5 to 5.5	V	Data hold
		1.65 to 5.5		At operation
Input / Output voltage	Vı	0 to 5.5	V	A
	Vo	0 to V _{CC}		Υ
Operating temperature	Та	-40 to 85	°C	
Output current	l _{OH}	-4	mA	V _{CC} = 1.65 V
		-8		$V_{CC} = 2.3 \text{ V}$
		- 12		$V_{CC} = 2.7 \text{ V}$
		-24		$V_{CC} = 3.0 \text{ V to } 5.5 \text{ V}$
	I _{OL}	4	mA	V _{CC} = 1.65 V
		8		$V_{CC} = 2.3 \text{ V}$
		12		$V_{CC} = 2.7 \text{ V}$
		24		$V_{CC} = 3.0 \text{ V to } 5.5 \text{ V}$
Input rise / fall time*1	t _r , t _f	20	ns/V	$V_{CC} = 1.65 \text{ V to } 2.7 \text{ V}$
		10		$V_{CC} = 3.0 \text{ V to } 5.5 \text{ V}$

Notes: 1. This item guarantees maximum limit when one input switches.

Waveform: Refer to test circuit of switching characteristics.

Electrical Characteristics

			$Ta = -40 \text{ to } 85^{\circ}C$			
Item	Symbol	V _{CC} (V)	Min	Max	Unit	Test Conditions
Input voltage	V _{IH}	1.65 to 1.95	V _{CC} ×0.65	_	V	
		2.3 to 2.7	1.7	_		
		2.7 to 3.6	2.0	_		
		4.5 to 5.5	V _{CC} ×0.7	_		
	V _{IL}	1.65 to 1.95	_	V _{CC} ×0.35	V	_
		2.3 to 2.7	_	0.7		
		2.7 to 3.6	_	0.8		
		4.5 to 5.5	_	V _{CC} ×0.3		
Output voltage	V _{OH}	1.65 to 5.5	V _{CC} -0.2	_	V	I _{OH} = -100 μA
		1.65	1.2	_		$I_{OH} = -4 \text{ mA}$
		2.3	1.7	_		$I_{OH} = -8 \text{ mA}$
		2.7	2.2	_		$I_{OH} = -12 \text{ mA}$
		3.0	2.4	_		
		3.0	2.2	_		$I_{OH} = -24 \text{ mA}$
		4.5	3.8	_		
	V _{OL}	1.65 to 5.5	_	0.2	V	I _{OL} = 100 μA
		1.65	_	0.45		I _{OL} = 4 mA
		2.3	_	0.7		$I_{OL} = 8 \text{ mA}$
		2.7	_	0.4		I _{OL} = 12 mA
		3.0	_	0.55		I _{OL} = 24 mA
		4.5	_	0.55		
Input current	I _{IN}	0 to 5.5	_	±5.0	μΑ	V _{IN} = 5.5 V or GND
Quiescent supply	Icc	2.7 to 3.6	_	±5.0	μΑ	V _{IN} = 3.6 V to 5.5 V
current		2.7 to 5.5	_	5.0		$V_{IN} = V_{CC}$ or GND
	ΔI_{CC}	2.7 to 3.6	_	500	μA	V_{IN} = one input at $(V_{CC}-0.6)V$, other inputs at V_{CC} or GND

Switching Characteristics

			Ta = -40 to 85°C				From	То
Item	Symbol	V _{CC} (V)	Min	Тур	Max	Unit	(Input)	(Output)
Propagation delay time	t _{PLH}	1.8±0.15	1.0	_	8.0	ns	Α	Υ
	t_{PHL}	2.5±0.2	1.0	_	7.5			
		2.7	1.0	_	5.5			
		3.3±0.3	1.0	_	4.5			
		5.0±0.5	1.0	_	4.0			
Between output pins skew*1	t _{OSLH}	1.8±0.15	_	_	_	ns		
	t_{OSHL}	2.5±0.2	_	_	_			
		2.7	_	_	_			
		3.3±0.3	_	_	1.0			
		5.0±0.5	_	_	1.0			
Input capacitance	C _{IN}	3.3	_	5.0	_	pF		

Note: 1. This parameter is characterized but not tested.

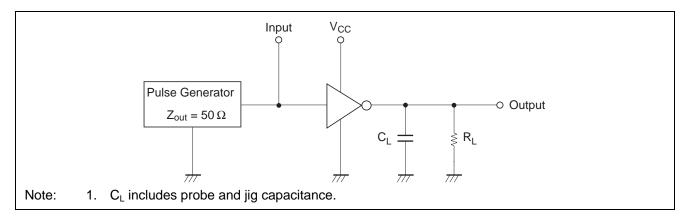
 $t_{\text{OSLH}} = | \; t_{\text{PLHm}} - t_{\text{PLHn}} |, \; t_{\text{OSHL}} = | \; t_{\text{PHLm}} - t_{\text{PHLn}} |$

Operating Characteristics

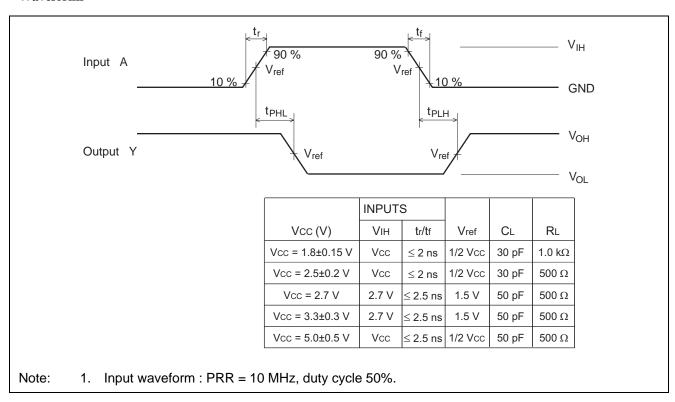
Та	=	25°	С

Item	Symbol	V _{CC} (V)	Min	Тур	Max	Unit	Test conditions
Power dissipation	C_PD	1.8	_	15	_	pF	f = 10 MHz
Capacitance		2.5	_	16	_		
		3.3	_	18	_		
		5.0	_	22	_		

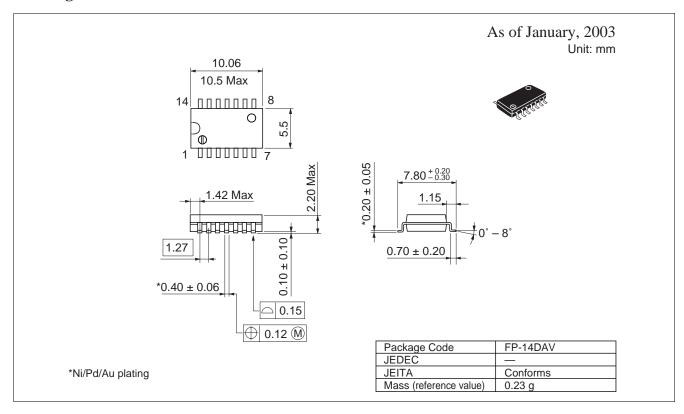
Test Circuit

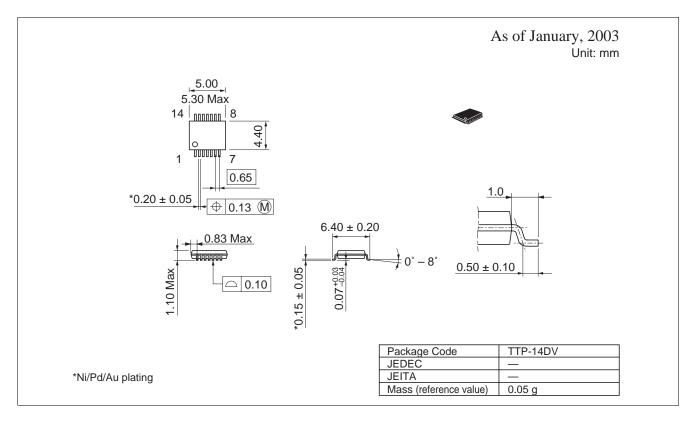


Waveforms



Package Dimensions





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Renesas Technology Europe GmbHDornacher Str. 3, D-85622 Feldkirchen, Germany
Tel: <49> (89) 380 70 0, Fax: <49> (89) 929 30 11

Renesas Technology Hong Kong Ltd. 7/F., North Tower, World Finance Centre, Harbour City, Canton Road, Hong Kong Tel: <852> 2265-6688, Fax: <852> 2375-6836

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Renesas Technology (Shanghai) Co., Ltd. 26/F., Ruijin Building, No.205 Maoming Road (S), Shanghai 200020, China Tel: <86> (21) 6472-1001, Fax: <86> (21) 6415-2952

Renesas Technology Singapore Pte. Ltd.
1, Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632 Tel: <65> 6213-0200, Fax: <65> 6278-8001