

66300

MICROCOUPLER, PHOTOTRANSISTOR OUTPUT
SIMILAR TO 4N55

03/06/03

Features:

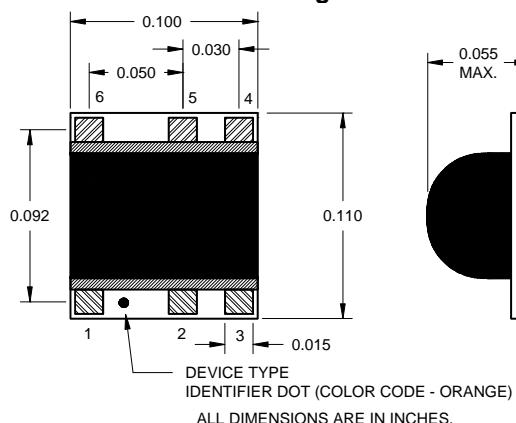
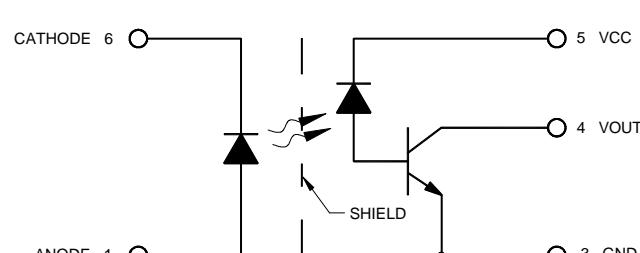
- 2MHz bandwidth typical
- Small size saves real estate
- Large thick film gold bond pads
- High common mode rejection

Applications:

- Eliminate ground loops
- Level shifting
- Line receiver
- Solid state switching
- Switching power supplies

DESCRIPTION

The 66300 microcoupler is a single channel optocoupler consisting of an LED optically coupled to a light sensitive high speed phototransistor. Each microcoupler is provided with full 100% DC testing (+125°C test option upon request) or 100% element evaluation. All microcouplers are capable of operating over the full military temperature range.

Package Dimensions**Schematic Diagram****ELECTRICAL CHARACTERISTICS**

Ta = -55°C to 125°C unless otherwise specified.

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS	TEST CONDITIONS	NOTE
Current Transfer Ratio	CTR	9	20		%	I_F = 16mA, V_O = 0.4V, V_CC = 4.5V	1
Logic High Output Current	I_OH		20	100	µA	I_F = 250µA, V_CC = V_O = 18V	
High Level Output Current	I_CCH		0.2	10	µA	I_F = 0, V_CC = 18V	
Low Level Supply Current	I_CCL		35	200	µA	I_F1 = I_F2 = 20mA, V_CC = 18V	
Input Forward Voltage	V_F		1.5	1.8	V	I_F = 20mA	
Input Reverse Breakdown Voltage	BVR	3			V	I_R = 10µA	
Propagation Delay Time To High Output Level	tPLH		2	6	µs	I_F = 16mA, V_CC = 5V, R_L = 8.2kΩ, C_L = 50pF	
Propagation Delay Time To Low Output Level	tPHL		0.4	2	µs	I_F = 16mA, V_CC = 5V, R_L = 8.2kΩ, C_L = 50pF	
Input Capacitance	C_IN		120		pF	V_F = 0, f = MHz	
Capacitance (Input-Output)	C_I-O		1.5		pF	f = 1MHz, V_F = 0	2
Resistance (Input-Output)	R_I-O		10 ¹²		Ω	V_I-O = 500Vdc	

NOTES:

1. CURRENT TRANSFER RATIO is defined as the ratio of output collector current, I_O, to the forward LED input current., I_F , times 100%.
2. Measured between input pins shorted together and the output pins for that channel shorted together.

RECOMMENDED OPERATING CONDITIONS:

PARAMETER	SYMBOL	MIN	MAX	UNITS
Input Current, Low Level	I_FL	0	2	µA
Supply Voltage	V_CC	2.0	18	V

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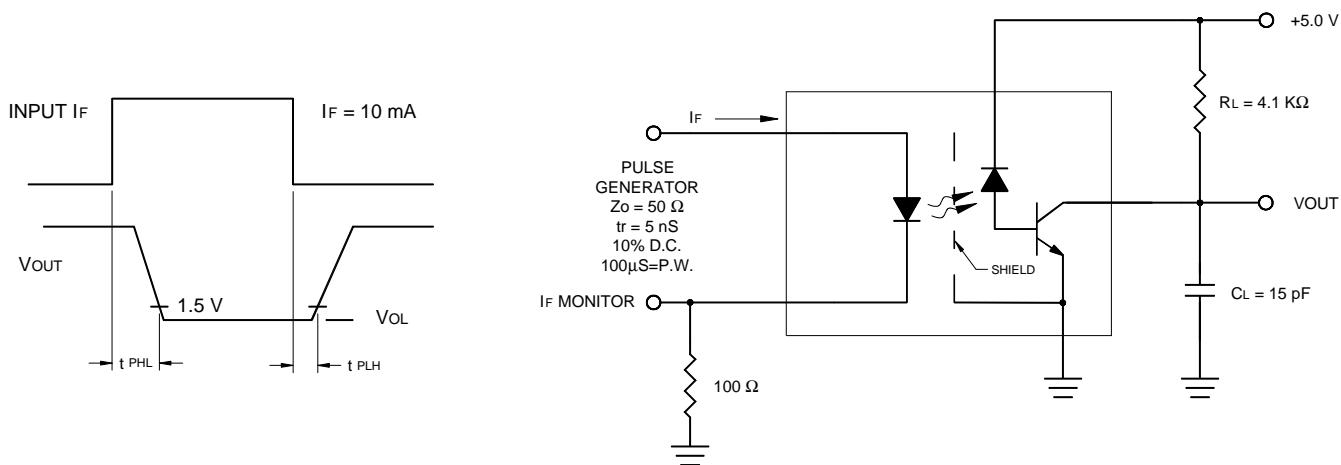


Figure 1. Switching Test Circuit