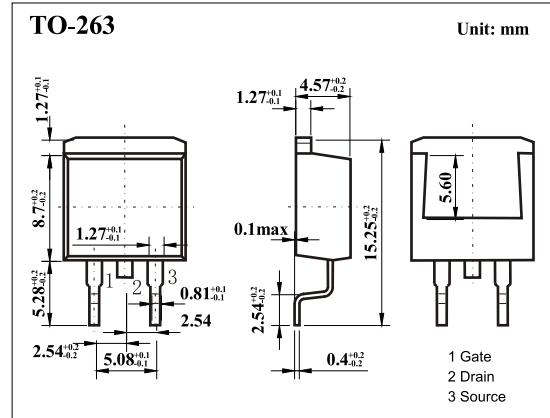
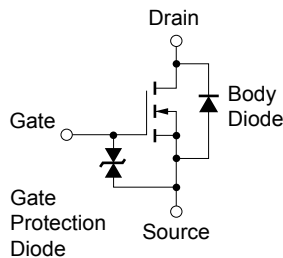


## N-Channel MOSFET

### 2SK3430-ZJ

#### ■ Features

- $V_{DS} = 40V$
- $I_D = 80 A$  ( $V_{GS} = 10V$ )
- $R_{DS(ON)} < 7.3m\Omega$  ( $V_{GS} = 10V$ )
- $R_{DS(ON)} < 15m\Omega$  ( $V_{GS} = 4V$ )
- Low Ciss: Ciss = 2800 pF TYP.



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	40	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	
Continuous Drain Current	$I_D$	80	A
Pulsed Drain Current (Note.1)	$I_{DM}$	200	
Single Avalanche Current (Note.2)	$I_{AS}$	37	
Power Dissipation	$P_D$	$T_c = 25^\circ C$	W
		$T_a = 25^\circ C$	
Single Avalanche Energy (Note.2)	$E_{AS}$	137	mJ
Thermal Resistance.Junction- to-Ambient	$R_{thJA}$	83.3	$^\circ C/W$
Thermal Resistance.Junction- to-Case	$R_{thJC}$	1.49	
Junction Temperature	$T_J$	150	$^\circ C$
Storage Temperature Range	$T_{stg}$	-55 to 150	

Note.1:  $PW \leq 10 \mu s$ , Duty Cycle  $\leq 1\%$

Note.2: Starting  $T_J = 25^\circ C$ ,  $V_{DD} = 150 V$ ,  $R_G = 25 \Omega$ ,  $V_{GS} = 20 V \rightarrow 0 V$

## N-Channel MOSFET

### 2SK3430-ZJ

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =250 μA, V <sub>GS</sub> =0V	40			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> =40V, V <sub>GS</sub> =0V			10	μA
Gate-Body Leakage Current	I <sub>GSS</sub>	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±10	μA
Gate to Source Cut-off Voltage	V <sub>GS(off)</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	1.5		2.5	V
Static Drain-Source On-Resistance	R <sub>DS(on)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =40A			7.3	mΩ
		V <sub>GS</sub> =4V, I <sub>D</sub> =40A			15	
Forward Transconductance	g <sub>FS</sub>	V <sub>DS</sub> =10V, I <sub>D</sub> =40A	20	40		S
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =10V, f=1MHz		2800		pF
Output Capacitance	C <sub>oss</sub>			730		
Reverse Transfer Capacitance	C <sub>rss</sub>			320		
Total Gate Charge	Q <sub>g</sub>	V <sub>GS</sub> =10V, V <sub>DS</sub> =32V, I <sub>D</sub> =80A		50		nC
Gate Source Charge	Q <sub>gs</sub>			10		
Gate Drain Charge	Q <sub>gd</sub>			14		
Turn-On DelayTime	t <sub>d(on)</sub>	V <sub>DD</sub> = 20V, I <sub>D</sub> = 40A, V <sub>GS(on)</sub> =10V, R <sub>G</sub> = 10 Ω		110		ns
Turn-On Rise Time	t <sub>r</sub>			1800		
Turn-Off DelayTime	t <sub>d(off)</sub>			170		
Turn-Off Fall Time	t <sub>f</sub>			350		
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 80A, V <sub>GS</sub> =0, di/dt= 100A/μs		50		nC
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			77		
Diode Forward Voltage	V <sub>SD</sub>	I <sub>F</sub> =80A, V <sub>GS</sub> =0V		1		V