## 2SA2028G

## Silicon PNP epitaxial planar type

For DC-DC converter

## Features

- Low collector-emitter saturation voltage $\mathrm{V}_{\mathrm{CE}(\mathrm{sat})}$
- High-speed switching
- S-Mini type package, allowing downsizing and thinning of the equipment and automatic insertion through the tape packing

Absolute Maximum Ratings $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}$

| Parameter | Symbol | Rating | Unit |
| :--- | :---: | :---: | :---: |
| Collector-base voltage (Emitter open) | $\mathrm{V}_{\mathrm{CBO}}$ | -20 | V |
| Collector-emitter voltage (Base open) | $\mathrm{V}_{\mathrm{CEO}}$ | -20 | V |
| Emitter-base voltage (Collector open) | $\mathrm{V}_{\mathrm{EBO}}$ | -5 | V |
| Collector current | $\mathrm{I}_{\mathrm{C}}$ | -1 | A |
| Peak collector current | $\mathrm{I}_{\mathrm{CP}}$ | -3 | A |
| Collector power dissipation | $\mathrm{P}_{\mathrm{C}}$ | 150 | mW |
| Junction temperature | $\mathrm{T}_{\mathrm{j}}$ | 150 | ${ }^{\circ} \mathrm{C}$ |
| Storage temperature | $\mathrm{T}_{\text {stg }}$ | -55 to +125 | ${ }^{\circ} \mathrm{C}$ |

Electrical Characteristics $\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C} \pm 3^{\circ} \mathrm{C}$

| Parameter | Symbol | Conditions | Min | Typ | Max |
| :--- | :---: | :--- | :---: | :---: | :---: |
| Collector-base voltage (Emitter open) | $\mathrm{V}_{\mathrm{CBO}}$ | $\mathrm{I}_{\mathrm{C}}=-10 \mu \mathrm{~A}, \mathrm{I}_{\mathrm{E}}=0$ | -20 |  |  |
| Collector-emitter voltage (Base open) | $\mathrm{V}_{\mathrm{CEO}}$ | $\mathrm{I}_{\mathrm{C}}=-1 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=0$ | -20 |  |  |
| Emitter-base voltage (Collector open) | $\mathrm{V}_{\mathrm{EBO}}$ | $\mathrm{I}_{\mathrm{E}}=-10 \mu \mathrm{~A}, \mathrm{I}_{\mathrm{C}}=0$ | -5 |  |  |
| Forward current transfer ratio | $\mathrm{h}_{\mathrm{FE}}$ | $\mathrm{V}_{\mathrm{CE}}=-2 \mathrm{~V}, \mathrm{I}_{\mathrm{C}}=-100 \mathrm{~mA}$ | 160 |  | 560 |
| Collector-emitter saturation voltage | $\mathrm{V}_{\mathrm{CE}(\text { sat })}$ | $\mathrm{I}_{\mathrm{C}}=-200 \mathrm{~mA}, \mathrm{I}_{\mathrm{B}}=-10 \mathrm{~mA}$ | - |  |  |
| Transition frequency | $\mathrm{f}_{\mathrm{T}}$ | $\mathrm{V}_{\mathrm{CB}}=-10 \mathrm{~V}, \mathrm{I}_{\mathrm{E}}=10 \mathrm{~mA}, \mathrm{f}=200 \mathrm{MHz}$ |  | 170 | -40 |
| Collector output capacitance <br> (Common base, input open circuited) | $\mathrm{C}_{\mathrm{ob}}$ | $\mathrm{V}_{\mathrm{CB}}=-10 \mathrm{~V}, \mathrm{I}_{\mathrm{E}}=0, \mathrm{f}=1 \mathrm{MHz}$ |  | 20 | 30 |

Note) Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 measuring methods for transistors.








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