20 V, single N-channel Trench MOSFET 3 August 2012

Product data sheet

1. Product profile

1.1 General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

1.2 Features and benefits

- Low threshold voltage
- Very fast switching
- Trench MOSFET technology

1.3 Applications

- Relay driver
- High-speed line driver
- Low-side loadswitch
- Switching circuits

1.4 Quick reference data

Table 1. Quie	ck reference data						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{DS}	drain-source voltage	T _{amb} = 25 °C		-	-	20	V
V _{GS}	gate-source voltage			-8	-	8	V
I _D	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	-	1.5	А
Static characte	Static characteristics						
R _{DSon}	drain-source on-state resistance	V _{GS} = 4.5 V; I _D = 1 A; T _j = 25 °C		-	140	165	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².





20 V, single N-channel Trench MOSFET

2. Pinning information

Table 2.	Pinning	information		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	G	gate	3	D
2	S	source		
3	D	drain	1 2 TO-236AB (SOT23)	G S 017aaa253

3. Ordering information

Table 3. Ordering information					
Type number	Package				
	Name	Description	Version		
PMV170UN	TO-236AB	plastic surface-mounted package; 3 leads	SOT23		

4. Marking

Table 4. Marking codes	
Type number	Marking code
	[1]
PMV170UN	EG%

[1] % = placeholder for manufacturing site code

5. Limiting values

Table 5.Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{DS}	drain-source voltage	T _{amb} = 25 °C		-	20	V
V _{GS}	gate-source voltage			-8	8	V
I _D	drain current	V_{GS} = 4.5 V; T_{amb} = 25 °C; t ≤ 5 s	[1]	-	1.5	А
	V_{GS} = 4.5 V; T_{amb} = 25 °C	[1]	-	1	А	
		V_{GS} = 4.5 V; T_{amb} = 100 °C	[1]	-	0.9	А
I _{DM}	peak drain current	T_{amb} = 25 °C; single pulse; $t_p \le 10 \ \mu s$		-	4	А
P _{tot}	total power dissipation	T _{amb} = 25 °C	[2]	-	325	mW
			[1]	-	455	mW
		T _{sp} = 25 °C		-	1140	mW

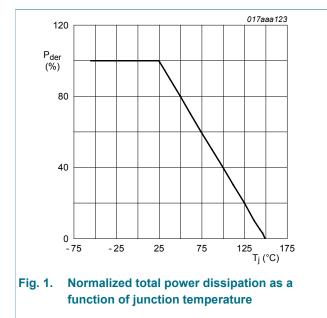
PMV170UN

20 V, single N-channel Trench MOSFET

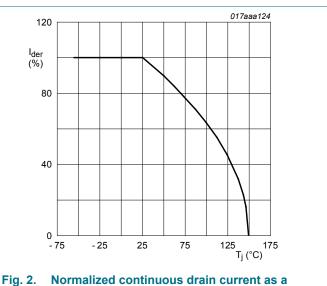
Symbol	Parameter	Conditions		Min	Мах	Unit
Tj	junction temperature			-55	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
Source-drain o	Source-drain diode					
l _S	source current	T _{amb} = 25 °C	[1]	-	0.7	А

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated, mounting pad for drain 6 cm².

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.



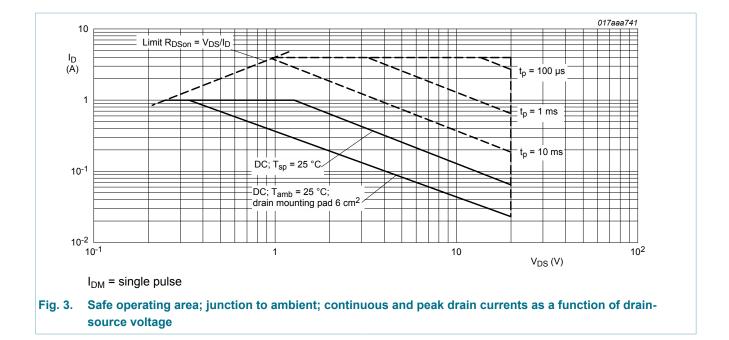
$$P_{der} = \frac{P_{tot}}{P_{tot(25^{\circ}C)}} \times 100 \%$$





$$I_{der} = \frac{I_D}{I_{D(25^*C)}} \times 100 \%$$

20 V, single N-channel Trench MOSFET



6. Thermal characteristics

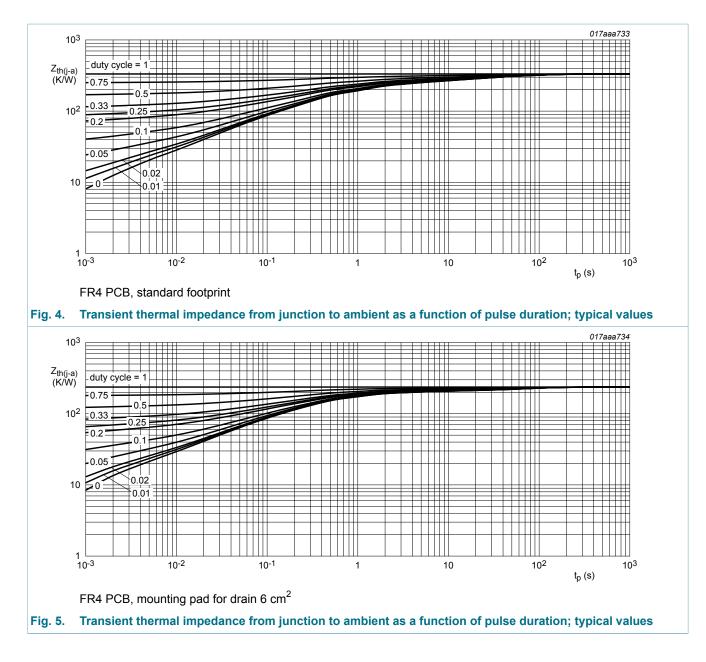
Table 6. 1	Thermal characteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R _{th(j-a)} thermal resistance from junction to ambient		in free air	[1]	-	333	385	K/W
		[2]	-	240	275	K/W	
	ampient	in free air; t ≤ 5 s	[2]	-	203	235	K/W
R _{th(j-sp)}	thermal resistance from junction to solder point			-	85	100	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for drain 6 cm².

PMV170UN

20 V, single N-channel Trench MOSFET



7. Characteristics

Table 7. Ch	naracteristics						
Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
Static characteristics							
V _{(BR)DSS}	drain-source breakdown voltage	I_D = 250 µA; V_{GS} = 0 V; T_j = 25 °C		20	-	-	V
V _{GSth}	gate-source threshold voltage	I_D = 250 µA; V_{DS} = V_{GS} ; T_j = 25 °C		0.4	0.7	1	V
I _{DSS}	drain leakage current	V_{DS} = 20 V; V_{GS} = 0 V; T_{amb} = 25 °C		-	-	1	μA
		V_{DS} = 20 V; V_{GS} = 0 V; T_{amb} = 150 °C		-	-	20	μA
PMV170UN		All information provided in this document is subject to legal disclaimers.			©NX	P B.V. 2012.	All rights reserve

PMV170UN

20 V, single N-channel Trench MOSFET

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
I _{GSS}	gate leakage current	V_{GS} = 8 V; V_{DS} = 0 V; T_j = 25 °C	-	-	100	nA
		V_{GS} = -8 V; V_{DS} = 0 V; T_j = 25 °C	-	-	100	nA
R _{DSon}	drain-source on-state	V_{GS} = 4.5 V; I _D = 1 A; T _j = 25 °C	-	140	165	mΩ
	resistance	V _{GS} = 4.5 V; I _D = 1 A; T _j = 150 °C	-	208	248	mΩ
		V_{GS} = 2.5 V; I _D = 1 A; T _j = 25 °C	-	175	220	mΩ
		V_{GS} = 1.8 V; I _D = 0.25 A; T _j = 25 °C	-	237	337	mΩ
9fs	forward transconductance	V _{DS} = 10 V; I _D = 1 A; T _j = 25 °C	-	3.4	-	S
Dynamic cl	haracteristics	,				
Q _{G(tot)}	total gate charge	V_{DS} = 10 V; I _D = 1 A; V _{GS} = 4.5 V;	-	1.1	1.65	nC
Q _{GS}	gate-source charge	T _j = 25 °C	-	0.15	-	nC
Q _{GD}	gate-drain charge	_	-	0.3	-	nC
C _{iss}	input capacitance	V_{DS} = 10 V; f = 1 MHz; V_{GS} = 0 V;	-	83	-	pF
C _{oss}	output capacitance	T _j = 25 °C	-	37	-	pF
C _{rss}	reverse transfer capacitance	_	-	26	-	pF
t _{d(on)}	turn-on delay time	V_{DS} = 10 V; I _D = 1 A; V _{GS} = 4.5 V;	-	6	-	ns
t _r	rise time	R _{G(ext)} = 6 Ω; T _j = 25 °C	-	12	-	ns
t _{d(off)}	turn-off delay time		-	16	-	ns
tr	fall time		_	8	_	ns

ce-drain diode

source-drain voltage

 V_{SD}

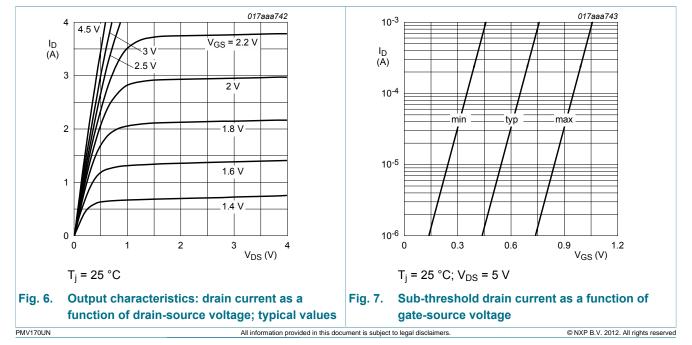
 I_{S} = 0.7 A; V_{GS} = 0 V; T_{j} = 25 °C

1.2

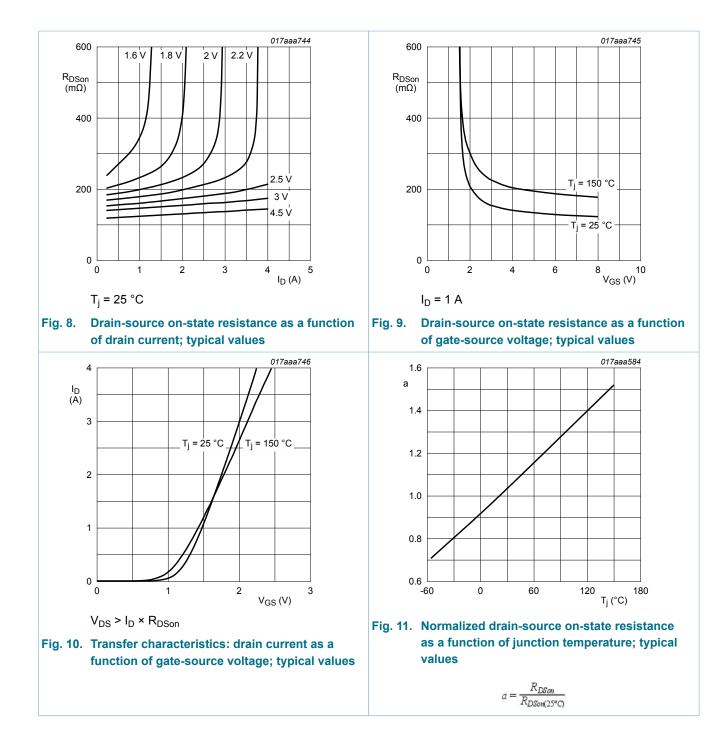
0.8

-

V

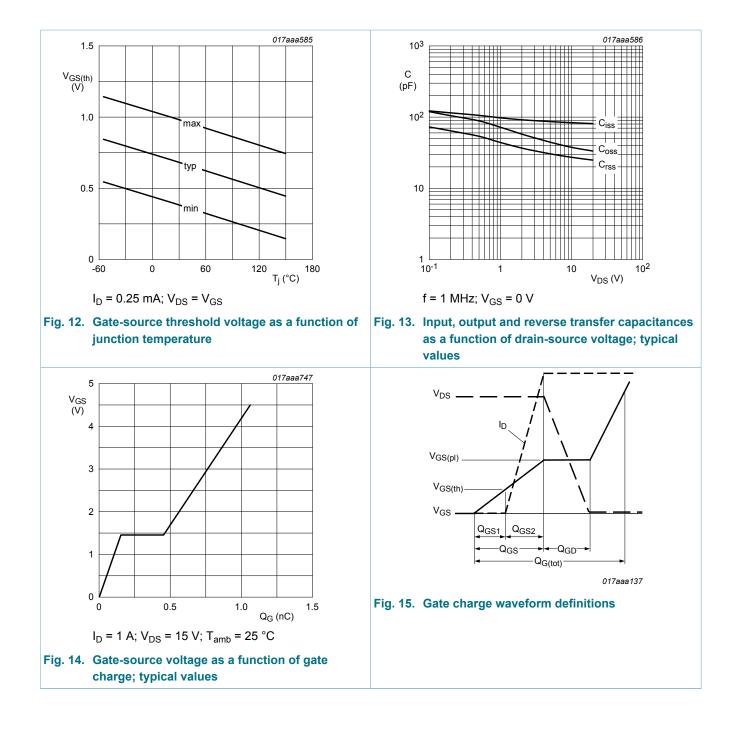


20 V, single N-channel Trench MOSFET



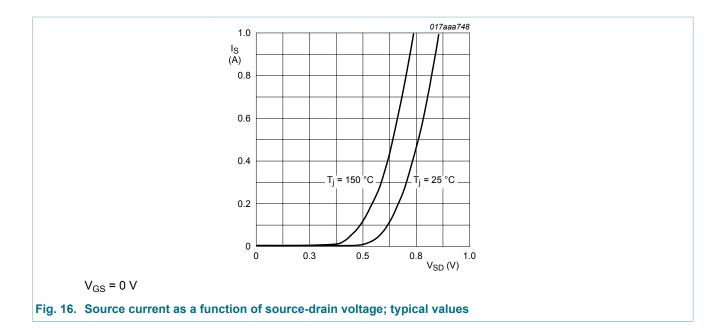
PMV170UN

20 V, single N-channel Trench MOSFET

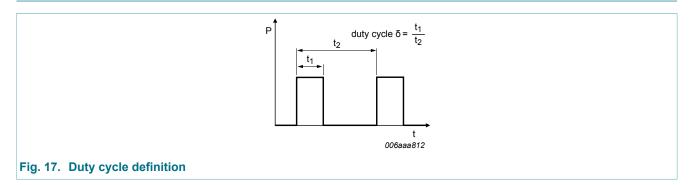


PMV170UN

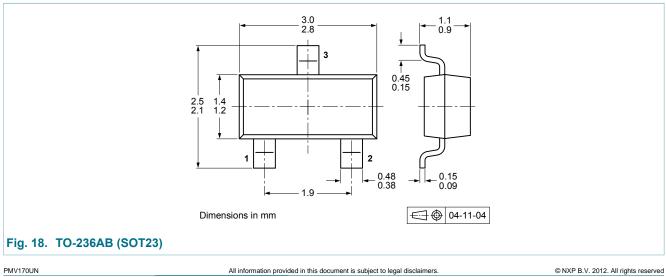
20 V, single N-channel Trench MOSFET



Test information 8.

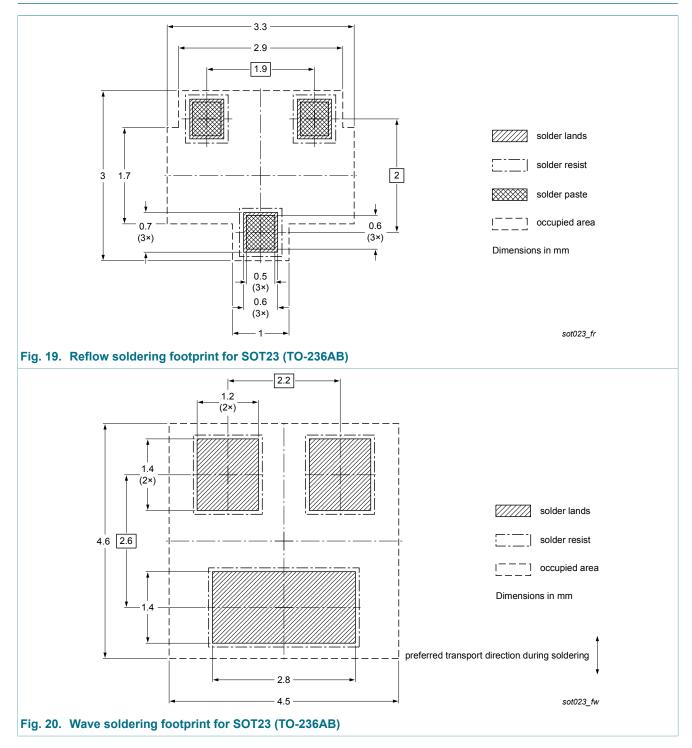


Package outline 9.



20 V, single N-channel Trench MOSFET

10. Soldering



20 V, single N-channel Trench MOSFET

11. Revision history

Table 8. Revision history					
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes	
PMV170UN v.1	20120803	Product data sheet	-	-	

20 V, single N-channel Trench MOSFET

12. Legal information

12.1 Data sheet status

Document status [1][2]	Product status [<u>3]</u>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <u>http://www.nxp.com</u>.

12.2 Definitions

Preview — The document is a preview version only. The document is still subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Draft — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

Short data sheet — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

Product specification — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

12.3 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Suitability for use — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

Quick reference data — The Quick reference data is an extract of the product data given in the Limiting values and Characteristics sections of this document, and as such is not complete, exhaustive or legally binding.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Limiting values — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at http://www.nxp.com/profile/terms, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

No offer to sell or license — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the

PMV170UN

© NXP B.V. 2012. All rights reserved

20 V, single N-channel Trench MOSFET

grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Non-automotive qualified products — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

Translations — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

12.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

Adelante, Bitport, Bitsound, CoolFlux, CoReUse, DESFire, EZ-HV, FabKey, GreenChip, HiPerSmart, HITAG, I²C-bus logo, ICODE, I-CODE, ITEC, Labelution, MIFARE, MIFARE Plus, MIFARE Ultralight, MoReUse, QLPAK, Silicon Tuner, SiliconMAX, SmartXA, STARplug, TOPFET, TrenchMOS, TriMedia and UCODE — are trademarks of NXP B.V.

HD Radio and HD Radio logo — are trademarks of iBiquity Digital Corporation.

20 V, single N-channel Trench MOSFET

13. Contents

1	Product profile1
1.1	General description1
1.2	Features and benefits1
1.3	Applications1
1.4	Quick reference data1
2	Pinning information2
3	Ordering information2
4	Marking2
5	Limiting values2
6	Thermal characteristics4
7	Characteristics5
8	Test information9
9	Package outline9
10	Soldering 10
11	Revision history11
12	Legal information12
12.1	Data sheet status 12
12.2	Definitions12
12.3	Disclaimers12
12.4	Trademarks 13

© NXP B.V. 2012. All rights reserved

For more information, please visit: http://www.nxp.com For sales office addresses, please send an email to: salesaddresses@nxp.com Date of release: 3 August 2012