

# FGL35N120FTD

## 1200 V, 35 A Field Stop Trench IGBT

### Features

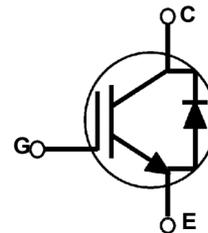
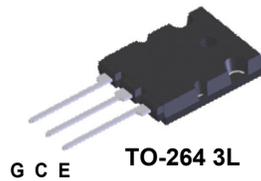
- Field Stop Trench Technology
- High Speed Switching
- Low Saturation Voltage:  $V_{CE(sat)} = 1.68 \text{ V @ } I_C = 35 \text{ A}$
- High Input Impedance

### Applications

- Solar Inverter, UPS, Welder, PFC

### General Description

Using advanced field stop trench IGBT technology, Fairchild®'s 1200V trench IGBTs offer the optimum performance for hard switching application such as solar inverter, UPS, welder applications.



### Absolute Maximum Ratings

Symbol	Description	Ratings	Unit
$V_{CES}$	Collector to Emitter Voltage	1200	V
$V_{GES}$	Gate to Emitter Voltage	$\pm 25$	V
$I_C$	Collector Current @ $T_C = 25^\circ\text{C}$	70	A
	Collector Current @ $T_C = 100^\circ\text{C}$	35	A
$I_{CM(1)}$	Pulsed Collector Current @ $T_C = 25^\circ\text{C}$	105	A
$I_F$	Diode Continuous Forward Current @ $T_C = 100^\circ\text{C}$	40	A
$P_D$	Maximum Power Dissipation @ $T_C = 25^\circ\text{C}$	368	W
	Maximum Power Dissipation @ $T_C = 100^\circ\text{C}$	147	W
$T_J$	Operating Junction Temperature	-55 to +150	$^\circ\text{C}$
$T_{stg}$	Storage Temperature Range	-55 to +150	$^\circ\text{C}$
$T_L$	Maximum Lead Temp. for soldering Purposes, 1/8" from case for 5 seconds	300	$^\circ\text{C}$

**Notes:**

1: Repetitive rating: Pulse width limited by max. junction temperature

### Thermal Characteristics

Symbol	Parameter	Ratings	Unit
$R_{\theta JC}(IGBT)$	Thermal Resistance, Junction to Case	0.34	$^\circ\text{C/W}$
$R_{\theta JC}(\text{Diode})$	Thermal Resistance, Junction to Case	0.9	$^\circ\text{C/W}$

$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	25	$^{\circ}C/W$
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### Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FGL35N120FTD	FGL35N120FTDTU	TO-264	-	-	30

### Electrical Characteristics of the IGBT $T_C = 25^{\circ}C$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Unit
<b>Off Characteristics</b>						
$BV_{CES}$	Collector to Emitter Breakdown Voltage	$V_{GE} = 0 V, I_C = 250 \mu A$	1200	-	-	V
$I_{CES}$	Collector Cut-Off Current	$V_{CE} = V_{CES}, V_{GE} = 0 V$	-	-	1	mA
$I_{GES}$	G-E Leakage Current	$V_{GE} = V_{GES}, V_{CE} = 0 V$	-	-	$\pm 250$	nA
<b>On Characteristics</b>						
$V_{GE(th)}$	G-E Threshold Voltage	$I_C = 35 mA, V_{CE} = V_{GE}$	3.5	6.2	7.5	V
$V_{CE(sat)}$	Collector to Emitter Saturation Voltage	$I_C = 35 A, V_{GE} = 15 V$	-	1.68	2.2	V
		$I_C = 35 A, V_{GE} = 15 V, T_C = 125^{\circ}C$	-	2.0	-	V
<b>Dynamic Characteristics</b>						
$C_{ies}$	Input Capacitance	$V_{CE} = 30 V, V_{GE} = 0 V, f = 1 MHz$	-	5090	-	pF
$C_{oes}$	Output Capacitance		-	180	-	pF
$C_{res}$	Reverse Transfer Capacitance		-	95	-	pF
<b>Switching Characteristics</b>						
$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 600 V, I_C = 35 A, R_G = 10 \Omega, V_{GE} = 15 V, \text{Inductive Load}, T_C = 25^{\circ}C$	-	34	-	ns
$t_r$	Rise Time		-	63	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	172	-	ns
$t_f$	Fall Time		-	107	-	ns
$E_{on}$	Turn-On Switching Loss		-	2.5	-	mJ
$E_{off}$	Turn-Off Switching Loss		-	1.7	-	mJ
$E_{ts}$	Total Switching Loss		-	4.2	-	mJ
$t_{d(on)}$	Turn-On Delay Time	$V_{CC} = 600 V, I_C = 35 A, R_G = 10 \Omega, V_{GE} = 15 V, \text{Inductive Load}, T_C = 125^{\circ}C$	-	33	-	ns
$t_r$	Rise Time		-	66	-	ns
$t_{d(off)}$	Turn-Off Delay Time		-	180	-	ns
$t_f$	Fall Time		-	146	-	ns
$E_{on}$	Turn-On Switching Loss		-	3.1	-	mJ
$E_{off}$	Turn-Off Switching Loss		-	2.1	-	mJ
$E_{ts}$	Total Switching Loss		-	5.2	-	mJ
$Q_g$	Total Gate Charge	$V_{CE} = 600 V, I_C = 35 A, V_{GE} = 15 V$	-	210	-	nC
$Q_{ge}$	Gate to Emitter Charge		-	42	-	nC
$Q_{gc}$	Gate to Collector Charge		-	101	-	nC

**Electrical Characteristics of the Diode**  $T_C = 25^\circ\text{C}$  unless otherwise noted

Symbol	Parameter	Test Conditions	Min.	Typ.	Max	Unit	
$V_{FM}$	Diode Forward Voltage	$I_F = 35\text{ A}$	$T_C = 25^\circ\text{C}$	-	2.7	3.4	V
			$T_C = 125^\circ\text{C}$	-	2.5	-	
$t_{rr}$	Diode Reverse Recovery Time		$T_C = 25^\circ\text{C}$	-	337	-	ns
			$T_C = 125^\circ\text{C}$	-	520	-	
$I_{rr}$	Diode Peak Reverse Recovery Current	$I_F = 35\text{ A},$ $di/dt = 200\text{ A}/\mu\text{s}$	$T_C = 25^\circ\text{C}$	-	7.6	-	A
			$T_C = 125^\circ\text{C}$	-	12.9	-	
$Q_{rr}$	Diode Reverse Recovery Charge		$T_C = 25^\circ\text{C}$	-	1292	-	nC
			$T_C = 125^\circ\text{C}$	-	3377	-	

## Typical Performance Characteristics

Figure 1. Typical Output Characteristics

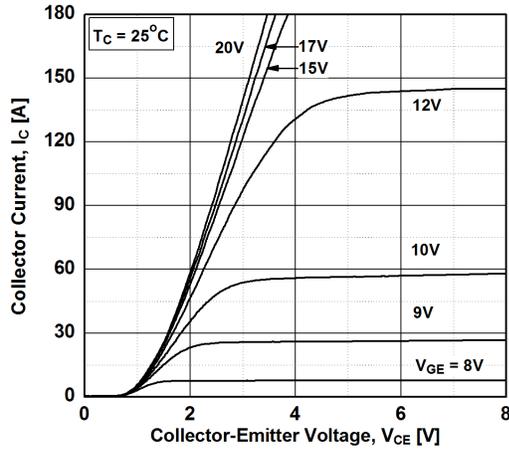


Figure 2. Typical Output Characteristics

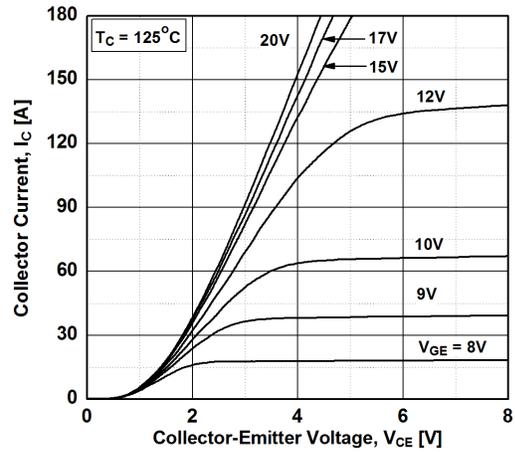


Figure 3. Typical Saturation Voltage Characteristics

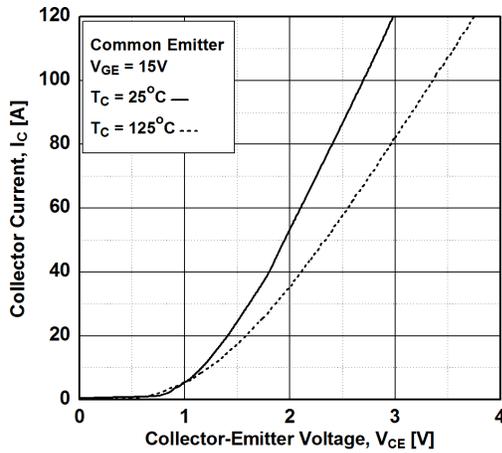


Figure 4. Transfer Characteristics

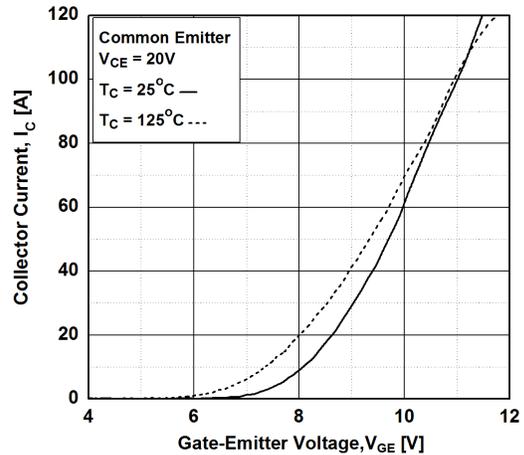


Figure 5. Saturation Voltage vs. Case Temperature at Variant Current Level

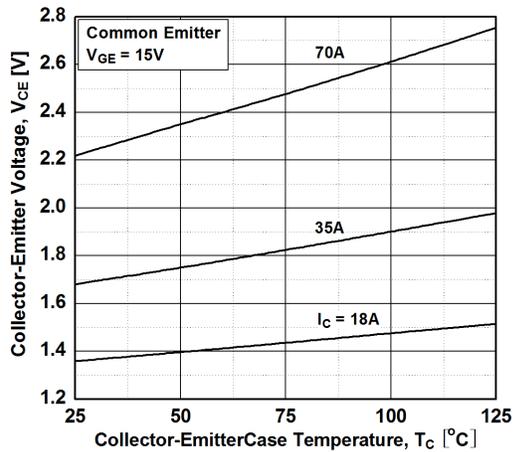
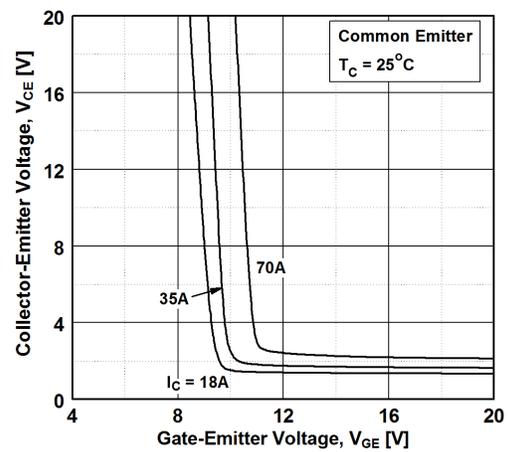


Figure 6. Saturation Voltage vs. Vge



## Typical Performance Characteristics

Figure 7. Saturation Voltage vs.  $V_{GE}$

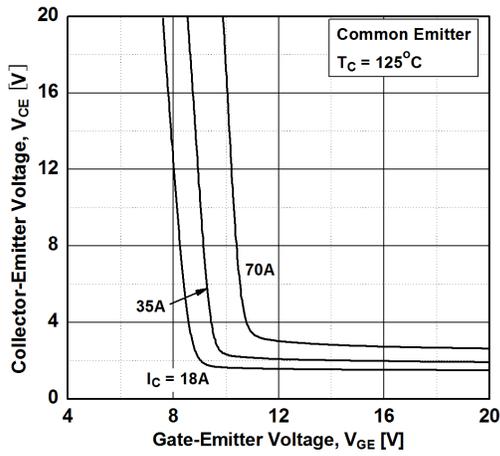


Figure 8. Load Current vs. Frequency

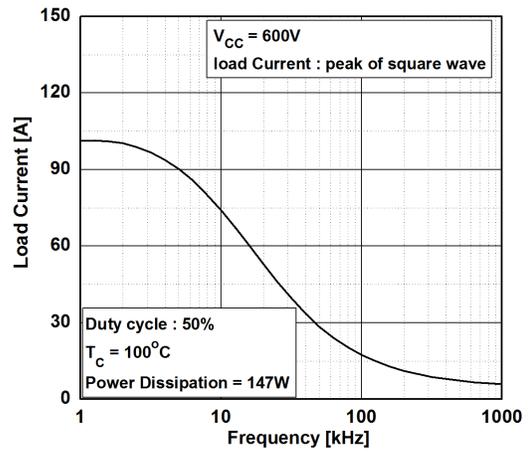


Figure 9. Capacitance Characteristics

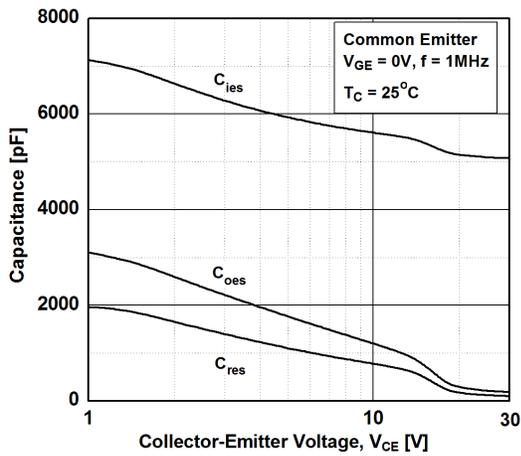


Figure 10. Gate Charge Characteristics

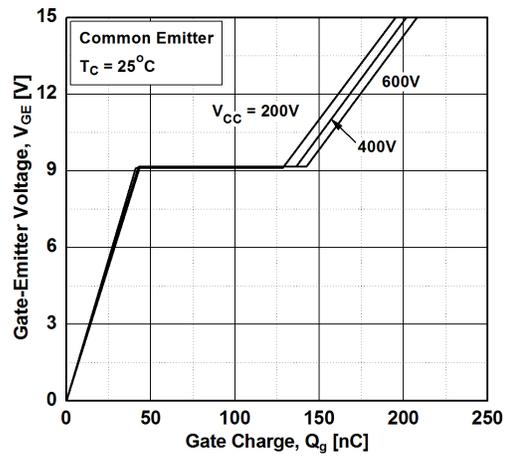


Figure 11. SOA Characteristics

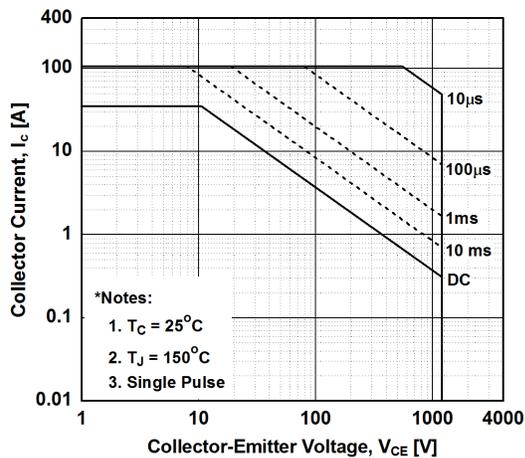
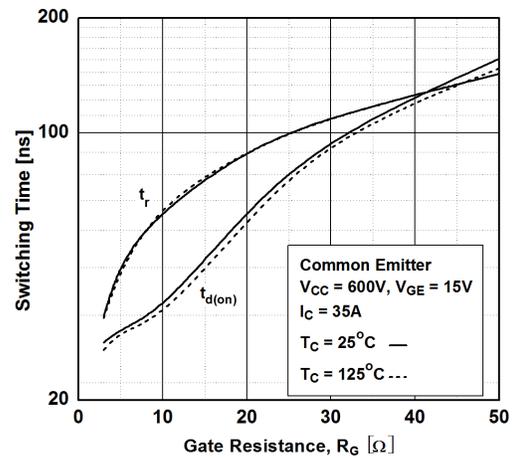
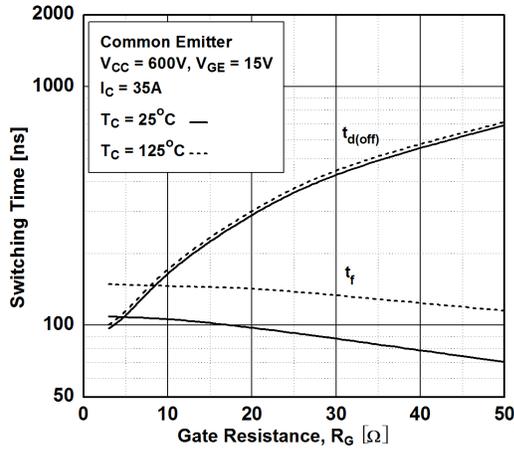


Figure 12. Turn-on Characteristics vs. Gate Resistance

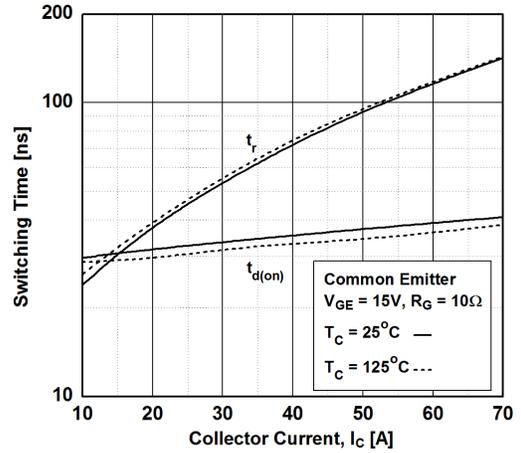


## Typical Performance Characteristics

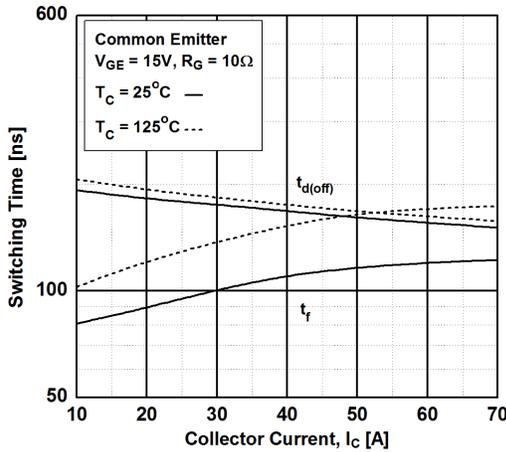
**Figure 13. Turn-off Characteristics vs. Gate Resistance**



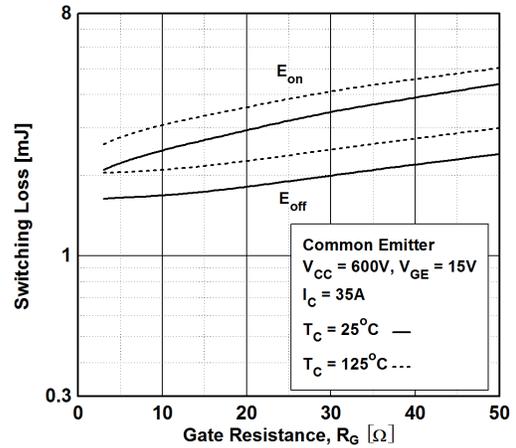
**Figure 14. Turn-on Characteristics vs. Collector Current**



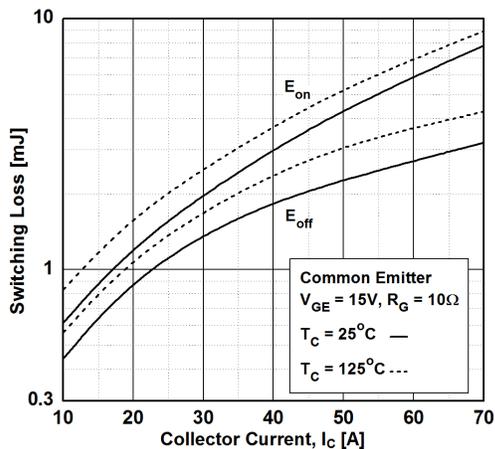
**Figure 15. Turn-off Characteristics vs. Collector Current**



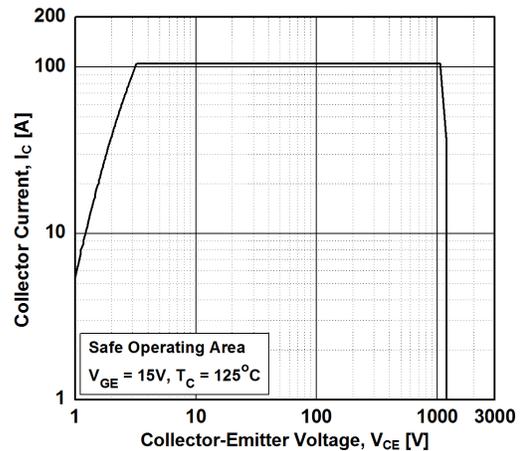
**Figure 16. Switching Loss vs. Gate Resistance**



**Figure 17. Switching Loss vs. Collector Current**



**Figure 18. Turn off Switing SOA Characteristics**



## Typical Performance Characteristics

Figure 19. Forward Characteristics

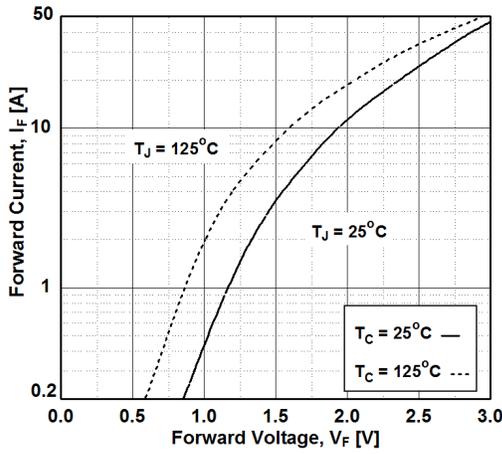


Figure 20. Reverse Recovery Current

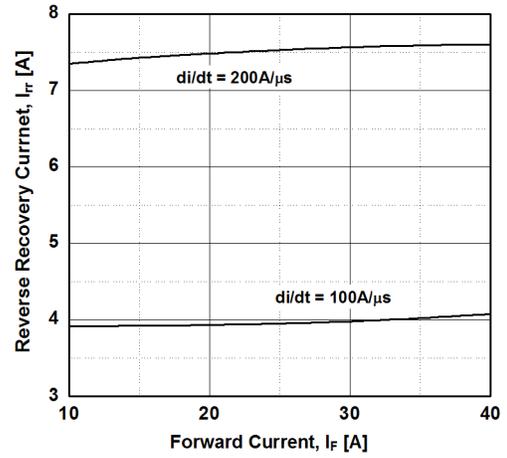


Figure 21. Stored Charge

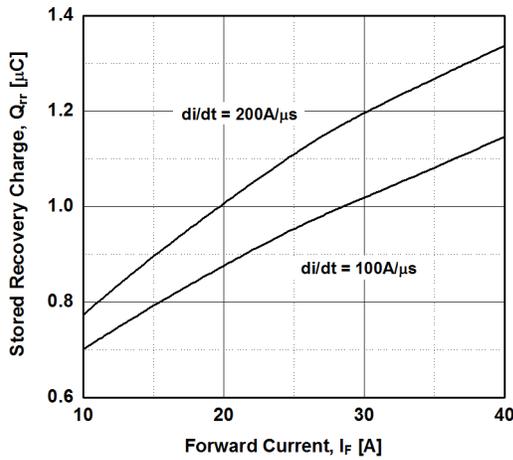


Figure 22. Reverse Recovery Time

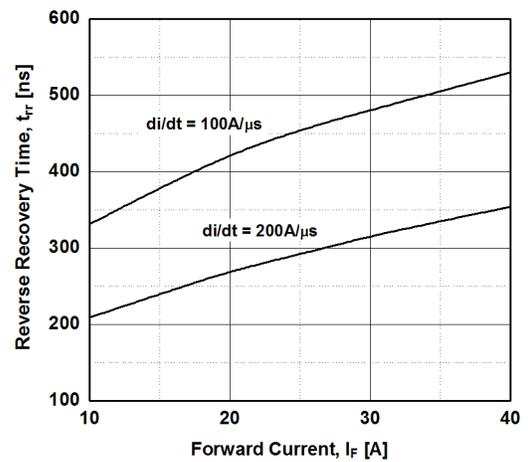
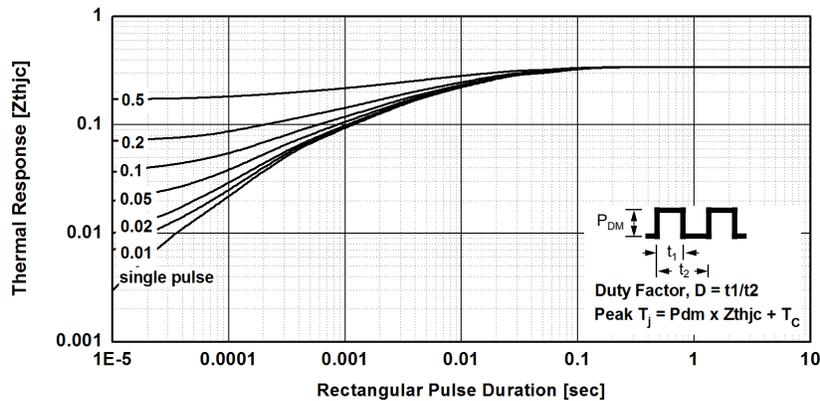
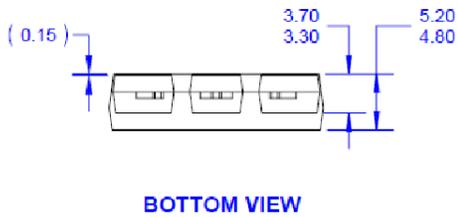
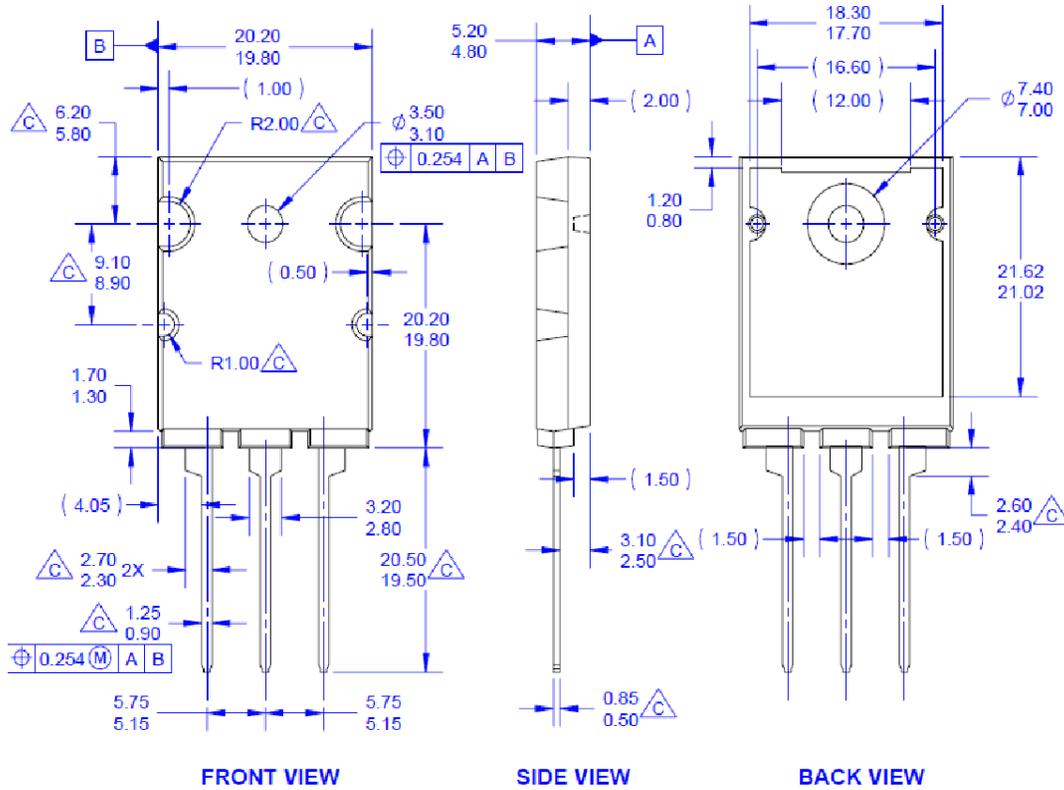


Figure 23. Transient Thermal Impedance of IGBT



**Mechanical Dimensions**

**TO-264**



**NOTES:**

- A. PACKAGE REFERENCE: JEDEC TO264 VARIATION AA.
- B. ALL DIMENSIONS ARE IN MILLIMETERS.
- C. OUT OF JEDEC STANDARD VALUE.
- D. DIMENSION AND TOLERANCE AS PER ASME Y14.5-1994.
- E. DIMENSIONS ARE EXCLUSIVE OF BURRS, MOLD FLASH AND TIE BAR PROTRUSIONS.
- F. THIS PACKAGE IS INTENDED ONLY FOR "FS PKG CODE AR"
- G. DRAWING FILE NAME: T0264A03REV1

**\* Front/Back Side Isolation Voltage : AC 2700V**



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| AccuPower™               | F-PFS™  | SYSTEM GENERAL®  |
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| BitSiC™                  | Global Power Resource <sup>SM</sup>             | TinyBuck™        |
| Build it Now™            | Green Bridge™                                   | TinyCalc™        |
| CorePLUS™                | Green FPS™                                      | TinyLogic®       |
| CorePOWER™               | Green FPS™ e-Series™                            | TINYOPTO™        |
| CROSSVOLT™               | Gmax™   | TinyPower™       |
| CTL™                     | GTO™  | TinyPWM™         |
| Current Transfer Logic™  | IntelliMAX™                                     | TinyWire™        |
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| EcoSPARK®                | MegaBuck™                                       | TRUECURRENT®*    |
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| ESBC™                    | MicroFET™                                       | SerDes™          |
| Fairchild®               | MicroPak™                                       | UHC®             |
| Fairchild Semiconductor® | MicroPak2™                                      | Ultra FRFET™     |
| FACT Quiet Series™       | MillerDrive™                                    | UniFET™          |
| FACT®                    | MotionMax™                                      | VXC™             |
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| FastvCore™               | OptoHiT™  | VoltagePlus™     |
| FETBench™                | OPTOLOGIC®                                      | XS™              |
|                          | OPTOPLANAR®                                     |                  |
|                          | PowerTrench®                                    |                  |
|                          | PowerXS™  |                  |
|                          | Programmable Active Droop™                      |                  |
|                          | QFET®   |                  |
|                          | QS™   |                  |
|                          | Quiet Series™                                   |                  |
|                          | RapidConfigure™                                 |                  |
|                          | Saving our world, 1mW/W/kW at a time™           |                  |
|                          | SignalWise™                                     |                  |
|                          | SmartMax™                                       |                  |
|                          | SMART START™                                    |                  |
|                          | Solutions for Your Success™                     |                  |
|                          | SPM®  |                  |
|                          | STEALTH™  |                  |
|                          | SuperFET®                                       |                  |
|                          | SuperSOT™-3                                     |                  |
|                          | SuperSOT™-6                                     |                  |
|                          | SuperSOT™-8                                     |                  |
|                          | SupreMOS®                                       |                  |
|                          | SyncFET™  |                  |

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- A critical component in any component of a life support, device, or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	Datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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