# 3SK285 Silicon N-Channel MOS FET

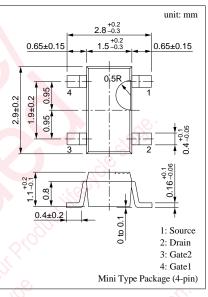
## For UHF amplification

#### Features

- Low noise-figure (NF)
- Large power gain PG
- Mini-type package, allowing downsizing of the sets and automatic insertion through the tape/magazine packing.

Absolute Maximum Ratings (Ta = 25°C)							
Parameter	Symbol	Ratings	Unit				
Drain to Source voltage	V <sub>DS</sub>	13	V				
Gate 1 to Source voltage	V <sub>G1S</sub>	±8	v				
Gate 2 to Source voltage	V <sub>G2S</sub>	±8	V				
Drain current	I <sub>D</sub>	±30	mA				
Allowable power dissipation	P <sub>D</sub>	150	mW				
Channel temperature	T <sub>ch</sub>	150	°C				
Storage temperature	T <sub>stg</sub>	-55 to +150	°C				

#### Absolute Maximum Ratings ( $Ta = 25^{\circ}C$ )



## Marking Symbol: HV

### ■ Electrical Characteristics (Ta = 25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Drain current	I <sub>DS</sub>	$V_{DS} = 10V, V_{G1S} = 1V, V_{G2S} = 4V$	10	18	25	mA
Gate 1 cut-off current	I <sub>G1SS</sub>	$V_{DS} = V_{G2S} = 0, V_{G1S} = \pm 8V$			±20	nA
Gate 2 cut-off current	I <sub>G2SS</sub>	$V_{DS} = V_{G1S} = 0, V_{G2S} = \pm 8V$	all'		±20	nA
Gate 1 to Source cut-off voltage	V <sub>G1SC</sub>	$V_{DS} = 10V, V_{G2S} = 4V, I_D = 0.1mA$	-1		1	V
Gate 2 to Source cut-off voltage	V <sub>G2SC</sub>	$V_{DS} = 10V, V_{G1S} = 4V, I_D = 0.1mA$	0		1	V
Drain to Source voltage	V <sub>DSX</sub>	$I_D = 50\mu A, V_{G1S} = -5V, V_{G2S} = 0$	15			V
Forward transfer admittance	Y <sub>fs</sub>	$V_{DS} = 10V, I_D = 10mA, V_{G2S} = 4V, f = 1kHz$	24	29	34	mS
Input capacitance (Common Source)	C <sub>iss</sub>	$V_{DS} = 10V, V_{G1S} = V_{G2S} = -5V$ $f = 1MHz$	1.4	1.7	2.2	pF
Output capacitance (Common Source)	C <sub>oss</sub>			0.9	1.2	pF
Reverse transfer capacitance (Common Source)	C <sub>rss</sub>			0.02		pF
Power gain	PG	$V_{DS} = 8V, I_D = 8mA, V_{G2S} = 3V$	15.8	17.5	20	dB
Noise figure	NF	f = 800 MHz		2.2	2.7	dB

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