3SK241 GaAs N-Channel MES FET

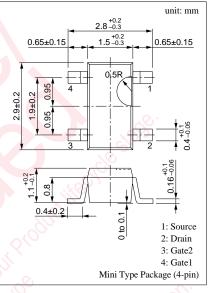
For VHF-UHF amplification

Features

- Low noise-figure (NF)
- Large power gain PG
- Mini-type package, allowing downsizing of the sets and automatic insertion through the tape/magazine packing.

ADSOLUTE MAXIMUM Ratings $(1a = 25^{\circ}C)$							
Parameter	Symbol	Ratings	Unit				
Drain to Source voltage	V _{DS}	13	V				
Gate 1 to Source voltage	V _{G1S}	-6	V				
Gate 2 to Source voltage	V _{G2S}	-6	v				
Drain current	ID	50	mA				
Gate 1 current	I _{G1}	1	mA				
Gate 2 current	I _{G2}	1	mA				
Allowable power dissipation	P _D	200	mW				
Channel temperature	T _{ch}	150	°C				
Storage temperature	T _{stg}	-55 to +150	S°C				

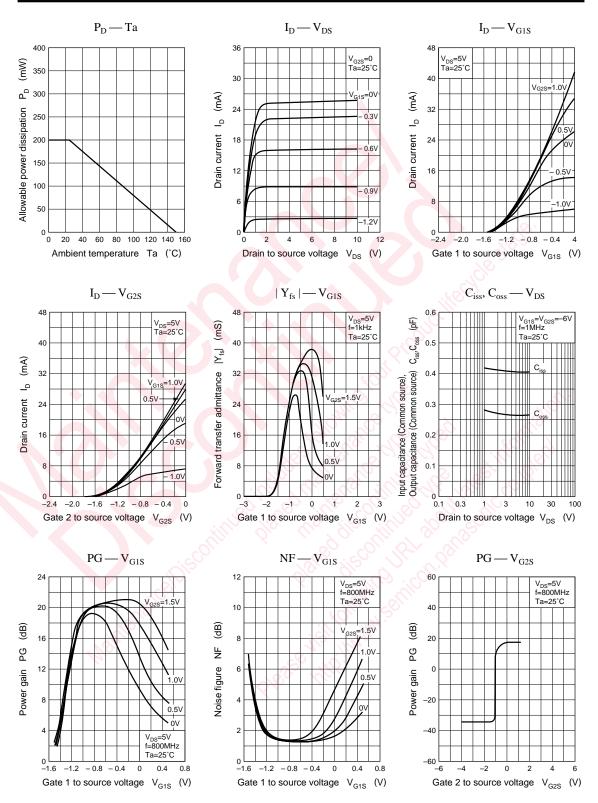
Absolute Maximum Ratings ($Ta = 25^{\circ}C$)



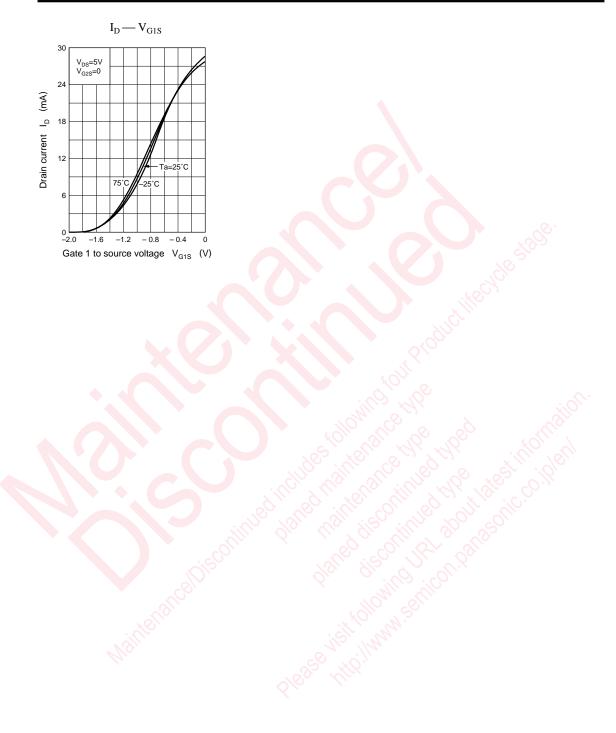
Marking Symbol: DU

Electrical Characteristics (Ta = 25°C)

Parameter	Symbol	Conditions	min	typ	max	Unit
Drain to Source cut-off current	I _{DSS}	$V_{DS} = 5V, V_{G1S} = 0, V_{G2S} = 0$	8.5	S.	35	mA
Gate 2 to Drain current	I _{G2DO}	$V_{G2D} = -13V (G1, S = Open)$	2. %	X	50	μΑ
Gate 1 cut-off current	I _{G1SS}	$V_{DS} = V_{G2S} = 0, V_{G1S} = -6V$			-20	μΑ
Gate 2 cut-off current	I _{G2SS}	$V_{DS} = V_{G1S} = 0, V_{G2S} = -6V$	SL.		-20	μΑ
Drain cut-off current	I _{DSX}	$V_{DS} = 13V, V_{G1S} = -3.5V, V_{G2S} = 0$			50	μΑ
Gate 1 to Source cut-off voltage	V _{G1SC}	$V_{DS} = 5V, V_{G2S} = 0, I_D = 200 \mu A$			-3.5	V
Gate 2 to Source cut-off voltage	V _{G2SC}	$V_{DS} = 5V, V_{G1S} = 0, I_D = 200 \mu A$			-3.5	V
Forward transfer admittance	Y _{fs}	$V_{DS} = 5V, I_D = 10mA, V_{G2S} = 1.5V, f = 1kHz$	18	23		mS
Input capacitance (Common Source)	C _{iss}			0.4	2	pF
Output capacitance (Common Source)	C _{oss}	$V_{DS} = 5V, V_{G1S} = V_{G2S} = -6V$ f = 1MHz		0.3	1.2	pF
Reverse transfer capacitance (Common Source)	C _{rss}			0.02	0.04	pF
Power gain	PG	$V_{DS} = 5V, I_D = 10mA$	13	19		dB
Noise figure	NF	$V_{G2S} = 1.5V, f = 800MHz$		1.5	2.5	dB
Gain reduction	G _R	$V_{DS} = 5V, V_{AGC} = 1.5V/-3.5V, f = 800MHz$	37	45		dB



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