TOSHIBA Bipolar Digital Integrated Circuit Silicon Monolithic

TD62107PG,TD62107FG

4ch High-current Darlington Sink Driver

The TD62107PG/FG are high-voltage, high-current darlington drivers and enable inputs which can gate the outputs. All units feature integral clamp diodes for switching inductive loads.

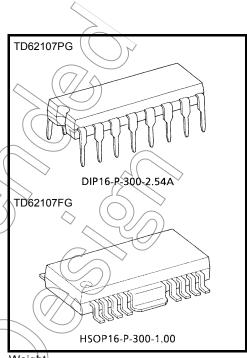
The TD62107PG/FG have a wide supply voltage range and all input are compatible with TTL and 5-V CMOS.

Application include relay, hammer, lamp and stepping moter drivers.

Please observe the thermal condition for using. The suffix (G) appended to the part number represents a RoHS-compatible product.

Features

- Output current (single output) 750 mA (max)
- High sustaining voltage output: 45 V min (TD62107PG) 35 V min (TD62107FG)
- Output clamp diodes
- Enable inputs E1, E2
- Wide supply voltage range $V_{CC} = 4.75$ to 7 V
- Input compatible with TTL and 5-V CMOS
- GND terminal = heat sink
- Package type-PG: DIP-16pin
- Package type-FG: HSOP-16pin



Weight

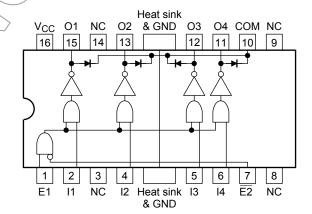
1D62107FG

DIP16-P-300-2.54A: 1.11 g (typ.) HSOP16-P-300-1.00: 0.50 g (typ.)

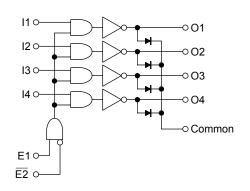
Pin Assignment (top-view)

TD62107PG

Heat sink COM V_{CC} 01 Ω2 & GND O304 16 15 14 13 12 11 10 9 5 6 3 4 8 E2 11 12 Heat sink 13 & GND



Schematics (each driver)

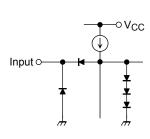


Truth Table

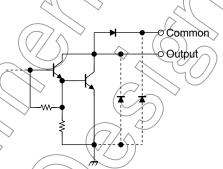
E1	E2	I1 to I4	O1 to O4
L	L	L or H	Disable OFF
L	Н	L or H	Disable OFF
Н	L	L or H	Enable In
Н	Н	L or H	Disable OFF

In = I1 to I4

Input Equivalent Circuit



Output Equivalent Circuit



Note: The input and output parasitic diodes cannot be used as clamp diodes.

Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit	
Supply voltage	Vcc	-0.5 to 17	\ \	
Output sustaining voltage PG	V _{CE} (SUS)	-0.5 to 45 -0.5 to 35	٧	
Output current	lout	750	mA	
Input voltage	V _{IN}	-0.5 to V _{CC} + 0.5	V	
Clamp diode reverse PG	⟨V _R	45	V	
voltage FG	WR WR	35	V	
Clamp diode forword current	(F)	500	mA	
Power dissipation PG	Pn	2.7 (Note 1)	W	
FG FG	(J-1)	1.4 (Note 2)	VV	
Operating temperature	T _{opr}	-40 to 85	°C	
Storage temperature	T _{stg}	-55 to 150	°C	

Note 1: On glass epoxy PCB ($50 \times 50 \times 1.6$ mm Cu 50%)

Note 2: On glass epoxy PCB ($60 \times 30 \times 1.6$ mm Cu 30%)

Operating Conditions ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristics		Symbol	Test Condition		Min	Тур.	Max	Unit
Supply voltage		V_{CC}	_		4.75	_	15	V
Output sustaining voltage	PG	Vor. (aug.)	_ <		0	_	45	V
	FG	V _{CE} (SUS)			0/		35	
	_	Іоит	T _{pw} = 25 ms, Duty = 75	5%, 1 Circuit	0		500	mA
Output current	PG		$T_{DW} = 25 \text{ ms}, 4 \text{ Circuit}$	Duty = 30%	0)	400	
	FG			Duty = 40%	<u> </u>	_	300	
Input voltage		V _{IN}	_		<u>)</u> 9)	_	V_{CC}	V
Clamp diode reverse voltage	PG	- V _R			_	_	45	V
	FG		_		_	_	35	V
Clamp diode forward current		lF			_		500	mA
Power dissipation	PG	- P _D	_<			4	1.0	W
	FG		Ta = 85°C	(Note 1)	-	/-/	> 0.7	VV

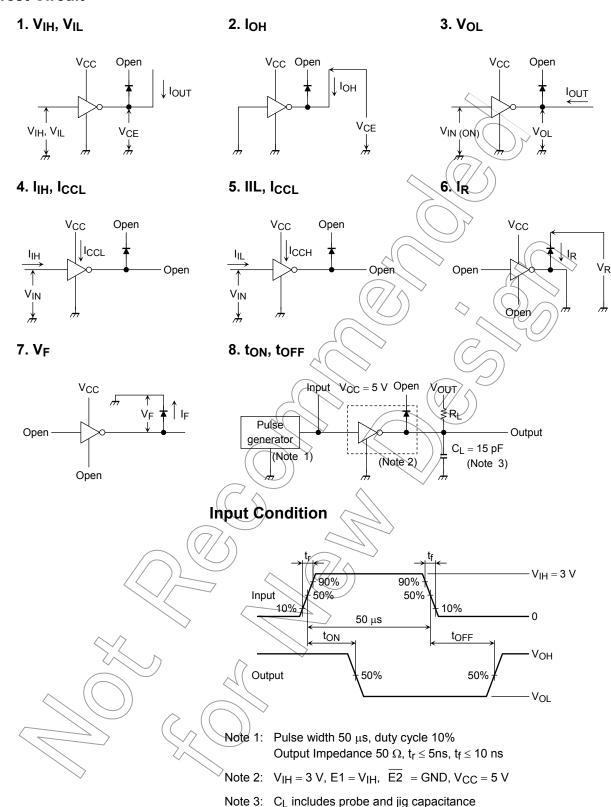
Note1: On Glass Epoxy PCB (60 \times 30 \times 1.6 mm Cu 30%)

Electrical Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit		
	High lev	High level V _{IH}				2.0	_	V _{CC}	\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
Input voltage	Low lev	el	VIL	1		_	_	0.8	V	
Output current	High PG		lou)2	V _{CE} = 45 V, Ta = 75°C	_		100	μΑ	
	level	FG	Гон		V _{CE} = 35 V, Ta = 85°C	_	_	100	μΑ	
Output voltage	Low level		(VOL)	3	I _{OUT} = 50 mA	_	_	1.3	V	
	2011101		OL)	Ŭ	I _{OUT} = 750 mA	_	_	1.6	v	
Input current	High lev	/el (// Лун	4	V _{IN} = 13-V	_	_	100	μΑ	
input current	Low lev	el		5 (V _{IN} = 0.4 V	_	_	-0.3	mA	
Clamp diode reverse current FG		PG	\supset $_{I_R}$	6	V _R =45 V	_	_	100	μА	
		FG IR		-0	V _R = 35 V	_		100	μΛ	
Clamp diode forward voltage V _F		VF	7	I _F = 500 mA	_		2.0	٧		
Supply current	Output high	ГССН	4	$V_{CC} = 13 \text{ V}, V_{IN} = 0 \text{ V}$ Output open	_	ı	13	mA		
	Output low	Icc	FCC	5	$V_{CC} = 13 \text{ V}, V_{IN} = 5 \text{ V}$ Output open		ı	17	IIIA	
Turn-on-delay	PG		8	$\begin{aligned} &V_{CC}=5~\text{V, R}_{L}=90~\Omega\\ &C_{L}=15~\text{pF, V}_{OUT}=45~\text{V} \end{aligned}$		5				
		FG	ton'	0	V_{CC} = 5 V, R_L = 70 Ω C_L = 15 pF, V_{OUT} = 35 V		5		μ\$	
Turn-off delay		PG	torr	8	$V_{CC} = 5 \text{ V}, R_L = 90 \Omega$ $C_L = 15 \text{ pF}, V_{OUT} = 45 \text{ V}$		5	_	6	
		FG	^t OFF	0	$V_{CC} = 5 \text{ V}, R_L = 70 \Omega$ $C_L = 15 \text{ pF}, V_{OUT} = 35 \text{ V}$	_	5		μ\$	

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Test Circuit



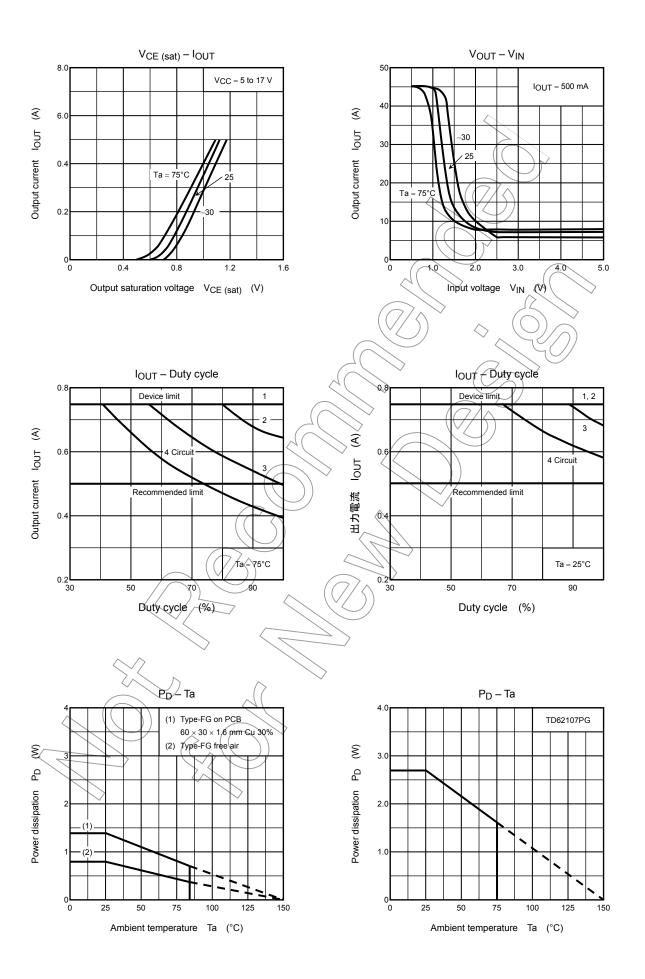
Precautions for Using

This IC does not include built-in protection circuits for excess current or overvoltage.

If this IC is subjected to excess current or overvoltage, it may be destroyed.

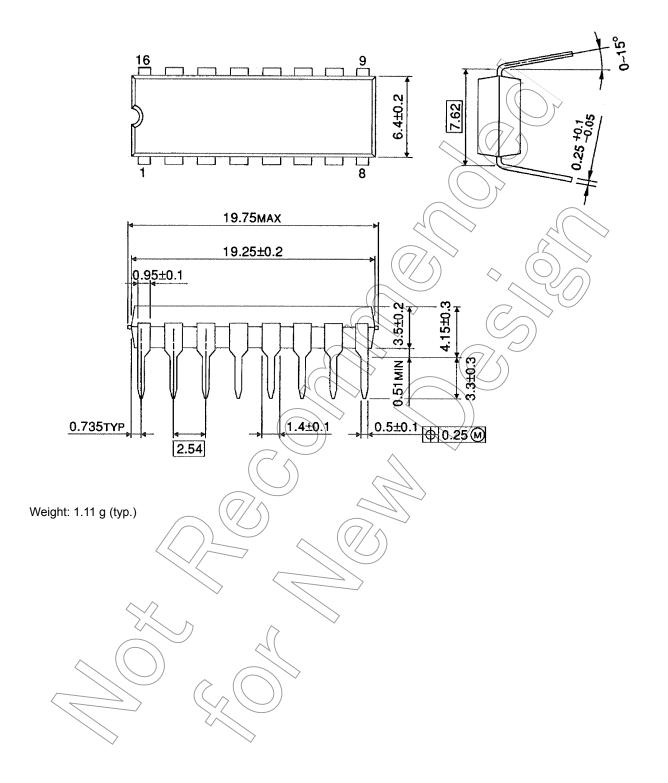
Hence, the utmost care must be taken when systems which incorporate this IC are designed.

Utmost care is necessary in the design of the output line, VCC, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.



Package Dimensions

DIP16-P-300-2.54A Unit: mm



Package Dimensions

HSOP16-P-300-1.00 Unit: mm 16 9.6±0.3 6.4 ± 0.2 8 1.0TYP 1.0 0.4±0.1 **⊕**[0.2∭ 2.5 13.5MAX 13.0±0.2 0.92±0.2 Weight: 0.50 g (typ.)

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