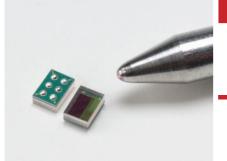


# **Color sensor**



S11059-01WT

## I<sup>2</sup>C interface-compatible color sensor

The S11059-01WT is a color sensor that supports the  $I^2C$  (inter-integrated circuit) interface. It is sensitive to red ( $\lambda$ =615 nm), green ( $\lambda$ =530 nm), blue ( $\lambda$ =460 nm), and infrared ( $\lambda$ =855 nm) light, and outputs detected results as 16-bit digital data for each color. The photodiode for each color is automatically switched sequentially to perform measurements. The sensitivity and integration time can be adjusted so that light measurements can be performed over a wide range.

#### Features

- **■** I<sup>2</sup>C interface compatible
- Sequential measurements of red, green, blue, and infrared light
- 2-step sensitivity switching (sensitivity ratio 1 : 10)
- **■** Sensitivity adjustment by setting the integration time
- **■** Low voltage (2.5 V or 3.3 V) operation
- **Description:** 75 μA typ.
- Small package (WL-CSP: wafer level-chip size package)
- → Internal infrared-cut filter
- $\blacksquare$  Wide dynamic range (Low gain: 1 to 10 k/x)

## Applications

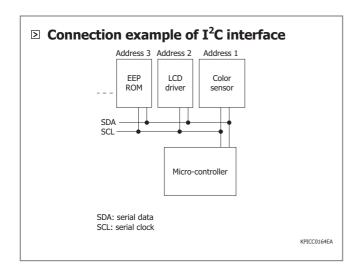
- LCD backlight adjustment for cell phones, notebook PC, etc.
- **■** Energy-saving sensor for large-size TV, etc.
- Various types of light detection or color adjustment

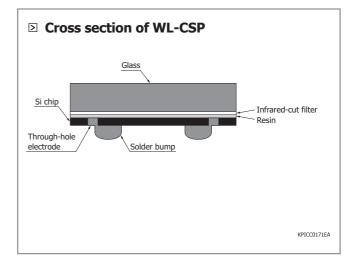
## Feature $\bigcup 1$ I<sup>2</sup>C interface-compatible to allow direct connection to micro-controller

The sensor supports the  $I^2C$  interface and so can exchange data with the micro-controller using two signal lines jointly usable with other devices. The digital output makes it easy to install into electronic devices such as cell phones and flat-panel TVs whose micro-controller is compatible with the  $I^2C$  interface. The sensor supports  $I^2C$  Fast mode (400 kHz) and operates on 2.25 to 3.63 V.

## Feature 02 WL-CSP makes the device even smaller and highly reliable

The WL-CSP measures only 1.18  $\times$  1.68  $\times$  0.58 mm and lead-free reflow solder (260 °C) can be used.





## **♣** Absolute maximum ratings (Ta=25 °C)

Parameter	Symbol	Condition	Value	Unit
Supply voltage	Vdd		-0.3 to +6	V
Load current	Io		±10	mA
Power dissipation	Р		100	mW
Operating temperature	Topr	No dew condensation*1	-40 to +85	°C
Storage temperature	Tstg	No dew condensation*1	-40 to +100	°C
Reflow soldering conditions*2	Tsol		Peak temperature 260 °C max., 3 times	-

<sup>\*1:</sup> When there is a temperature difference between a product and the surrounding area in high humidity environment, dew condensation may occur on the product surface. Dew condensation on the product may cause deterioration in characteristics and reliability.

### - Recommended operating conditions

Parameter	Symbol	Condition	Min.	Тур.	Max.	Unit
Supply voltage	Vdd		2.25	-	3.63	V
High level input voltage (SDA, SCL)*3	Vih		0.7Vdd	-	Vdd + 0.5	V
Low level input voltage (SDA, SCL)*3	Vil		-0.5	-	0.2Vdd	V
Bus capacitance (SDA, SCL)	Cbus		-	-	400	pF

<sup>\*3:</sup> Set so that Vdd=Vbus. Normal operation cannot be guaranteed unless used with this condition.

### **=** Electrical and optical characteristics

■ Sensor section [Ta=25 °C, Vdd=3.3 V, A light source, unless otherwise noted (initial setting: low gain, integration time: 546 ms/ch)]

Parame	ter	Symbol		Condition	Min.	Тур.	Max.	Unit	
			Blue			400 to 540			
Spectral response	range*4	λ	Green			455 to 630		nm	
Spectral response	range	^	Red			''''			
			Infrare	d, more than 700 nm					
			Blue		-	460	-		
Peak sensitivity wa	wolongth	λр	Green		-	530	-	nm	
reak sensitivity wa	ivelengui	\ \Ab	Red		-	615	-	nm	
			Infrare	d, more than 700 nm	-	855	-		
Comment consumention	Operating mode	Idd	E=0 lx	(dark state),	30	75	150		
Current consumption	Standby mode	Idds	excludi	ng output current	0.1	1.0	3.0	μA	
Dark count		Sd	E=0 lx	(dark state)	-	-	5	counts	
Gain ratio		rg	High ga	in/Low gain	-	10	-	-	
		Sbl	Blue		2.01	3.35	4.69		
		Sgl	Green	Initial authina	4.57	7.61	10.66		
Dhoto consitivity		Srl	Red	Initial setting	5.69	9.48	13.28		
	l avv anim	Sirl	Infrared		-	1.66	-	counts/lx	
Photo sensitivity	Low gain	Sbl	Blue		2.51	3.35	4.19	Counts/ <i>ix</i>	
		Sgl	Green	T (1.1 11.1 45	5.71	7.61	9.52		
		Srl	Red	Initial setting*5	7.11	9.48	11.85		
		Sirl	Infrared		-	1.66	-		
Red/Blue sensi. ratio		Srl/Sbl			2.12	2.83	3.54		
Red/Green sensi. ratio	Low gain	Srl/Sgl	Initial s		0.93	1.25	1.56	-	
Blue/Green sensi. ratio		Sbl/Sgl	Same o	пір	0.33	0.44	0.55		
		Sbh	Blue		19.0	31.7	44.4		
		Sgh	Green	Integration time	45.7	76.2	106.7		
		Srh	Red	546 ms/ch	56.7	94.5	132.4		
Disable and this ite.	Litala mata	Sirh	Infrared		-	15.3	-		
Photo sensitivity	High gain	Sbh	Blue		23.8	31.7	39.7	counts/lx	
		Sgh	Green	Integration time	57.2	76.2	95.3		
		Srh	Red	546 ms/ch* <sup>5</sup>	70.9	94.5	118.2		
		Sirh	Infrared		-	15.3	-		
Red/Blue sensi. ratio		Srh/Sbh		546 / 1	2.24	2.98	3.73		
Red/Green sensi. ratio	High gain	Srh/Sgh	Integra Same o	tion time 546 ms/ch	0.93	1.24	1.55	-	
Blue/Green sensi. ratio		Sbh/Sgh	Jame C	ıııþ	0.31	0.42	0.52		
Integration time		-	Integra "00"	tion time setting (Tint)	105	175	245	μs	

<sup>\*4:</sup> Relative sensitivity=more than 10%

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<sup>\*2:</sup> Moisture absorption and reflow conditions: JEDEC J-STD-020D LEVEL2a

Note: Exceeding the absolute maximum ratings even momentarily may cause a drop in product quality. Always be sure to use the product within the absolute maximum ratings.

<sup>\*5:</sup> Integration time is measured and corrected. See "Compensation method for sensitivity variation". Integration time measurement accuracy is 0.36%.

■ I<sup>2</sup>C section (Ta=25 °C, Vdd=3.3 V, unless otherwise noted)

Parameter	·	Symbol	Condition	Min.	Тур.	Max.	Unit
I <sup>2</sup> C address		ADDR	7 bits				
I <sup>2</sup> C clock frequency		fclk		1	-	400	kHz
SDA, SCL output	High level	Voh	Rp=2.2 kΩ	0.7Vbus	-	-	V
voltage	Low level	Vol	Rp=2.2 kΩ	0	-	0.4	V
Input/output terminal of	apacitance	Ci		-	-	20	pF
SDA/SCL output fall time*6			Rp=2.2 kΩ, Cp=400 pF	-	-	250	ns

<sup>\*6:</sup> SCL/SDA output rise time is determined by a time constant of Cbus × Rp.

Note: The I<sup>2</sup>C interface (SDA, SCL) timings conform to the "I<sup>2</sup>C bus specification version 2.1".

#### Register map

Adrs	Function					bit						
Auis	Function	7 6		5	4	3	2	1	0			
		ADC reset	Standby function	Standby function		Gain selection		Integration				
00	Control	1: Reset	1: Standby mode	monitor	-		1: Manual setting mode					
		0: Operation	0: Operating mode	HIOHILOI		0: Low gain	0: Fixed period mode	(10) 22.4 ms,	(11) 179.2 ms			
01	  Manual timing register			Integration	n tin	ne manual se	tting register (MSB)					
02	manual ullilling register		Integration time manual setting register (LSB)									
03	Sensor data register				Ou	tput data (re	d, MSB)					
04	(red)				Οι	itput data (re	ed, LSB)					
05	Sensor data register				Out	out data (gre	en, MSB)					
06	(green)				Out	put data (gre	en, LSB)					
07	Sensor data register				Out	put data (blu	ue, MSB)					
08	(blue)				Ou	tput data (bli	ue, LSB)					
09	Sensor data register				Outp	ut data (infra	red, MSB)					
0A	(infrared)				Outp	ut data (infra	ared, LSB)					

- Adrs 00 bit 7: Asserting this bit to "1", the ADC block is reset. The register data is not reset. To start the operation, set this bit to "0". Adrs 00 bit 6: Asserting this bit to "1" the device goes into standby mode. The ADC block stops its operation. The register data is not reset. To start the operation, set this bit to "0".
- Adrs 00 bit 5: This monitors auto standby function. "1" means standby mode. This is read only.
- Adrs 00 bit 3: Gain selection bit. "1" is high gain mode and "0" is low gain mode. This bit is selecting the photodiode area. The size ratio of high gain photodiode area and low gain photodiode area is 10:1. Therefore the gain ratio is 10 times from low to
- Adrs 00 bit 2: Asserting this bit to "1", the device goes into manual setting mode. Deasserting this bit to 0, goes into fixed period mode. In manual setting mode, the S11059-01WT automatically goes to standby mode after a measurement is made. In fixed period mode, measurements are continuously repeated.
- Adrs 00 bit 1,0: These bits select the period of internal basis clock. The period is equal to integration time per color in fixed period mode. "00" is 87.5 us, "01" is 1.4 ms, "10" is 22.4 ms, "11" is 179.2 ms. In manual setting mode, "00" is 175 μs, "01" is 2.8 ms, "10" is 44.8 ms, "11" is 368 ms. The integration time per color is set to multiple value (Adrs 01 & 02) with the period.
- Adrs 01 & 02: This is a multiple value setting in manual setting mode, and can be set to a minimum of 0x0000 and a maximum of 0xFFFF (65535). This is used to set how far to expand the integration time per color which specified by "Integration time setting" (Tint). For example, if you want to set the integration time per color to 546 ms, set 175 µs by Tint="00" and then set this register to N=3120 (0xC30).

Mode	Manual timing register	Integration time setting (Tint)								
	(Adrs 01 & 02)	00	01	10	11					
Fixed period mode	Disabled	87.5 μs	1.4 ms	22.4 ms	179.2 ms					
Manual setting mode	N	175 × N μs	2.8 × N ms	44.8 × N ms	358.4 × N ms					

Adrs 03 to 0A: These bytes are register for sensor data. S11059-01WT measurement result is stored in these registers when the I<sup>2</sup>C command is changed to read mode. The values are kept until next read cycle.

#### 🖶 Initial setting [Low gain, manual setting mode, Tint=00 (175 μs), integartion time 546 ms/ch]

Adra	Function	bit										
Adrs	Function	7	6	5	4	3	2	1	0	Hex		
00	Control	1	1	1	-	0	1	0	0	0xE4		
01	Manual timing register	0	0	0	0	1	1	0	0	0x0C		
02	Manual timing register	0	0	1	1	0	0	0	0	0x30		



### Program example

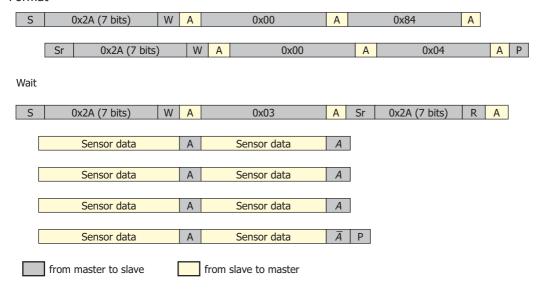
Condition 1: Initial setting [manual setting mode, low gain, Tint=00 (175 µs), integration time 546 ms/ch (0x0C30 is set in manual timing register)]

#### Command

Action				Data	body				Ack	Remark			
Address call (0x2A) S	0	1	0	1	0	1	0	W	Α	7-bit address			
Register call (0x00)	0	0	0	0	0	0	0	0	Α	Calls control byte			
Register write (0x84)	1	0	0	0	0	1	0	0	Α	ADC reset, standby disabled			
Address call (0x2A) Sr	0	1	0	1	0	1	0	W	Α	Restart, address			
Register call (0x00)	0	0	0	0	0	0	0	0	Α	Calls control byte			
Register write (0x04)	0	0	0	0	0	1	0	0	Α	P ADC reset disabled, bus release			
			V	Vait Ic	nger	than	integr	ation	time (	(>2184 ms)			
Address call (0x2A) S	0	1	0	1	0	1	0	W	Α	7-bit address			
Register call (0x03)	0	0	0	0	0	0	1	1	Α	Calls output data byte			
Address call (0x2A) Sr	0	1	0	1	0	1	0	R	Α	Changes to read mode			
Data read out (R: MSB)	Χ	Х	Х	Χ	Χ	Χ	Χ	Χ	Α	Red data output			
Data read out (R: LSB)	Χ	Х	Х	Χ	Χ	Χ	Χ	Χ	Α	Red data output			
Data read out (G: MSB)	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Green data output			
Data read out (G: LSB)	Χ	Х	Χ	Χ	Χ	Χ	Χ	Χ	Α	Green data output			
Data read out (B: MSB)	Χ	Х	Χ	Χ	Χ	Χ	Χ	Χ	Α	Plue data output			
Data read out (B: LSB)	Χ	Х	Х	Χ	Χ	Χ	Χ	Χ	Α	Blue data output			
Data read out (Infrared: MSB)	Χ	Х	Χ	Χ	Χ	Χ	Χ	Χ	Α	Infrared data output			
Data read out (Infrared: LSB)	X	Х	Χ	Χ	Χ	Χ	Χ	Χ	Ā	P			

S=Start condition, Sr=Restart condition, A=Acknowledge, A=Acknowledge by host, P=Stop condition, R=Read mode (1), W=Write mode (0),  $\overline{A}$ =not acknowledge

#### ■ Format





#### S11059-01WT

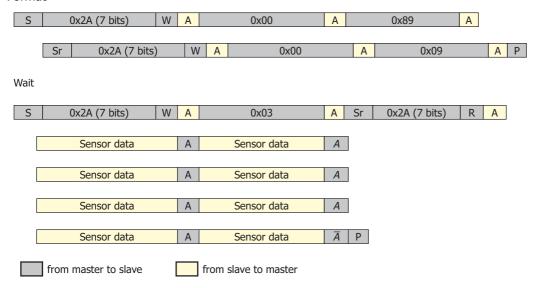
Condition 2 [fixed period mode, high gain, Tint=01 (1.4 ms), integration time 1.4 ms/ch]

#### Command

Action					Data	body	,			Ack	Remark
Address call (0x2A)	S	0	1	0	1	0	1	0	W	Α	7-bit address
Register call (0x00)		0	0	0	0	0	0	0	0	Α	Calls control byte
Register write (0x89)		1	0	0	0	1	0	0	1	Α	ADC reset, standby disabled
Address call (0x2A)	Sr	0	1	0	1	0	1	0	W	Α	7-bit address
Register call (0x00)		0	0	0	0	0	0	0	0	Α	Calls control byte
Resistor write (0x09)		0	0	0	0	1	0	0	1	Α	P ADC reset disabled, bus release
Wait long	er t	han ir	ntegra	ation	time	(> 5.	6 ms	). Wit	hin tl	his pe	riod, repeat measurement is continued.
Address call (0x2A)	S	0	1	0	1	0	1	0	W	Α	7-bit address
Register call (0x03)		0	0	0	0	0	0	1	1	Α	Calls output data byte
Address call (0x2A)	Sr	0	1	0	1	0	1	0	R	Α	Changes to read mode
Data read out (R: MSB)		Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Red data output
Data read out (R: LSB)		Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Red data output
Data read out (G: MSB)		Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Green data output
Data read out (G: LSB)		Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Green data output
Data read out (B: MSB)		Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Blue data output
Data read out (B: LSB)		Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	
Data read out (Infrared: MS	SB)	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Χ	Α	Infrared data output
Data read out (Infrared: LS	SB)	Χ	Χ	Χ	X	Χ	Χ	X	Χ	Ā	P

S=Start condition, Sr=Restart condition, A=Acknowledge, A=Acknowledge by host, P=Stop condition, R=Read mode(1), W=Write mode(0),  $\overline{A}$ =not acknowledge

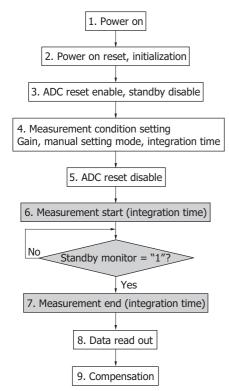
#### ■ Format





#### S11059-01WT

## Compensation method for sensitivity variation



Sensitivity variation can be decreased using the compensation coefficient which is calculated from the integration time measurement result. Explanation of compensation method is shown as follows.

#### Integration time measurement method

In case of integration time measurement, it is necessary to set manual setting mode. The integration time measurement starts after "ADC reset" disabled. To measure the finishing integration time (measurement) Tmeas, check "Standby monitor" bit until it becomes to "1".

### Compensation method

The sensitivity compensation that used integration time is as follows:

$$K = \frac{Tset}{Tmeas}$$
$$S' = S \cdot K$$

K : compensation coefficient
 Tset : integration time (setting)
 Tmeas: integration time (measurement)
 S : photo sensitivity (measurement)
 S' : photo sensitivity (compensation)

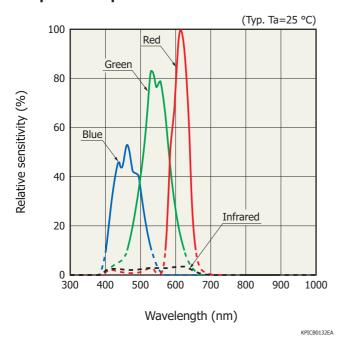
## Measurement accuracy of integration time

The measurement minimum resolution of Tmeas is defined by the looping duration (Tunit). In case of default setting, the Tset is 2184 ms and assuming the Tunit to 7.8 ms, the accuracy of integration time is calulated by following formula.

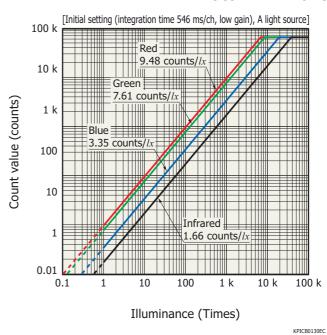
$$\frac{\text{Tunit}}{\text{Tset}} \times 100 = \frac{7.8}{2184} \times 100 = 0.36\%$$

The specification of compensated sensitivity is defined as 0.36% accuracy.

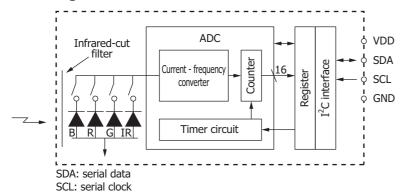
#### Spectral response



## - Count value vs. illuminance (typical example)

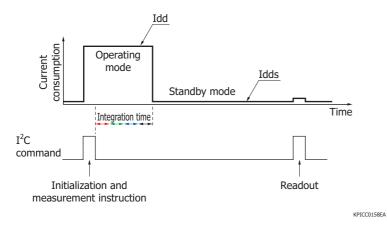


## **Block diagram**

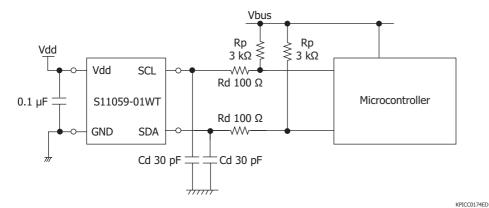


KPICC0152EA

## Timing chart of standby function



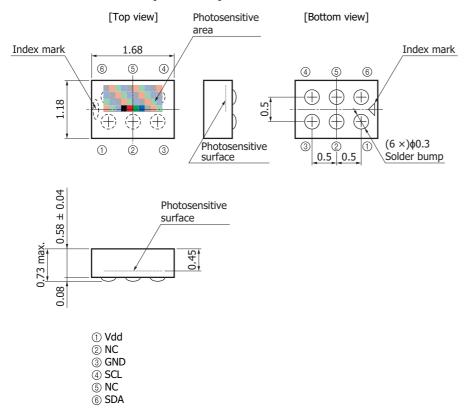
## - Connection example

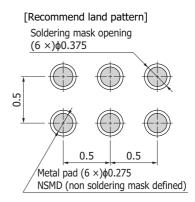


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#### S11059-01WT

## Dimensional outline (unit: mm)

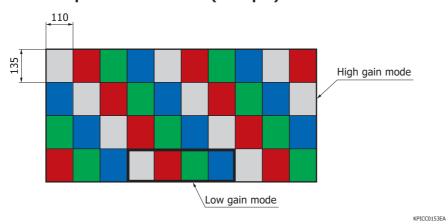




KPICA0081EC

## **Details** of photosensitive area (unit: μm)

Tolerance unless otherwise noted: ±0.05



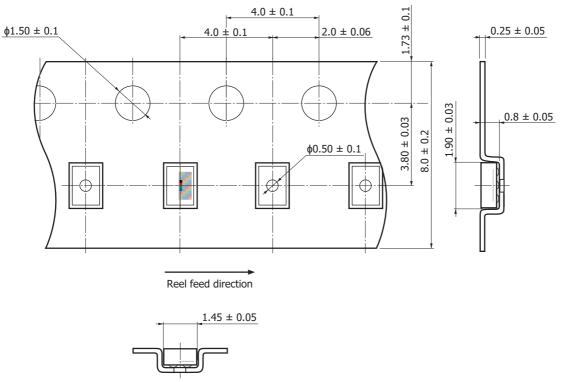
Note: When using this product, please request our technical information (S11059 series) and ensure appropriate design according to the information.

## **Standard packing specifications**

■ Reel (conforms to JEITA ET-7200)

Dimension	Hub diameter	Tape width	Material	Electrostatic characteristics
180 mm	60 mm	8 mm	PS (polystyrene)	-

■ Embossed tape (unit: mm, material: PS, conductive)

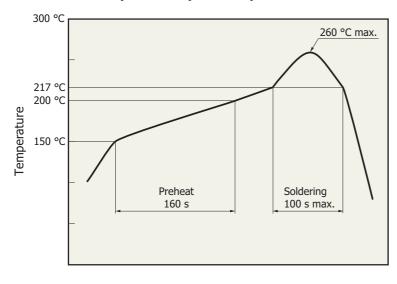


KPICC0196EA

- Packing quantity 3000 pcs/reel
- Packing type

Reel and desiccant in moisture-proof packaging (vacuum-sealed)

## Measured example of temperature profile with our hot-air reflow oven for product testing



Time

KPICB0168EB

- This product supports lead-free soldering. After unpacking, store it in an environment at a temperature of 30 °C or less and a humidity of 60% or less, and perform soldering within 1 month.
- The effect that the product receives during reflow soldering varies depending on the circuit board and reflow oven that are used. Before actual reflow soldering, check for any problems by tesiting out the reflow soldering methods in advance.

#### Line-up of RGB color sensors

Type no.	Туре	Photosensitive area (mm)	Package (mm)	wa	sensitivity velength nm)			Photose	nsit	ivity	,	Photo
			$4 \times 4.8 \times 1.8^{t}$	В	460	В	( ) / [ ]					
S9032-02	Photodiode	ф2.0	6 pin	G	540	G		0.23 (A/W				
			(filter 0.75 <sup>t</sup> )	R	620	R		0.16 (A/W) [λ=620 nm]				
			$3 \times 4 \times 1.3^{t}$	В	460	В		0.18 (A/W) [λ=460 nm]				
S9702	Photodiode	$1.0 \times 1.0$	4 pin	G	540	G		0.23 (A/W				( Interest
			(filter 0.75 <sup>t</sup> )	R	620	R		• •		[λ=620 nm]		
			$3 \times 1.6 \times 1.0^{t}$	В	460	В		0.2 (A/W)				
S10917-35GT	Photodiode	$1.0 \times 1.0$	COB	G	540	G		0.23 (A/W			_	
			(on-chip filter)	R	620	R		0.17 (A/W	<u> </u>			
			$3 \times 1.6 \times 1.0^{t}$			В		0.21 (A/W				
S10942-01CT	10942-01CT Photodiode	$1.0 \times 1.0$	COB (on-chip filter)		*	G		0.25 (A/W				
						R		0.45 (A/W	) [x			
	Digital		$4 \times 4.8 \times 1.8^{t}$	В	465	>	В	0.21 (LSB/lx)	ے	В	1.9 (LSB/lx)	
S9706	photo IC	1.2 × 1.2	6 pin	G	540	Low	G	0.45 (LSB/lx)	High	G	4.1 (LSB/lx)	
			(filter 0.75 <sup>t</sup> )	R	615		R	0.64 (LSB/lx)		R	5.8 (LSB/ <i>lx</i> )	JEEL.
	Digital		$3.43 \times 3.8 \times 1.6^{t}$			>	В	0.3 (LSB/lx)	ے	В	2.6 (LSB/lx)	4
S11012-01CR	photo IC	1.2 × 1.2	COB		*	Low	G	0.6 (LSB/lx)	High	G	5.3 (LSB/ <i>lx</i> )	
	p000 10		(on-chip filter)				R	1.4 (LSB/lx)		R	12.9 (LSB/lx)	
	$I^2C$		$3 \times 4.2 \times 1.3^{t}$	В	460		В	4.4 (count/lx)		В	44.8 (count/lx)	
S11059-02DT	compatible	0.56 × 1.22	10 pin	G	530	Low	G	8.3 (count/lx)	High	G	85.0 (count/lx)	
/-03DS	color	0.00 ** 2.122	(on-chip filter)	R	615	Ľ	R	11.2 (count/lx)	工	R	117.0 (count/lx)	
	sensor		(	IR	855		IR	3.0 (count/ <i>lx</i> )		IR	30.0 (count/lx)	
	I <sup>2</sup> C		$1.68 \times 1.18 \times 0.58^{t}$	В	460		В	3.35 (count/ <i>lx</i> )		В	31.7 (count/lx)	
S11059-01WT	compatible	1.22 × 0.56	WL-CSP		G	7.61 (count/lx)	High	G	76.2 (count/lx)			
	color		(on-chip filter)		615	ĭ	R	9.48 (count/ <i>lx</i> )	王	R	94.5 (count/lx)	
	sensor		(	IR	855		IR	1.66 (count/lx)		IR	15.3 (count/lx)	

<sup>\*</sup> Refer to the spectral response of each product's datasheet.

## Related information

www.hamamatsu.com/sp/ssd/doc en.html

- Precautions
  - · Disclaimer
  - · Metal, ceramic, plastic package products
- · Surface mount type products

Information described in this material is current as of August, 2015.

Product specifications are subject to change without prior notice due to improvements or other reasons. This document has been carefully prepared and the information contained is believed to be accurate. In rare cases, however, there may be inaccuracies such as text errors. Before using these products, always contact us for the delivery specification sheet to check the latest specifications.

The product warranty is valid for one year after delivery and is limited to product repair or replacement for defects discovered and reported to us within that one year period. However, even if within the warranty period we accept absolutely no liability for any loss caused by natural disasters or improper product use. Copying or reprinting the contents described in this material in whole or in part is prohibited without our prior permission.

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