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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

HAT1097R, HAT1097RJ

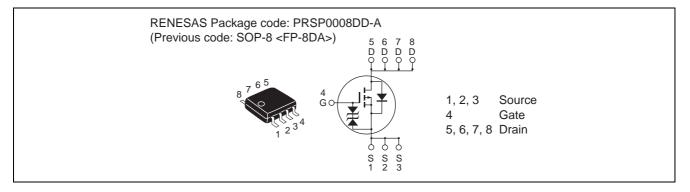
Silicon P Channel Power MOS FET High Speed Power Switching

> REJ03G0529-0100 Rev.1.00 Feb.15.2005

Features

- Low on-resistance
- Capable of 4.5 V gate drive
- High density mounting
- "J" is for Automotive application High temperature D-S leakage guarantee Avalanche rating

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

ltem	Symbol	Rat	Unit		
	Symbol	HAT1097R	HAT1097RJ	Unit	
Drain to source voltage	V _{DSS}	-60	-60	V	
Gate to source voltage	V _{GSS}	±20	±20	V	
Drain current	I _D	-5	-5	А	
Drain peak current	I _D (pulse) ^{Note1}	-40	-40	А	
Avalanche current	I _{AP} ^{Note3}	—	-5	А	
Avalanche energy	E _{AR} ^{Note3}	—	2.14	mJ	
Channel dissipation	Pch ^{Note2}	2	2	W	
Channel temperature	Tch	150	150	°C	
Storage temperature	Tstg	-55 to +150	-55 to +150	°C	

Notes: 1. $PW \leq 10 \mu s, \, duty \, cycle \leq 1\%$

2. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW \leq 10 s

3. Value at Tch = 25°C, Rg \geq 50 Ω



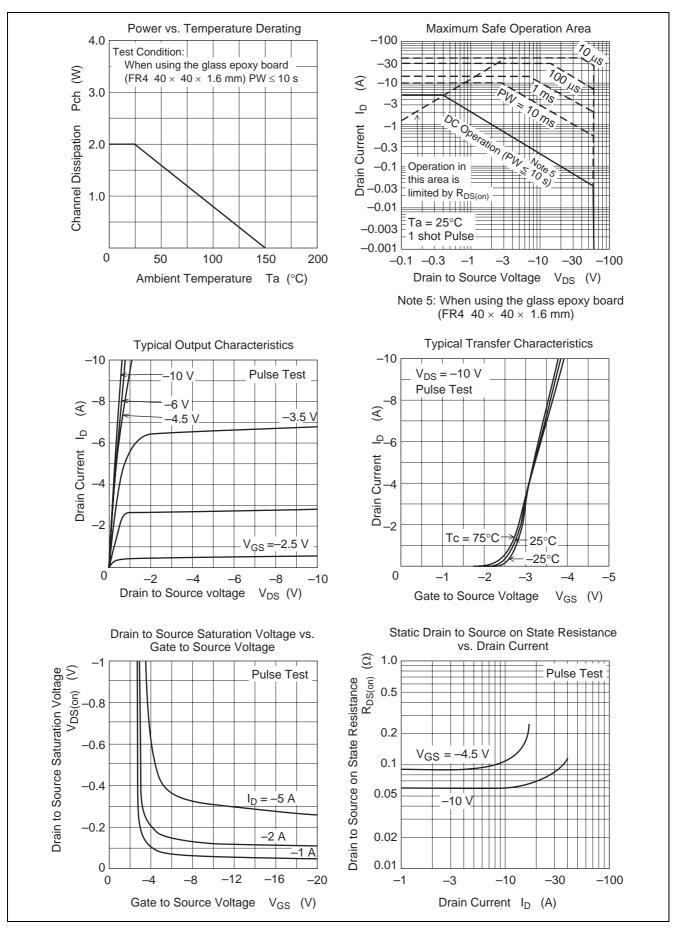
Electrical Characteristics

Item		Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage		V _{(BR)DSS}	-60	—	—	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to Source breakdown voltage		V _{(BR)GSS}	±20	—	—	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Zero gate voltage drain current		I _{DSS}	_	—	-1	μA	$V_{DS} = -60 \text{ V}, \text{ V}_{GS} = 0$
Zero gate voltage drain current	HAT1097R	I _{DSS}	_	—	—	μA	$V_{DS} = -48 \text{ V}, V_{GS} = 0$
	HAT1055RJ	I _{DSS}	—	—	-10	μA	Ta = 125°C
Gate to source leak current		I _{GSS}	—	—	±10	μA	$V_{GS} = \pm 16 V, V_{DS} = 0$
Gate to source cutoff voltage		V _{GS(off)}	-1.0	—	-2.5	V	$V_{DS} = -10 \text{ V}, \text{ I}_{D} = -1 \text{ mA}$
Forward transfer admittance		y _{fs}	3	5	—	S	$I_D = -2.5 \text{ A}^{\text{Note4}}, V_{DS} = -10 \text{ V}$
Static drain to source on state		R _{DS(on)}	_	60	76	mΩ	$I_D = -2.5 \text{ A}^{\text{Note4}}, V_{\text{GS}} = -10 \text{ V}$
resistance		R _{DS(on)}	_	90	130	mΩ	$I_D = -2.5 \text{ A}^{\text{Note4}}, V_{\text{GS}} = -4.5 \text{ V}$
Input capacitance		Ciss	_	1350	—	pF	$V_{DS} = -10 V, V_{GS} = 0$
Output capacitance		Coss	_	135	—	pF	f = 1 MHz
Reverse transfer capacitance		Crss	_	85	—	pF	
Total gate charge		Qg	_	21	—	nC	V _{DD} = -25 V
Gate to source charge		Qgs	_	3	—	nC	$V_{GS} = -10 V$
Gate to drain charge		Qgd	_	4	—	nC	$I_D = -5 A$
Turn-on delay time		td(on)	_	20	—	ns	$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -2.5 \text{ A}$
Rise time		tr		15	_	ns	$V_{DD} \cong -30 \text{ V}$
Turn-off delay time		td(off)		55	_	ns	$R_{L} = 12 \Omega$ $R_{G} = 4.7 \Omega$
Fall time		tf		10	_	ns	
Body-drain diode forward voltage		V _{DF}	_	-0.85	-1.10	V	$I_F = -5 \text{ A}, V_{GS} = 0^{Note4}$
Body-drain diode reverse recovery		trr	_	25	_	ns	$I_F = -5 A, V_{GS} = 0$
time							diF/dt = 100 A/µs

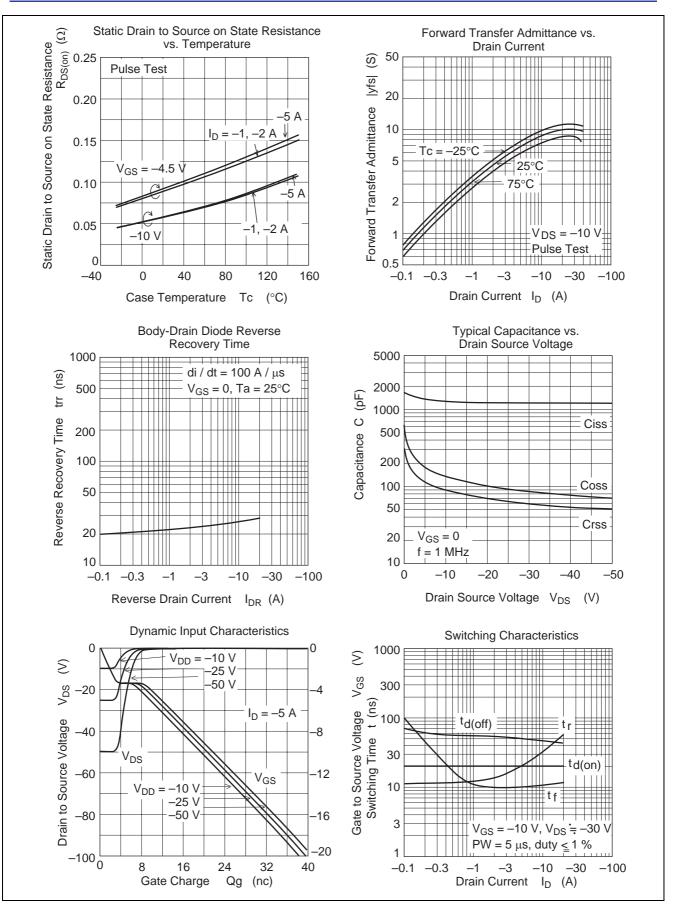
Notes: 4. Pulse test



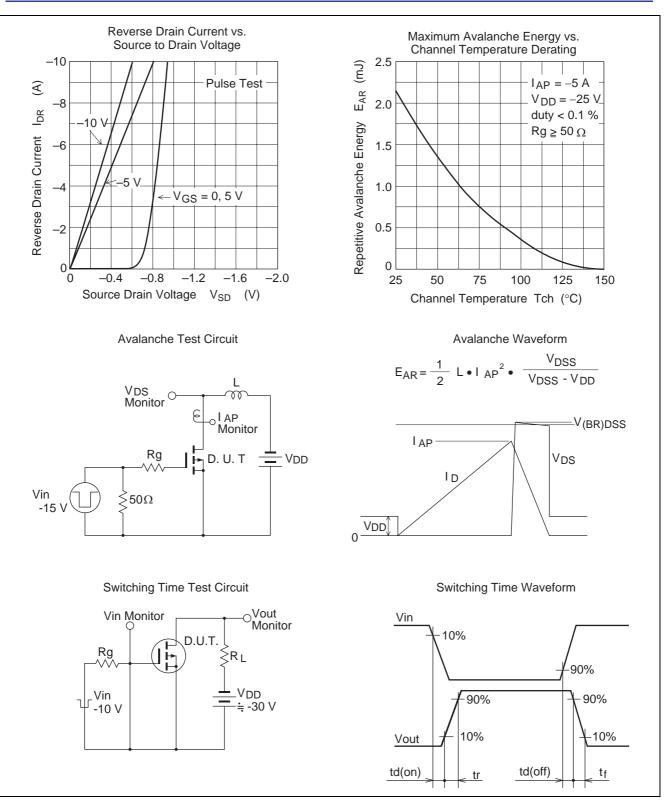
Main Characteristics

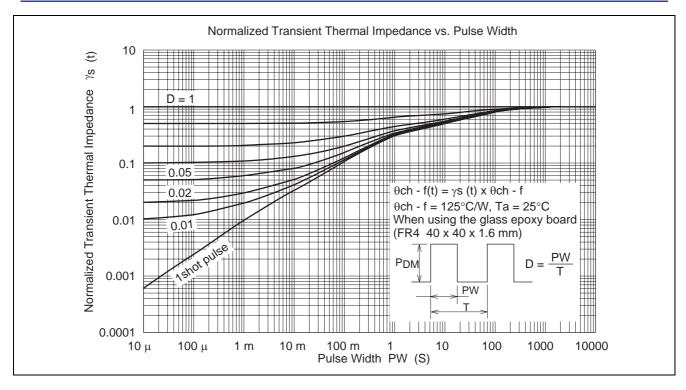






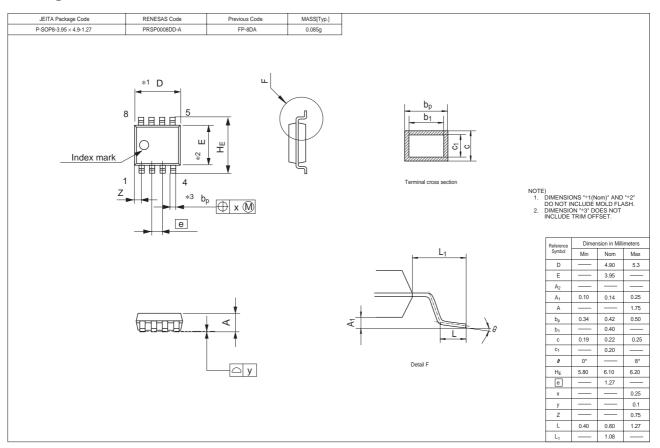








Package Dimensions



Ordering Information

Part Name	Quantity	Shipping Container
HAT1097R-EL-E	2500 pcs.	Taping
HAT1097RJ-EL-E	2500 pcs.	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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