

HAT1091C

Silicon P Channel MOS FET Power Switching

R07DS1172EJ0500

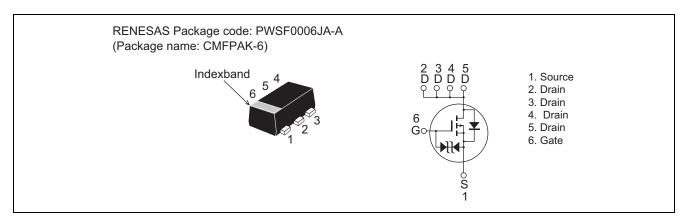
(Previous: REJ03G1229-0400) Rev.5.00

Mar 19, 2014

Features

- Low on-resistance $R_{DS(on)} = 134 \ m\Omega \ typ. \ (at \ V_{GS} = -4.5 \ V)$
- Low drive current.
- 2.5 V gate drive devices.
- High density mounting

Outline



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Ratings	Unit
Drain to Source voltage	V _{DSS}	-20	V
Gate to Source voltage	V _{GSS}	±12	V
Drain current	I _D	-1.5	A
Drain peak current	I _{D(pulse)} Note1	-6	A
Body - Drain diode reverse drain current	I _{DR}	-1.5	A
Channel dissipation	Pch ^{Note 2}	830	mW
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes 1. PW \leq 10 μ s, duty cycle \leq 1%

2. When using the glass epoxy board. (FR4 $40 \times 40 \times 1.6$ mm), Ta = 25° C

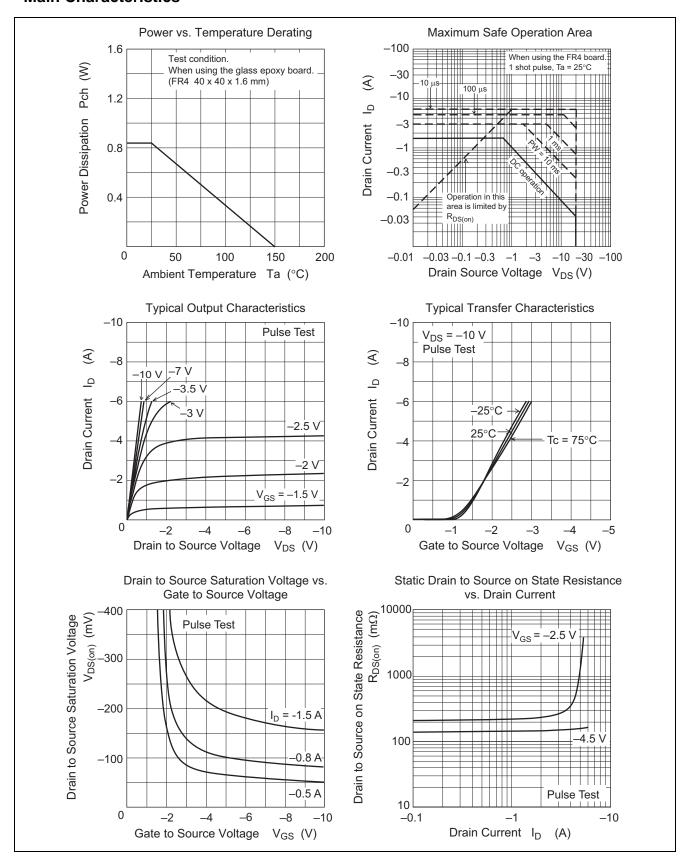
Electrical Characteristics

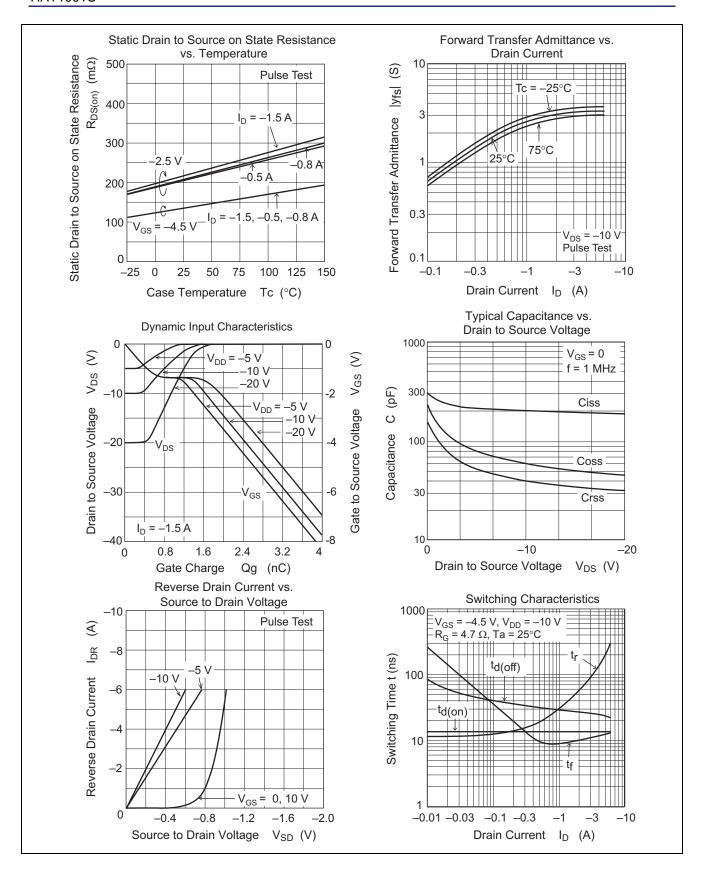
 $(Ta = 25^{\circ}C)$

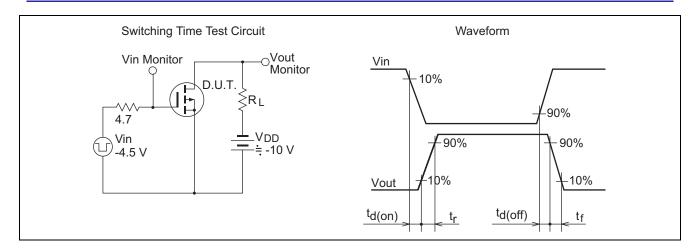
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to Source breakdown	V _{(BR)DSS}	-20	_	_	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
voltage						
Gate to Source breakdown	$V_{(BR)GSS}$	±12			V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
voltage						
Gate to Source leakage current	I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 10 \text{ V}, V_{DS} = 0$
Drain to Source leakage current	I _{DSS}	—	—	-1	μΑ	$V_{DS} = -20 \text{ V}, V_{GS} = 0$
Gate to Source cutoff voltage	$V_{GS(th)}$	-0.4	—	-1.4	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Drain to Source on state	R _{DS(on)}	_	134	175	mΩ	$I_D = -0.8 \text{ A}, V_{GS} = -4.5 \text{ V}^{\text{Note3}}$
resistance	R _{DS(on)}	_	205	287	mΩ	$I_D = -0.7 \text{ A}, V_{GS} = -2.5 \text{ V}^{\text{Note3}}$
Forward transfer admittance	y _{fs}	1.5	2.3	_	S	$I_D = -0.8 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	200	_	pF	$V_{DS} = -10 \text{ V}, V_{GS} = 0,$
Output capacitance	Coss	_	60	_	pF	f = 1 MHz
Reverse transfer capacitance	Crss	_	40	_	pF	
Total gate charge	Qg	_	2.6	_	nC	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V},$
Gate to Source charge	Qgs	_	0.7	_	nC	I _D = -1.5 A
Gate to Drain charge	Qgd	_	0.7	_	nC	
Turn - on delay time	t _{d(on)}	_	13	_	ns	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V},$
Rise time	t _r	_	26	_	ns	$I_D = -0.8 \text{ A}, R_L = 12.5 \Omega,$
Turn - off delay time	t _{d(off)}	_	30	_	ns	$R_g = 4.7 \Omega$
Fall time	t _f	_	9	_	ns	
Body - Drain diode forward voltage	V_{DF}	_	-0.85	-1.1	V	I _F = -1.5 A, V _{GS} = 0

Notes: 3. Pulse test

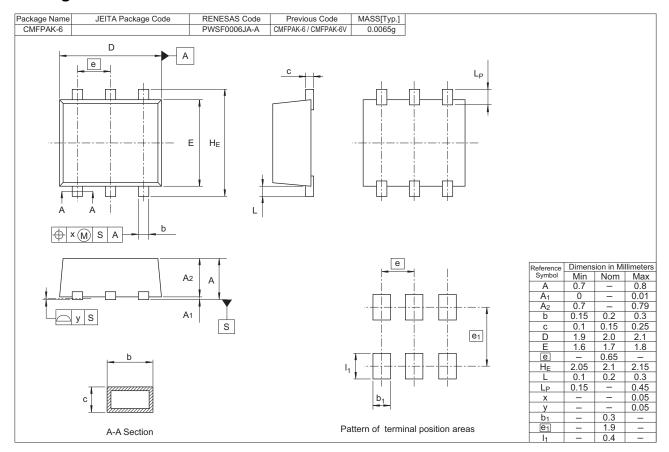
Main Characteristics







Package Dimensions



Ordering Information

Orderable Part Number	Quantity	Shipping Container
HAT1091C-EL-E	3000 pcs	Taping

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.

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