CMLT3410 NPN CMLT7410 PNP CMLT3474 NPN/PNP

SURFACE MOUNT SILICON DUAL, LOW V_{CE}(SAT) TRANSISTORS



www.centralsemi.com

DESCRIPTION:

These CENTRAL SEMICONDUCTOR dual devices are low $V_{\text{CE}(SAT)}$ silicon transistors in an SOT-563 surface mount package designed for small signal general purpose amplifier and switching applications requiring low collector emitter saturation voltage.

MARKING CODES: CMLT3410: C34

CMLT7410: C74 CMLT3474: C37



MAXIMUM RATINGS: (TA=25°C)	SYMBOL		UNITS
Collector-Base Voltage	V_{CBO}	40	V
Collector-Emitter Voltage	V_{CEO}	25	V
Emitter-Base Voltage	V_{EBO}	6.0	V
Continuous Collector Current	I _C	1.0	Α
Peak Collector Current	I _{CM}	1.5	Α
Power Dissipation	P_{D}	350	mW
Operating and Storage Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	Θ_{JA}	357	°C/W

ELECTRICAL CHARACTERISTICS PER TRANSISTOR: (T_A=25°C unless otherwise noted)

			<u>NPN</u>	PNP		
SYMBOL	TEST CONDITIONS	MIN	TYP	TYP	MAX	UNITS
ICBO	V _{CB} =40V				100	nA
^I EBO	V _{EB} =6.0V				100	nA
BV _{CBO}	I _C =100μA	40				V
BVCEO	I _C =10mA	25				V
BVEBO	I _E =100μA	6.0				V
V _{CE} (SAT)	I _C =50mA, I _B =5.0mA		25	30	50	mV
V _{CE} (SAT)	I _C =100mA, I _B =10mA		40	50	75	mV
VCE(SAT)	I _C =200mA, I _B =20mA		80	95	150	mV
VCE(SAT)	I _C =500mA, I _B =50mA		190	205	250	mV
VCE(SAT)	I _C =800mA, I _B =80mA		290	320	400	mV
VCE(SAT)	I _C =1.0A, I _B =100mA		360	400	450	mV
V _{BE} (SAT)	I _C =800mA, I _B =80mA				1.1	V
V _{BE(ON)}	V _{CE} =1.0V, I _C =10mA				0.9	V
hFE	V _{CE} =1.0V, I _C =10mA	100				
hFE	V _{CE} =1.0V, I _C =100mA	100			300	
hFE	V _{CE} =1.0V, I _C =500mA	100				
h _{FE}	V _{CE} =1.0V, I _C =1.0A	50				
f_{T}	V _{CE} =10V, I _C =50mA, f=100MHz	100				MHz
C _{ob}	V _{CB} =10V, I _E =0, f=1.0MHz (CMLT3410)		6.0		10	pF
C _{ob}	V _{CB} =10V, I _E =0, f=1.0MHz (CMLT7410)			10	15	pF

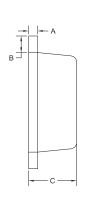
R5 (29-June 2015)

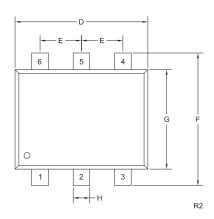
CMLT3410 NPN CMLT7410 PNP CMLT3474 NPN/PNP

SURFACE MOUNT SILICON DUAL, LOW V_{CE}(SAT) TRANSISTORS



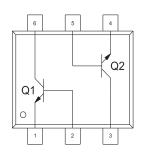
SOT-563 CASE - MECHANICAL OUTLINE



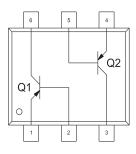


DIMENSIONS						
	INCHES		MILLIMETERS			
SYMBOL	MIN	MAX	MIN	MAX		
Α	0.0027	0.007	0.07	0.18		
В	0.008		0.20			
С	0.017	0.024	0.45	0.60		
D	0.059	0.067	1.50	1.70		
E	0.020		0.50			
F	0.059	0.067	1.50	1.70		
G	0.043	0.051	1.10	1.30		
Н	0.006	0.012	0.15	0.30		
SOT-563 (REV: R2)						

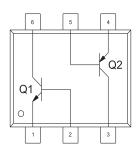
PIN CONFIGURATIONS



MARKING CODE: CMLT3410: C34



MARKING CODE: CMLT7410: C74



MARKING CODE: CMLT3474: C37

LEAD CODE:

- 1) Emitter Q1
- 2) Base Q1
- 3) Collector Q2
- 4) Emitter Q2
- 5) Base Q2
- 6) Collector Q1

R5 (29-June 2015)

CMLT3410 NPN CMLT7410 PNP CMLT3474 NPN/PNP

SURFACE MOUNT SILICON DUAL, LOW V_{CE(SAT)} TRANSISTORS



SERVICES

- · Bonded Inventory
- · Custom Electrical Screening
- Custom Electrical Characteristic Curves
- SPICE Models
- Custom Packaging
- Package Base Options
- Custom Device Development/Multi Discrete Modules (MDM™)
- · Bare Die Available for Hybrid Applications

LIMITATIONS AND DAMAGES DISCLAIMER: In no event shall Central be liable for any collateral, indirect, punitive, incidental, consequential, or exemplary damages in connection with or arising out of a purchase order or contract or the use of products provided hereunder, regardless of whether Central has been advised of the possibility of such damages. Excluded damages shall include, but not be restricted to: cost of removal or reinstallation, rework, ancillary costs to the procurement of substitute products, loss of profits, loss of savings, loss of use, loss of data, or business interruption. No claim, suit, or action shall be brought against Central more than two (2) years after the related cause of action has occurred.

In no event shall Central's aggregate liability from any warranty, indemnity, or other obligation arising out of or in connection with a purchase order or contract, or any use of any Central product provided hereunder, exceed the total amount paid to Central for the specific products sold under a purchase order or contract with respect to which losses or damages are claimed. The existence of more than one (1) claim against the specific products sold to Buyer under a purchase order or contract shall not enlarge or extend this limit.

Buyer understands and agrees that the foregoing liability limitations are essential elements of a purchase order or contract and that in the absence of such limitations, the material and economic terms of the purchase order or contract would be substantially different.

R5 (29-June 2015)