

P-Channel Enhancement Mode Field Effect Transistor

Description

Line current interrupter in telephone sets Relay High speed and line transformer drivers.

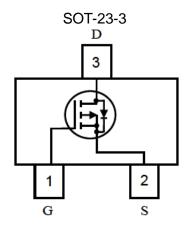
Features

- V_{DS}(V)=-50V, ID=-0.13A
- $R_{DS(ON)} < 10\Omega@V_{GS} = -5V$
- Voltage controlled p-channel small signal switch
- High density cell design for low R_{DS(ON)}

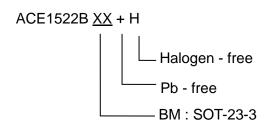
Absolute Maximum Ratings

Abootato maximam ratingo								
Parameter	Symbol	Max	Unit					
Drain-Source Voltage	V_{DS}	-50	V					
Gate-Source Voltage	V_{GS}	±20	V					
Drain Current (Continuous)	I _D	-0.13	Α					
Drain Current (Pulse)	I _D	-0.52						
Power Dissipation ⁽¹⁾	P _D	0.35	W					
Operating and Storage Temperature Range	$T_{J,}T_{STG}$	-55 to 150	°С					

Packaging Type



Ordering information





ACE1522B

P-Channel Enhancement Mode Field Effect Transistor

Electrical Characteristics

T_A=25 °C unless otherwise noted

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
	On/Off	characteristics				
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	V _{GS} =0V, I _D =-250uA	-50			V
Zero Gate Voltage Drain Current	I _{DSS}	V_{DS} =-50V, V_{GS} =0V			-15	uA
		V_{DS} =-50V, V_{GS} =0V, T_{J} =125°C			-60	
Gate Leakage Current	I _{GSS}	$V_{GS}=\pm20V$, $V_{DS}=0V$			±10	nA
	On ch	aracteristics ^b				
Static Drain-Source On-Resistance	R _{DS(ON)}	V _{GS} =-5V, I _D =-0.1A			10	Ω
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}$, $I_{D}=-1mA$	-0.8	-1.75	-2	V
Forward Transconductance	g FS	V _{DS} =-25V, I _D =-0.1A	0.05	0.6		S
	Switching	characteristics b				
Total Gate Charge	Qg			0.9	1.3	nC
Gate-Source Charge	Q_gs	V_{DS} =-25V, I_{D} =-0.1A V_{GS} =-5V		0.2		
Gate-Drain Charge	Q_{gd}	V _{GS} =-3 V		0.3		
Turn-On Delay Time	T _{d(on)}			2.5	5	ns
Turn-On Rise Time	t _f	V_{DD} =-30V, I_{D} =-0.27A,		6.3	13	
Turn-Off Delay Time	t _{d(off)}	V_{GS} =-10V R_{GEN} =6 Ω		10	20	
Turn-Off Fall Time	t _f	TKGEN-022		4.8	9.6	
	Dynamic	c characteristics				
Input Capacitance	C _{iss}	V _{DS} =-10V, V _{GS} =0V		33		
Output Capacitance	C _{oss}			38		pF
Reverse Transfer Capacitance	C _{rss}	f=200KHz		36		
Gate Resistance	R_{G}	V _{GS} =-15mV, f=1.0MHz		9		Ω
Drain-source of	diode chara	cteristics and maximum	ratings	b		
Drain-Source Diode Forward Voltage	V _{SD}	V _{GS} =0V, I _S =-0.26A (2)		-0.8	-1.2	V

Note: 1. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design.

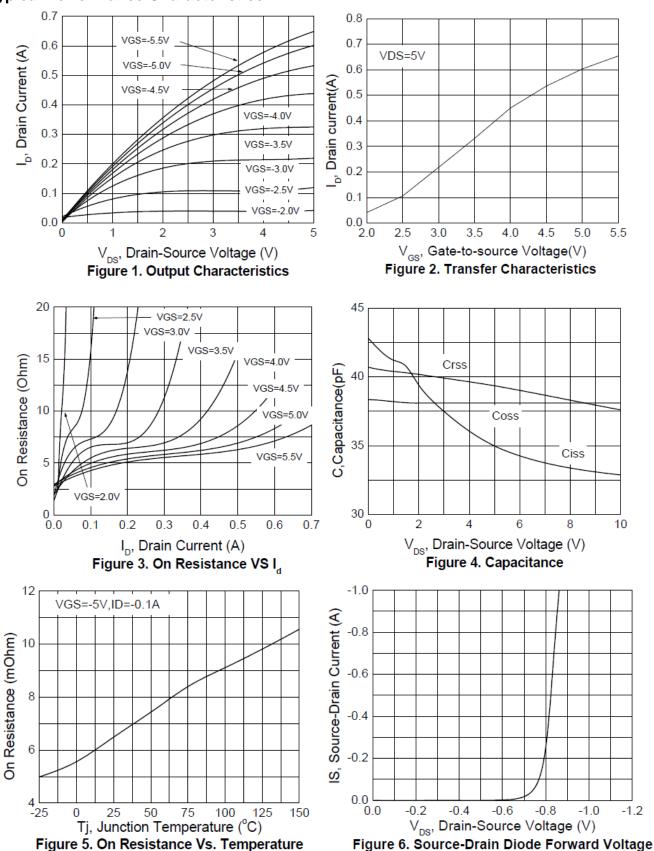
^{2.} Pulse Test: Pulse Width ≤ 300µs, Duty Cycle ≤ 2.0%





P-Channel Enhancement Mode Field Effect Transistor

Typical Performance Characteristics

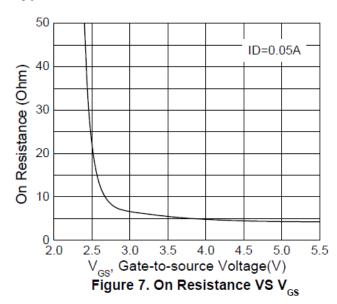




ACE1522B

P-Channel Enhancement Mode Field Effect Transistor

Typical Performance Characteristics



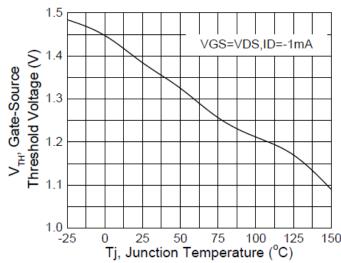


Figure 8. Gate Thershold Vs. Temperature

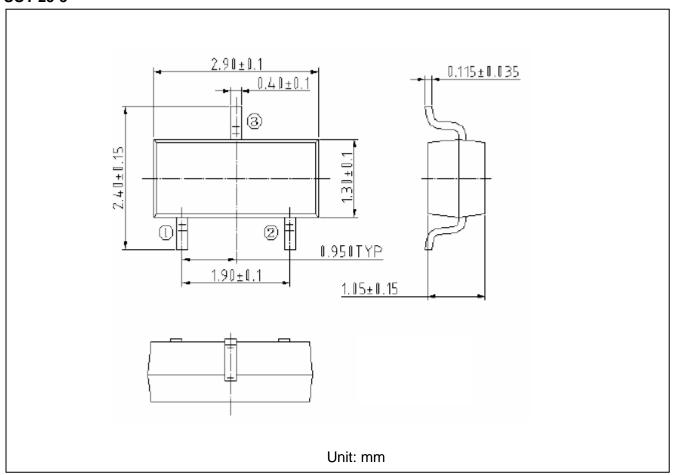




P-Channel Enhancement Mode Field Effect Transistor

Packing Information

SOT-23-3





ACE1522B

P-Channel Enhancement Mode Field Effect Transistor

Notes

ACE does not assume any responsibility for use as critical components in life support devices or systems without the express written approval of the president and general counsel of ACE Electronics Co., LTD. As sued herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and shoes failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

ACE Technology Co., LTD. http://www.ace-ele.com/