

# New Jersey Semi-Conductor Products, Inc.

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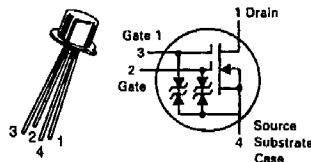
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## MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	25	Vdc
Gate-Source Voltage	$V_{GS}$	$\pm 7.0$	Vdc
Drain Current	$I_D$	30	mAdc
Gate Current	$I_G$	10	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	300	mW
Operating and Storage Channel Temperature Range	$T_{\text{channel}}, T_{\text{stg}}$	-65 to +175	°C

## MFE140

TO-72



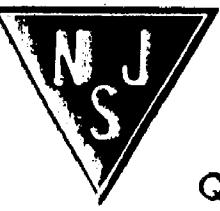
## DUAL-GATE MOSFET FM AMPLIFIER

N-CHANNEL — DEPLETION

ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$  unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
<b>OFF CHARACTERISTICS</b>					
Drain-Source Breakdown Voltage ( $I_D = 10 \mu\text{Adc}, V_S = 0, V_{G1} = -4.0 \text{ Vdc}, V_{G2} = +4.0 \text{ Vdc}$ )	$V_{(\text{BR})\text{DSX}}$	25	—	—	Vdc
Gate 1-Source Breakdown Voltage ( $I_{G1} = \pm 10 \mu\text{Adc}, V_{G2S} = 0$ )	$V_{(\text{BR})\text{G1SO}}$	$\pm 7.0$	—	$\pm 20$	Vdc
Gate 2-Source Breakdown Voltage ( $I_{G2} = \pm 10 \mu\text{Adc}, V_{G1S} = 0$ )	$V_{(\text{BR})\text{G2SO}}$	$\pm 7.0$	—	$\pm 20$	Vdc
Gate 1 Leakage Current ( $V_{G1S} = \pm 6.0 \text{ Vdc}, V_{G2S} = 0, V_{DS} = 0$ )	$I_{G1SS}$	—	—	20	nAdc
Gate 2 Leakage Current ( $V_{G2S} = \pm 6.0 \text{ Vdc}, V_{G1S} = 0, V_{DS} = 0$ )	$I_{G2SS}$	—	—	20	nAdc
Gate 1 to Source Cutoff Voltage ( $V_{DS} = 15 \text{ Vdc}, V_{G2S} = 4.0 \text{ Vdc}, I_D = 200 \mu\text{Adc}$ )	$V_{G1S(\text{off})}$	—	—	-4.0	Vdc
Gate 2 to Source Cutoff Voltage ( $V_{DS} = 15 \text{ Vdc}, V_{G1S} = 0, I_D = 200 \mu\text{Adc}$ )	$V_{G2S(\text{off})}$	—	—	-4.0	Vdc
<b>ON CHARACTERISTICS</b>					
Zero-Gate-Voltage Drain Current ( $V_{DS} = 15 \text{ Vdc}, V_{G2S} = 0, V_{G1S} = 4.0 \text{ Vdc}$ )	$I_{DSS}$	3.0	10	30	mAdc
<b>SMALL-SIGNAL CHARACTERISTICS</b>					
Forward Transfer Admittance (Gate 1 connected to Drain) ( $V_{DS} = 15 \text{ Vdc}, V_{G2S} = 4.0 \text{ Vdc}, I_D = 10 \mu\text{Adc}, f = 1.0 \text{ kHz}$ )	$ y_{rs} $	10	—	20	mmhos
Input Capacitance ( $V_{DS} = 15 \text{ Vdc}, V_{G2S} = 4.0 \text{ Vdc}, I_D = I_{DSS}, f = 1.0 \text{ MHz}$ )	$C_{iss}$	—	4.5	7.0	pF
Reverse Transfer Capacitance ( $V_{DS} = 15 \text{ Vdc}, V_{G1S} = 4.0 \text{ Vdc}, I_D = I_{DSS}, f = 1.0 \text{ MHz}$ )	$C_{rss}$	—	0.023	0.05	pF
Output Capacitance ( $V_{DS} = 15 \text{ Vdc}, V_{G1S} = 4.0 \text{ Vdc}, I_D = I_{DSS}, f = 1.0 \text{ MHz}$ )	$C_{oss}$	—	2.6	4.0	pF
<b>FUNCTIONAL CHARACTERISTICS</b>					
Noise Figure (Figure 8) (See Test Circuit in Figure 11)	NF	—	2.5	3.5	dB
Common Source Power Gain (Figure 7) (See Test Circuit in Figure 11)	$G_{ps}$	20	23	—	dB
Level of Unwanted Signal for 1.0% Cross Modulation (Figure 10) (See Test Circuit in Figure 11)	—	—	45	—	mV

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**MFE140**

 ELECTRICAL CHARACTERISTICS (continued) ( $T_A = 25^\circ\text{C}$  unless otherwise noted.)

Characteristic	Symbol	Min	Typ	Max	Unit
Common-Source Conversion Power Gain (Gate 1 or Gate 2 Injection, Figure 12) (See Test Circuit in Figure 13) (Signal Frequency = 100 MHz, Local Oscillator Frequency = 110.7 MHz)	$G_c$	15	18.5	—	dB
1/2 I.F. Rejection (See Test Circuit in Figure 13)	$1/2 f_{REJ}$	—	50	—	dB

 COMMON-SOURCE ADMITTANCE PARAMETERS  
( $V_{DS} = 15 \text{ Vdc}$ ,  $V_{G2S} = 4.0 \text{ Vdc}$ ,  $I_D = 6.0 \text{ mAdc}$ )

FIGURE 1 – INPUT ADMITTANCE

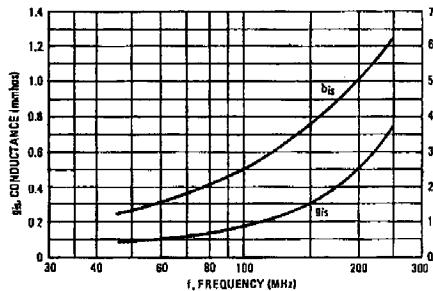


FIGURE 2 – REVERSE TRANSFER ADMITTANCE

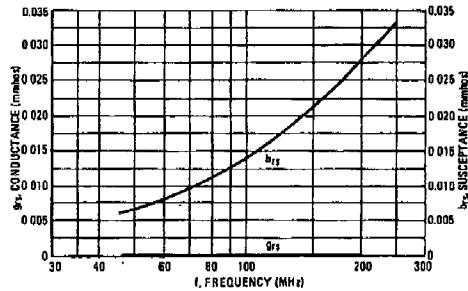


FIGURE 3 – FORWARD TRANSFER ADMITTANCE

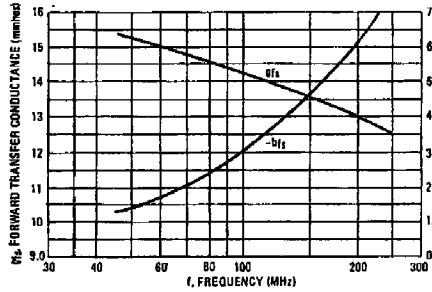


FIGURE 4 – OUTPUT ADMITTANCE

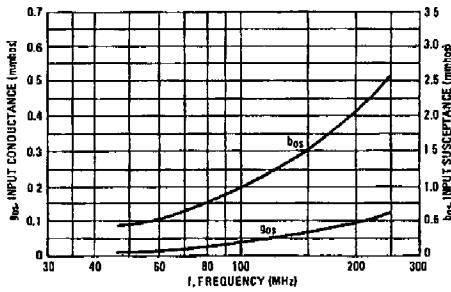

 FORWARD TRANSFER ADMITTANCE  
( $V_{DS} = 15 \text{ Vdc}$ ,  $f = 1.0 \text{ kHz}$ )

FIGURE 5 – GATE 1

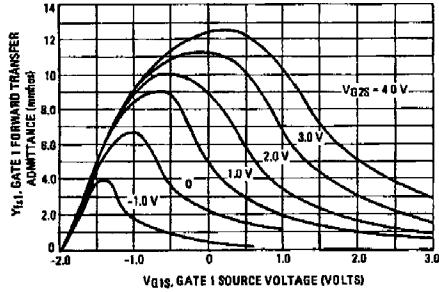


FIGURE 6 – GATE 2

