

9 bit Level Shifter/Transceiver With 3 State Outputs (Previous: REJ03D0300-0500) Rev.6.00 May 30, 2014

Description

The HD151015 is an IC which consists of 9 bus transceivers (three state output) in a 24 pin package. Signals are transmitter from A to B when the direction control input (DiR) is at a high level, and from B to A when DiR is at a low level. When the enable input ( $\overline{G}$ ) is high, A and B are isolated. And this product has two terminals ( $V_{CCA}$ ,  $V_{CCB}$ ),  $V_{CCA}$  is connected with control input and A bus side,  $V_{CCB}$  is connected with B bus side.  $V_{CCA}$  and  $V_{CCB}$  are isolated. Consequently, it is best to change the level in case of two supply voltage coexist on one board and application of power management.

### Features

- This product function as level shift transceiver that change V<sub>CCA</sub> input level to V<sub>CCB</sub> output level, V<sub>CCB</sub> input level to V<sub>CCA</sub> output level by providing different supply voltages to V<sub>CCA</sub> and V<sub>CCB</sub>.
- This product is able to the power management : Turn on and off the supply on  $V_{CCB}$  side with providing the supply of  $V_{CCA}$ .

(Enable input  $(\overline{G})$  : High level)

- Inputs and outputs are CMOS level, and the power dissipation is the same as CMOS standard logic.
- Wide operating supply voltage range:
  - $V_{CCA} = V_{CCB} = 2$  to 6 V ( $V_{CCB} \ge V_{CCA} 0.5$  V)
- Wide operating temperature range: Ta = -40 to  $85^{\circ}C$
- Ordering Information

Part Name	Package Type	Package Code (Previous Code)	Package Abbreviation	Taping Abbreviation (Quantity)	
151015TEL-E	TSSOP-24 pin	PTSP0024JB-A (TTP-24DBV)	Т	EL (1,000 pcs/reel)	



### **Pin Arrangement**

V <sub>CCA</sub> 1		24 V <sub>CCB</sub>
DIR 2		23 G
A0 3		22 B0
A1 4		21 B1
A2 5		20 B2
A3 6		19 B3
A4 7		18 B4
A5 8		17 B5
A6 9		16 B6
A7 10		15 B7
A8 11		14 B8
GND 12		13 GND
	(Top view)	

## **Function Table**

Inputs		
G	DIR	Outputs
L	L	B data to A bus
L	Н	A data to B bus
Н	x	Z

H : High level

L : Low level

Z : High Impedance

X : Immaterial

### **Absolute Maximum Ratings**

ltem	Symbol	Rating	Unit	Conditions
Supply Voltage	V <sub>CCA</sub> , V <sub>CCB</sub>	–0.5 to +7.0	V	
Input Diode Current	I <sub>IK</sub>	-20	mA	V <sub>1</sub> = -0.5
		20	mA	$V_{I} = V_{CC} + 0.5$
Input Voltage	V <sub>IN</sub>	–0.5 to V <sub>CC</sub> + 0.5	V	
Output Diode Current	I <sub>ок</sub>	-50	mA	V <sub>O</sub> = -0.5
		50	mA	$V_{\rm O} = V_{\rm CC} + 0.5$
Output Voltage	V <sub>OUT</sub>	–0.5 to V <sub>CC</sub> + 0.5	V	
Output Current	lo	±50	mA	
VCC or Ground Current	I <sub>CC</sub> or I <sub>GND</sub>	±50	mA	per output pin
Storage Temperature	Tstg	–65 to + 150	°C	

Note: 1. The absolute maximum ratings are values which must not individually be exceeded, and furthermore, no two of which may be realized at the same time.



### **Recommended Operating Conditions**

Item	Symbol	Rating	Unit	Conditions
Supply voltage	V <sub>CCA, B</sub>	2.0 to 6.0	V	$V_{CCB} \ge V_{CCA} - 0.5 V$
Input voltage	V <sub>IN</sub>	0 to V <sub>CC</sub>	V	
Output voltage	V <sub>OUT</sub>	0 to V <sub>CC</sub>	V	
Operating Temperature	T <sub>A</sub>	-40 to +85	°C	
Input Rise and Fall Time* <sup>1</sup>	t <sub>r</sub> , t <sub>f</sub>	8	ns/V	V <sub>CC</sub> @3.0 V (Input DiR, G, A)
				V <sub>CC</sub> @4.5 V (Input B)
				V <sub>CC</sub> @5.5 V (Input B)

Note: 1. The item guarantees maximum limit when one input switches. Waveform: Refer to test circuit of switching characteristics.

## Logick Diagram



### **Electrical Characteristics**

							Ta = -40 to		Ta = -40 to		Ta = -40 to					
	Sym-	V <sub>CCA</sub>	V <sub>CCB</sub>	1	Га = 25	°C	85°C									
ltem	bol	(V)	(V)	Min	Тур	Max	Min	Max	Unit	Conditions						
Input Voltage	V <sub>IH</sub>	3.0	3.0	2.1	1.5	—	2.1	—	V	$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$						
		4.5	4.5	3.15	2.25	—	3.15	—								
		5.5	5.5	3.85	2.75	—	3.85	—								
	VIL	3.0	3.0	_	1.5	0.9	—	0.9	V	V <sub>OUT</sub> = 0.1 V	or V <sub>CC</sub> – 0.1 V					
		4.5	4.5	_	2.25	1.35	—	1.35								
		5.5	5.5	—	2.75	1.65	—	1.65								
Output	V <sub>OH</sub>	2.7	4.5	2.6	2.69	—	2.6	—	V	$V_{IN} = V_{IL} \text{ or } V_{I}$	<sub>н</sub> , I <sub>он</sub> = –50 µА	A* <sup>1</sup>				
Voltage		2.7	4.5	4.4	4.49	—	4.4	—		$V_{IN} = V_{IL} \text{ or } V_{IH}, I_{OH} = -50 \ \mu\text{A}$ B		В				
		2.7	4.5	2.3		—	2.2	—	V	V <sub>IN</sub> =	I <sub>он</sub> = –4 mA	A				
		2.7	4.5	3.9		—	3.8	—		$V_{IL}$ or $V_{IH}$	I <sub>он</sub> = –12 mA	В				
	V <sub>OL</sub>	2.7	4.5	_	0.001	0.1	—	0.1	V	$V_{IN} = V_{IL} \text{ or } V_{I}$	<sub>Η</sub> , I <sub>OL</sub> = 50 μΑ	A.B				
		2.7	4.5	_	_	0.32	—	0.37	V	$V_{IN} = V_{IL} \text{ or } V_{IH}, I_{OL} = 12 \text{ mA}$ A.B		A.B				
Input Current	I <sub>IN</sub>	3.3	5.5	_	_	±0.1	—	±1.0	μA	V <sub>IN</sub> = V <sub>CC</sub> or GND						
Off State	l <sub>oz</sub>	3.3	5.5	_	_	±0.5	—	±5.0	μA	$V_{IN}(\overline{G}) = V_{IH}, V_{IN} = V_{CC} \text{ or } GND,$		,				
Output										V <sub>OUT</sub> = V <sub>CC</sub> or GND						
Current																
Supply	I <sub>CCA.B</sub>	3.3	5.5		_	8.0	_	80	μA	V <sub>IN</sub> = V <sub>CC</sub> or GND						
Current	I <sub>CCA</sub>	5.5	0			8.0		80	μA	$V_{IN} = V_{CC}$ or GND, B Input OPEN						

Note: 1. A: Output A, B: Output B, A.B: Output A.B

## **Switching Characteristics**

		Ta = 25°C		Ta = –40 to 85°C				
		$V_{CCA} = 3.0 \text{ V}, V_{CCB} = 5.0 \text{ V} \text{ V}_{CC} = 2.7 \text{ V}, V_{CCB} = 4.5 \text{ V}$			, $V_{CCB} = 4.5 V$			
Item	Symbol	Min	Тур	Max	Min	Max	Unit	Conditions
Propagation Delay Time	t <sub>PLH</sub>	1.0	5.0	10.0	1.0	12.0	ns	$B\toA$
		1.0	5.0	10.0	1.0	12.0		$A \rightarrow B$
	t <sub>PHL</sub>	1.0	5.0	10.0	1.0	12.0	ns	$B\toA$
		1.0	5.0	10.0	1.0	12.0		$A \rightarrow B$
Output Enable Time	t <sub>ZH</sub>	1.0	8.0	16.0	1.0	20.0	ns	$\overline{G} \to A$
		1.0	8.0	16.0	1.0	20.0		$\overline{G} \to B$
	t <sub>ZL</sub>	1.0	9.0	16.0	1.0	20.0	ns	$\overline{G} \to A$
		1.0	9.0	16.0	1.0	20.0		$\overline{G} \to A$
Output Disable Time	t <sub>HZ</sub>	1.0	9.0	16.0	1.0	20.0	ns	$\overline{G} \to A$
		1.0	9.0	16.0	1.0	20.0		$\overline{G} \to B$
	t <sub>LZ</sub>	1.0	8.0	16.0	1.0	20.0	ns	$\overline{G} \to A$
		1.0	8.0	16.0	1.0	20.0		$\overline{G} \to B$

# Input and Output Equivalent Circuit





### Switching Time Test Method

### **Test Circuit**





#### Waveforms-1



#### Waveforms-2





### **Typical Characteristic Curves**

### Propagation Delay Times vs Power Supply ( $V_{CCA}$ , $V_{CCB}$ )



### Application

### For power management system (1)



#### For power management system (2) (Common bus line in different power system)



### [Cautions on using]

Please use this IC on condition of  $V_{CCA}$  usually ON, because if you use it on condition of  $V_{CCA}$  being OFF,  $V_{CCB}$  being ON, it will be troubled.



### **Package Dimensions**





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