Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS HA17903A Series

Dual Comparators

REJ03D0807-0100 Rev.1.00 Mar 10, 2005

Description

The HA17903A series products are comparators designed for general purpose, especially for power control systems.

These ICs operate from a single power-supply voltage over a wide range of voltages, and feature a reduced powersupply current since the supply current is independent of the supply voltage.

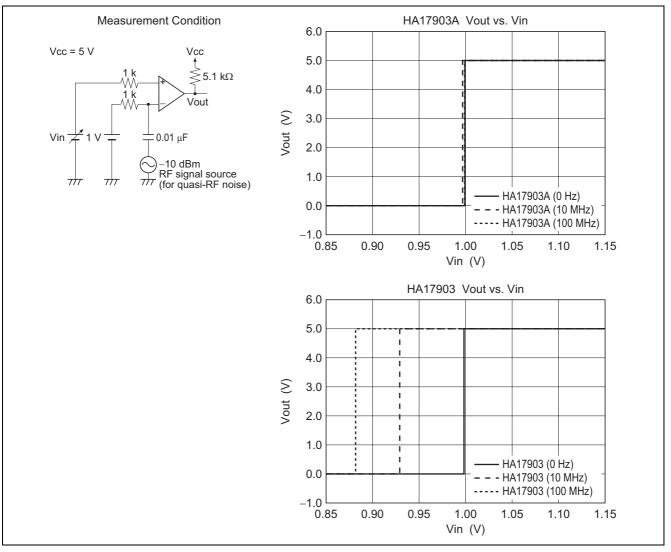
These comparators have the merit which ground is included in the common-mode input voltage range at a single-voltage power supply operation. These products have a wide range of applications, including limit comparators, simple A/D converters, pulse/square-wave/time delay generators, wide range VCO circuits, MOS clock timers, multivibrators, and high-voltage logic gates.

Features

- Wide power-supply voltage range : 2 to 36 V
- Very low supply current : 0.8 mA Typ.
- Low input bias current : 25 nA Typ.
- Low input offset current : 3 nA Typ.
- Low input offset voltage : 2 mV Typ.
- The common-mode input voltage range includes ground
- Output voltages compatible with CMOS logic systems



• Low electro-magnetic susceptibility

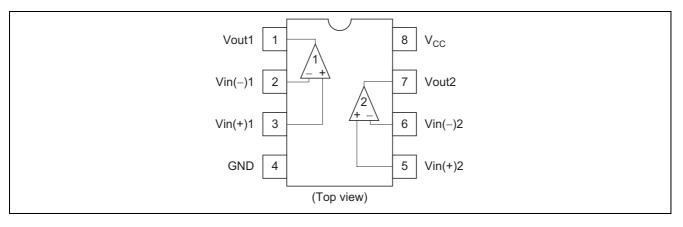


Ordering Information

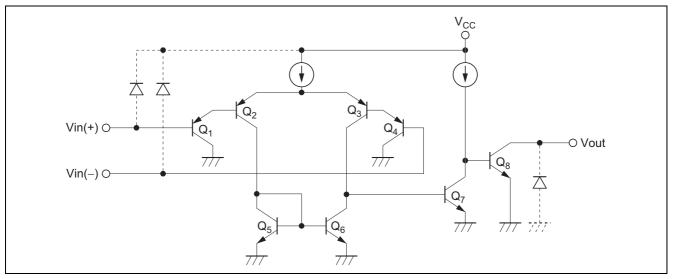
Type No.	Application	Package Name	Package Code
HA17903APS	Industry use	DIP-8 pin	PRDP0008AF-B
HA17903AFP		SOP-8 pin (JEITA)	PRSP0008DE-B
HA17903ARP		SOP-8 pin (JEDEC)	PRSP0008DD-C
HA17903AT		TSSOP-8 pin	PTSP0008JC-B



Pin Arrangement



Circuit Schematic (1/2)



Note: If Input/Output terminals voltage over the absolute maximum ratings, there is possibility of mis-operation, characteristics deterioration and destruction, because of the current's flowing to parasitic diode in IC. The Input/Output terminals are recommended to be protected with the clamp circuit which using the diode with low forward voltage (like schottky barrier diode) when there is a possibility for the Input/Output terminals voltage exceeds the absolute maximum ratings.



Absolute Maximum Ratings

				$(Ta = 25^{\circ}C)$
Item		Symbol Ratings		Unit
Power supply voltage		V _{CC}	36	V
Differential input voltage		Vin(diff)	±V _{CC}	V
Input voltage		Vin	-0.3 to +V _{CC}	V
Output pin voltage		Vout	–0.3 to +36	V
Output short current		los *1	constant	
Allowable power dissipation	DIP	PT	570 * ²	mW
	SOP		385 * ³	
	TSSOP		192 * ⁴	
Operating temperature		Topr	-40 to +85	°C
Storage temperature		Tstg	-55 to +125	°C

Notes: 1. Short circuit between the output and V_{CC} will be a cause to destory the circuit. The maximum output current is about 20 mA for any supply voltage.

These are the allowable values up to $Ta = 55^{\circ}C$. Derate by $8.3 \text{mW/}^{\circ}C$ above that temperature. 3. HA17903AFP/ARP:

These are the allowable values up to $Ta = 25^{\circ}C$ mounting in air. When it is mounted on glass epoxy board of 40 mm × 40 mm × 1.5 mmt with 30% wiring density, the allowable value is 570 mW up to Ta = 45° C. If Ta > 45° C, derate by 7.14 mW/°C.

 $V_{IN\;(-)} \geq 1V, \: V_{IN\;(+)} = 0, \: Iosink = 4mA$

 $V_{IN (-)} = 0, V_{IN (+)} \ge 1V, V_{O} = 5V$

4. HA17903AT: These are the allowable values up to Ta = 25° C. Derate by 1.92 mW/°C above that temperature.

						$(V_{CC} = 5 V, Ta = 25^{\circ}C)$			
ltem	Symbol	Min	Тур	Max	Unit	Test Conditions			
Input offset voltage *1	V _{IO}	—	2	5	mV				
Input offset current	l _{io}	—	3	50	nA	I _{IN (+)} - I _{IN (-)}			
Input bias current *2	I _{IB}	—	25	250	nA	I _{IN (+)} or I _{IN (-)}			
Common mode input voltage * ³	V _{CM} ⁺	3.5	—	—	V				
	V _{CM} ⁻	—	—	0	V				
Supply current	I _{CC}	—	0.8	2.0	mA	All comparators: $R_L = \infty$, All channels on			
Voltage gain * ⁵	A _{VD}	—	(200)	—	V/mV	V_{CC} = 15V, $R_L \ge 15k\Omega$			
Response time *4,5	t _R	—	(1.3)	—	μs	$V_{RL} = 5V, R_L = 5.1k\Omega$			
Large signal response time *5	t _{RI}		(300)		ns	V_{IN} = TTL Threshold width, V_{REF} = 1.4V			
Output sink current	I _{O(sink)}	6	16		mA	$V_{IN (-)} \ge 1V, V_{IN (+)} = 0, V_O \le 1.5V$			

Electrical Characteristics

Notes: 1. $V_{REF} = 1.4 \text{ V}$ and $R_S = 50 \Omega$, when $V_0 = 1.4 \text{ V}$ at output switching point.

V_{O(sat)}

I_{LO}

2. Under linear operation.

3. Common mode input voltage or each one of the input signal should not be less than -0.3 V.

(0.1)

400

mV

nA

4. This is a value to 100 mV input step voltage with 5 mV over drive.

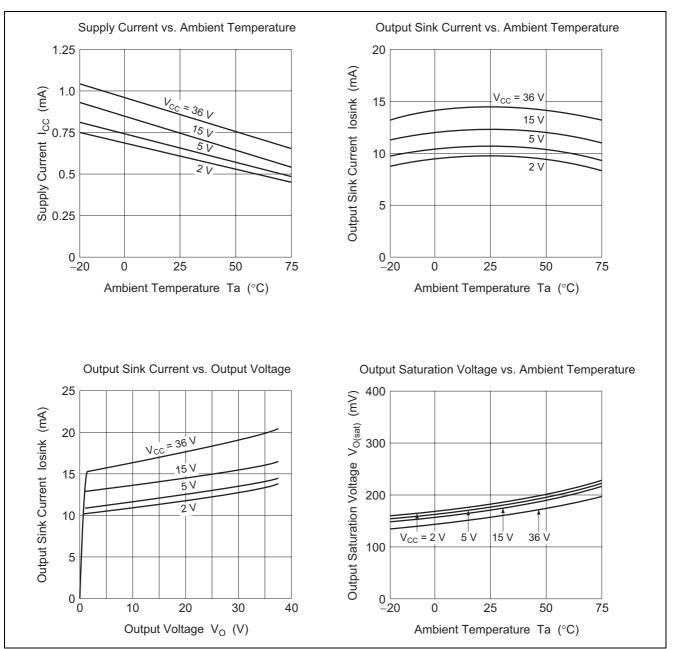
5. Design spec.

Output saturation voltage

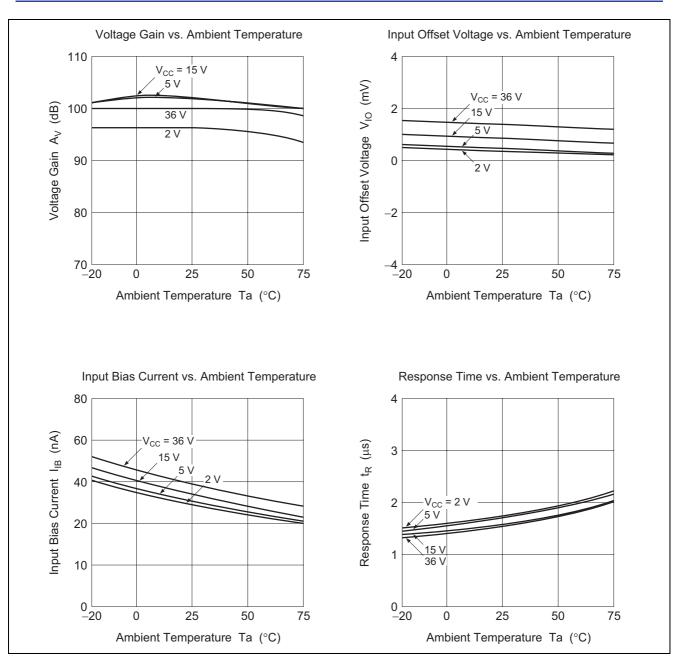
Output leak current *5

^{2.} HA17903APS:

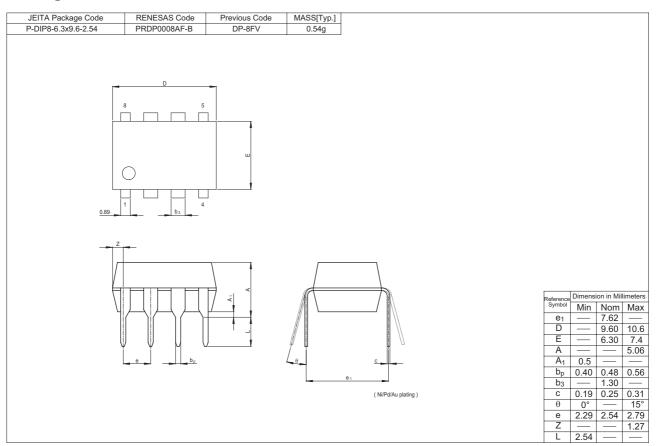
Characteristic Curves

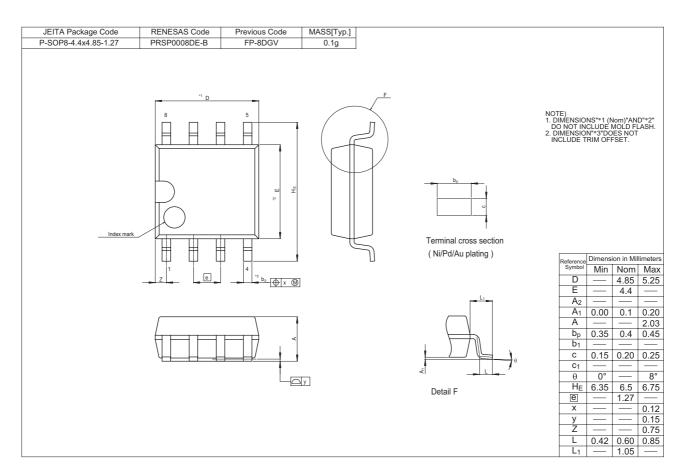






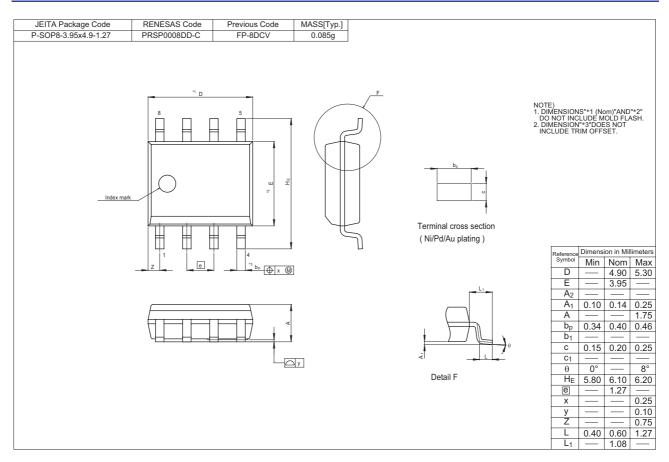
Package Dimensions

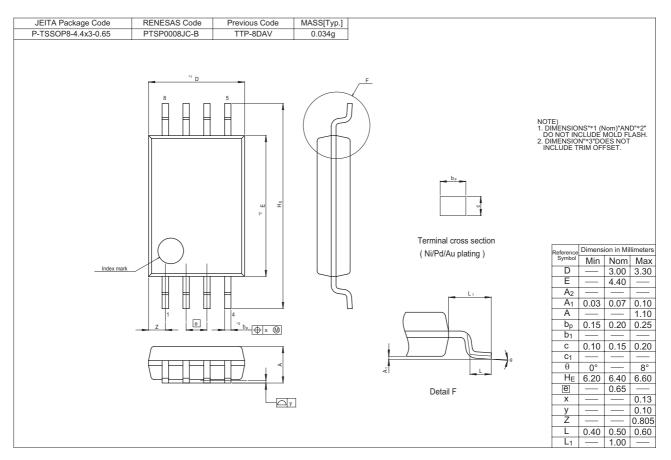






HA17903A Series







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