

## Digital Transistors (Built-in Resistors)

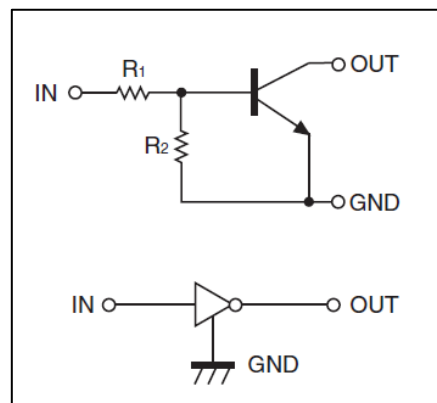
### DTC114EM/DTC114EE/DTC114EUA DTC114EKA /DTC114ECA/DTC114ESA

DIGITAL TRANSISTOR (NPN)

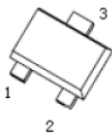
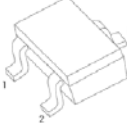
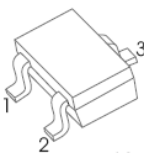
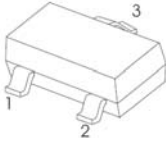
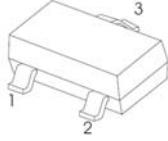

#### FEATURES

- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors(see equivalent circuit)
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input.They also have the advantage of almost completely eliminating parasitic effects
- Only the on/off conditions need to be set for operation, making device design easy

#### • Equivalent Circuit



#### PIN CONNENCTIONS and MARKING

<p><b>DTC114EM</b></p>  <p><b>SOT-723</b></p> <p>1. IN 2. GND 3. OUT</p> <p><b>MARKING:24</b></p>	<p><b>DTC114EE</b></p>  <p><b>SOT-523</b></p> <p>1. IN 2. GND 3. OUT</p> <p><b>MARKING:24</b></p>
<p><b>DTC114EUA</b></p>  <p><b>SOT-323</b></p> <p>1. IN 2. GND 3. OUT</p> <p><b>MARKING:24</b></p>	<p><b>DTC114EKA</b></p>  <p><b>SOT-23-3L</b></p> <p>1. IN 2. GND 3. OUT</p> <p><b>MARKING:24</b></p>
<p><b>DTC114ECA</b></p>  <p><b>SOT-23</b></p> <p>1. IN 2. GND 3. OUT</p> <p><b>MARKING:24</b></p>	<p><b>DTC114ESA</b></p>  <p><b>TO-92S</b></p> <p>1. GND 2. OUT 3. IN</p>

## MAXIMUM RATINGS(Ta=25°C unless otherwise noted)

Symbol	Parameter	Limits(DTC114E□)						Unit
		M	E	UA	KA	CA	SA	
V <sub>CC</sub>	Supply Voltage	50						V
V <sub>IN</sub>	Input Voltage	-10~+40						V
I <sub>O</sub>	Output Current	50						mA
I <sub>CM</sub>	Peak Collector Current	100						mA
P <sub>D</sub>	Power Dissipation	100	150	200		300		mW
T <sub>J</sub>	Junction Temperature	150						°C
T <sub>stg</sub>	Storage Temperature	-55~+150						°C

## ELECTRICAL CHARACTERISTICS (Ta=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Unit
Input voltage	V <sub>I(off)</sub>	V <sub>CC</sub> =5V, I <sub>O</sub> =100μA	0.5			V
	V <sub>I(on)</sub>	V <sub>O</sub> =0.3V, I <sub>O</sub> =10mA			3	V
Output voltage	V <sub>O(on)</sub>	I <sub>O</sub> /I <sub>I</sub> =10mA/0.5mA			0.3	V
Input current	I <sub>I</sub>	V <sub>I</sub> =5V			0.88	mA
Output current	I <sub>O(off)</sub>	V <sub>CC</sub> =50V, V <sub>I</sub> =0			0.5	μA
DC current gain	G <sub>I</sub>	V <sub>O</sub> =5V, I <sub>O</sub> =5mA	30			
Input resistance	R <sub>1</sub>		7	10	13	kΩ
Resistance ratio	R <sub>2</sub> /R <sub>1</sub>		0.8	1	1.2	
Transition frequency	f <sub>T</sub>	V <sub>O</sub> =10V, I <sub>O</sub> =5mA, f=100MHz		250		MHz

