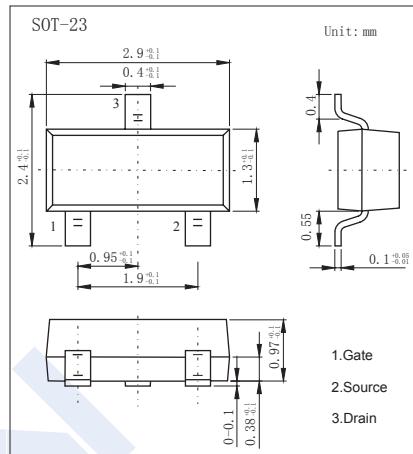
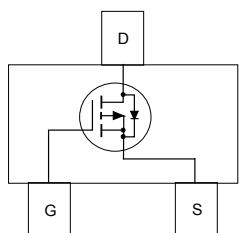


## P-Channel MOSFET

### FDN304P (KDN304P)

#### ■ Features

- $V_{DS} (V) = -20V$
- $I_D = -2.4A$  ( $V_{GS} = -4.5V$ )
- $R_{DS(ON)} < 52m\Omega$  ( $V_{GS} = -4.5V$ )
- $R_{DS(ON)} < 70m\Omega$  ( $V_{GS} = -2.5V$ )
- $R_{DS(ON)} < 100m\Omega$  ( $V_{GS} = -1.8V$ )



#### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	$V_{DS}$	-20	V
Gate-Source Voltage	$V_{GS}$	$\pm 8$	
Continuous Drain Current (Note.1)	$I_D$	-2.4	A
Pulsed Drain Current	$I_{DM}$	-10	
Power Dissipation (Note.1) (Note.2)	$P_D$	0.5 0.46	W
Thermal Resistance.Junction- to-Ambient (Note.1)	$R_{thJA}$	250	$^\circ C/W$
Thermal Resistance.Junction- to-Case	$R_{thJC}$	75	
Junction Temperature	$T_J$	150	
Junction Storage Temperature Range	$T_{stg}$	-55 to 150	$^\circ C$

Note.1:  $250^\circ C/W$  when mounted on a  $0.02 \text{ in}^2$  pad of 2 oz. copper.

Note.2:  $270^\circ C/W$  when mounted on a minimum pad.

**P-Channel MOSFET**  
**FDN304P (KDN304P)**

■ Electrical Characteristics  $T_a = 25^\circ\text{C}$

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	$V_{DSS}$	$I_D=-250 \mu\text{A}, V_{GS}=0\text{V}$	-20			V
Zero Gate Voltage Drain Current	$I_{DS}^0$	$V_{DS}=-16\text{V}, V_{GS}=0\text{V}$			-1	$\mu\text{A}$
Gate-Body leakage current	$I_{GSS}$	$V_{DS}=0\text{V}, V_{GS}=\pm 8\text{V}$			$\pm 100$	nA
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=-250 \mu\text{A}$ (Note.1)	-0.4		-1.5	V
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=-4.5\text{V}, I_D=-2.4\text{A}$ (Note.1)			52	$\text{m}\Omega$
		$V_{GS}=-2.5\text{V}, I_D=-2\text{A}$ (Note.1)			70	
		$V_{GS}=-1.8\text{V}, I_D=-1.8\text{A}$ (Note.1)			100	
On state drain current	$I_{D(on)}$	$V_{GS}=-4.5\text{V}, V_{DS}=-5\text{V}$	-10			A
Forward Transconductance	$g_{FS}$	$V_{DS}=-5\text{V}, I_D=1.25\text{A}$		12		S
Input Capacitance	$C_{iss}$	$V_{GS}=0\text{V}, V_{DS}=-10\text{V}, f=1\text{MHz}$		1312		$\text{pF}$
Output Capacitance	$C_{oss}$			240		
Reverse Transfer Capacitance	$C_{rss}$			106		
Total Gate Charge	$Q_g$	$V_{GS}=-4.5\text{V}, V_{DS}=-10\text{V}, I_D=-2.4\text{A}$		12	20	$\text{nC}$
Gate Source Charge	$Q_{gs}$			2		
Gate Drain Charge	$Q_{gd}$			2		
Turn-On DelayTime	$t_{d(on)}$	$V_{GS}=-4.5\text{V}, V_{DS}=-10\text{V}, I_D=-1\text{A}, R_G=6\Omega$		15	27	$\text{ns}$
Turn-On Rise Time	$t_r$			15	27	
Turn-Off DelayTime	$t_{d(off)}$			40	64	
Turn-Off Fall Time	$t_f$			25	40	
Maximum Body-Diode Continuous Current	$I_S$				-0.42	A
Diode Forward Voltage	$V_{SD}$	$I_S=-0.42\text{A}, V_{GS}=0\text{V}$			-1.2	V

Note.1: Pulse Test: Pulse Width  $\leq 300 \mu\text{s}$ , Duty Cycle  $\leq 2.0\%$

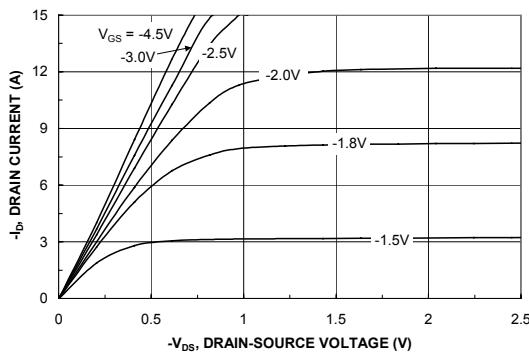
■ Marking

Marking	304
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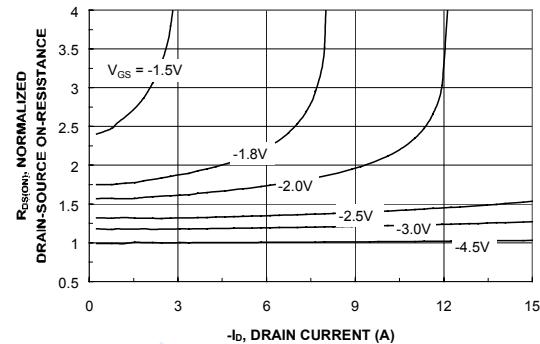
## P-Channel MOSFET

### FDN304P (KDN304P)

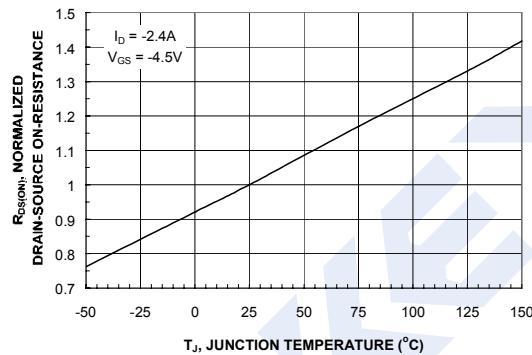
#### ■ Typical Characteristics



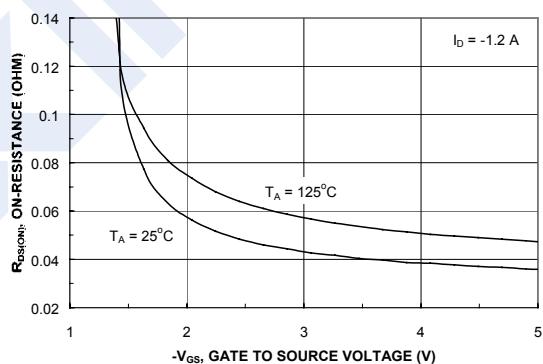
**Figure 1. On-Region Characteristics.**



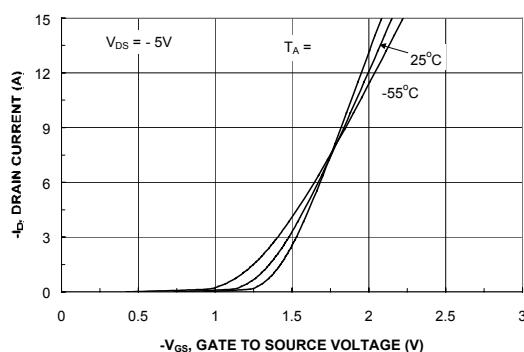
**Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.**



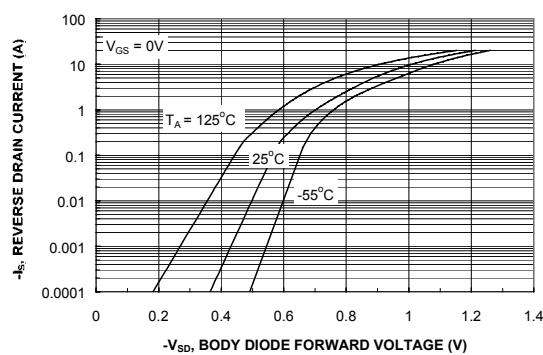
**Figure 3. On-Resistance Variation with Temperature.**



**Figure 4. On-Resistance Variation with Gate-to-Source Voltage.**



**Figure 5. Transfer Characteristics.**

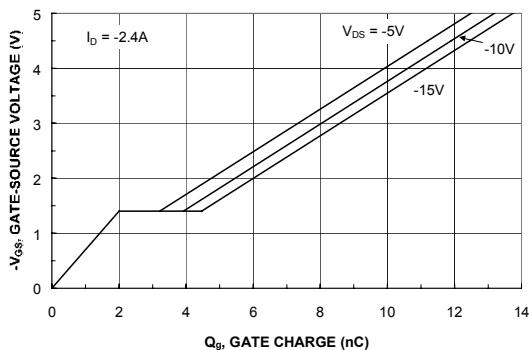


**Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.**

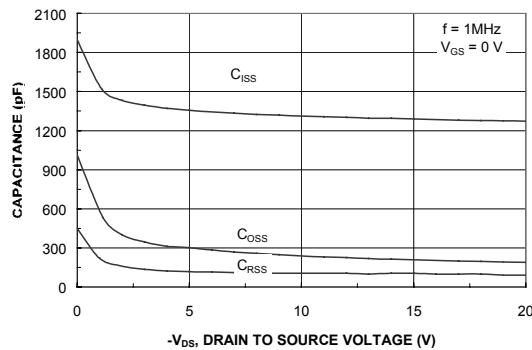
## P-Channel MOSFET

### FDN304P (KDN304P)

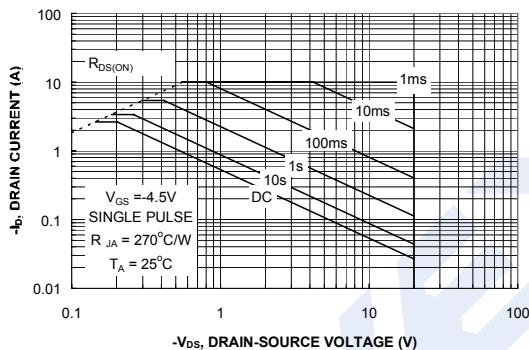
#### ■ Typical Characteristics



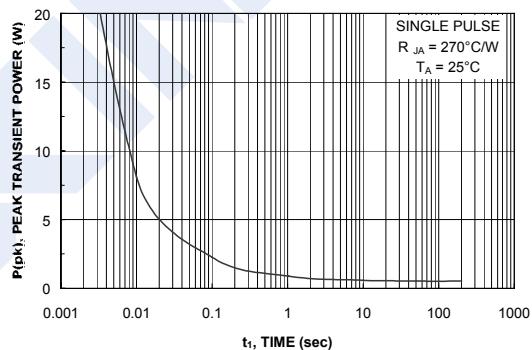
**Figure 7. Gate Charge Characteristics.**



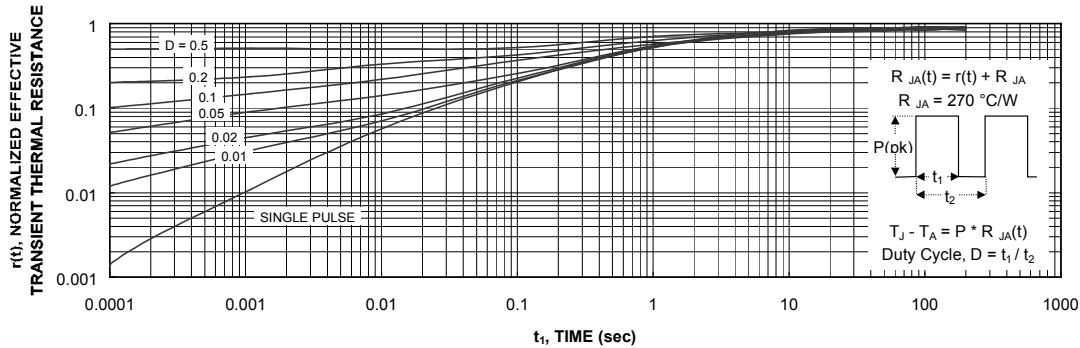
**Figure 8. Capacitance Characteristics.**



**Figure 9. Maximum Safe Operating Area.**



**Figure 10. Single Pulse Maximum Power Dissipation.**



**Figure 11. Transient Thermal Response Curve.**

Thermal characterization performed using the conditions described in Note 1b.  
Transient thermal response will change depending on the circuit board design.