

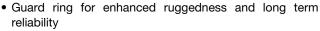
High Performance Schottky Rectifier, 200 A



PRODUCT SUMMARY				
I _{F(AV)}	200 A			
V_{R}	100 V			
Package	TO-244			
Circuit	Two diodes common cathodes			

FEATURES

- 175 °C T_J operation
- · Center tap module
- Low forward voltage drop
- High frequency operation







 Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-203CNQ.. center tap Schottky rectifier module series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature. Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES				
I _{F(AV)}	Rectangular waveform	200	А			
V_{RRM}		100	V			
I _{FSM}	$t_p = 5 \mu s sine$	12 800	А			
V _F	100 A _{pk} , T _J = 125 °C (per leg)	0.70	V			
T _J	Range	- 55 to 175	°C			

VOLTAGE RATINGS				
PARAMETER	SYMBOL	VS-203CNQ100PbF	UNITS	
Maximum DC reverse voltage	V_R	100	V	
Maximum working peak reverse voltage	V_{RWM}	100		

ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average	per leg		50 % duty cycle at T _C = 142 °C, rectangular waveform		100	А
forward current See fig. 5	per device	I _{F(AV)}			200	
Maximum peak one cycle non-repetitive surge current per leg See fig. 7		I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	12 800	А
			10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	1700	
Non-repetitive avalanche energy per leg E_{AS} $T_{J} = 25$ °C, $I_{AS} = 13$ A, $L = 0.2$ mH		nH	15	mJ		
Repetitive avalanche current per leg I _{AR}		Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		1	Α	

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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop per leg See fig. 1	V _{FM} ⁽¹⁾	100 A	T 05.00	0.86	V
		200 A	T _J = 25 °C	1.03	
		100 A	T 405.00	0.70	
		200 A	T _J = 125 °C	0.84	
Maximum reverse leakage current per leg See fig. 2	I _{RM} ⁽¹⁾	T _J = 25 °C	V - Pated V	3	- mA
		T _J = 125 °C	V _R = Rated V _R	40	
Threshold voltage	V _{F(TO)}	$T_J = T_J$ maximum		0.50	V
Forward slope resistance	r _t			1.08	mΩ
Maximum junction capacitance per leg	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		2650	pF
Typical series inductance per leg	L _S	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{^{(1)}\,}$ Pulse width < 300 µs, duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER		SYMBOL	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}	- 55	-	175	°C
Thermal resistance, junction to case	per leg	D	-	-	0.38	°C/W
	per module	R _{thJC}	-	-	0.19	
Thermal resistance, case to heatsink		R _{thCS}	-	0.10	-	
W-:-L				68		g
Weight			-	2.4]	oz.
Mounting torque			35.4 (4)	-	53.1 (6)	
Mounting torque center hole			30 (3.4)	-	40 (4.6)	lbf ⋅ in (N ⋅ m)
Terminal torque			30 (3.4)	-	44.2 (5)	
Vertical pull			-	-	80	- lbf ⋅ in
2" lever pull			-	-	35] IDI · III

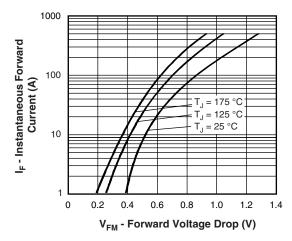


Fig. 1 - Maximum Forward Voltage Drop Characteristics (Per Leg)

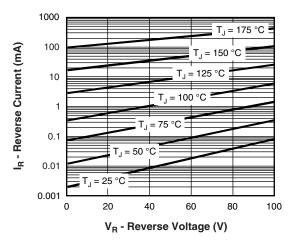


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage (Per Leg)



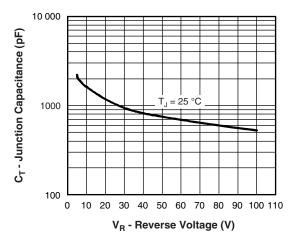


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

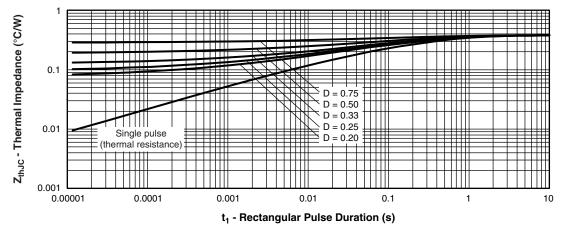


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)

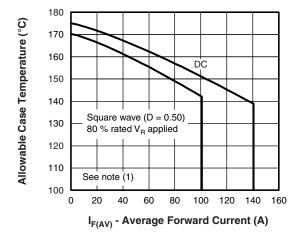


Fig. 5 - Maximum Allowable Case Temperature vs. Average Forward Current (Per Leg)

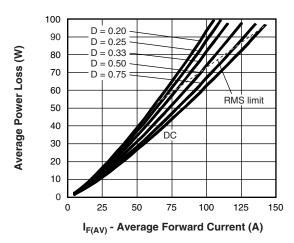
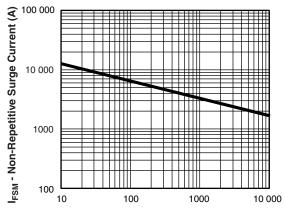


Fig. 6 - Forward Power Loss Characteristics (Per Leg)



t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current (Per Leg)

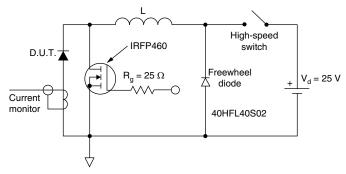


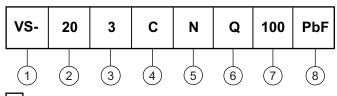
Fig. 8 - Unclamped Inductive Test Circuit

Note

 $\begin{array}{ll} \text{(1)} & \text{Formula used: } T_C = T_J - (\text{Pd} + \text{Pd}_{\text{REV}}) \times \text{R}_{\text{th,JC}}; \\ \text{Pd} & = \text{Forward power loss} = \text{I}_{F(AV)} \times \text{V}_{FM} \text{ at } (\text{I}_{F(AV)}/D) \text{ (see fig. 6)}; \\ \text{Pd}_{REV} & = \text{Inverse power loss} = \text{V}_{R1} \times \text{I}_{R} \text{ (1 - D); I}_{R} \text{ at V}_{R1} = 80 \text{ \% rated V}_{R} \\ \end{array}$

ORDERING INFORMATION TABLE

Device code



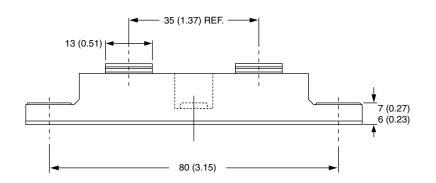
- Vishay Semiconductors product
- Average current rating (x 10)
- Product silicon identification
- 4 C = Circuit configuration
- 5 N = Not isolated
- 6 Q = Schottky rectifier diode
- 7 Voltage rating (100 = 100 V)
- 8 Lead (Pb)-free

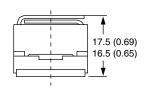
LINKS TO RELATED DOCUMENTS				
Dimensions <u>www.vishay.com/doc?95021</u>				

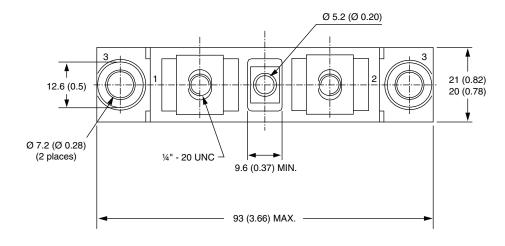


TO-244

DIMENSIONS in millimeters (inches)









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