RoHS COMPLIANT

Vishay Semiconductors

High Performance Schottky Rectifier, 3.0 A





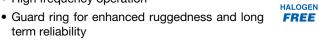
DO-214AB (SMC)

| PRODUCT SUMMARY | | | | |
|----------------------------------|-----------------|--|--|--|
| Package | SMC | | | |
| I _{F(AV)} | 3.0 A | | | |
| V _R | 40 V | | | |
| V _F at I _F | 0.43 V | | | |
| I _{RM} max. | 35 mA at 125 °C | | | |
| T _J max. | 150 °C | | | |
| Diode variation | Single die | | | |
| E _{AS} | 6.0 mJ | | | |

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FEATURES

- · Small foot print, surface mountable
- · Very low forward voltage drop
- High frequency operation



- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-MBRS340-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

| MAJOR RATINGS AND CHARACTERISTICS | | | | | |
|-----------------------------------|---|------------------------------|----|--|--|
| SYMBOL | CHARACTERISTICS | CHARACTERISTICS VALUES UNITS | | | |
| I _{F(AV)} | Rectangular waveform | 3.0 | A | | |
| V _{RRM} | | 40 | V | | |
| I _{FSM} | t _p = 5 μs sine | 1580 | A | | |
| V _F | 3.0 A _{pk} , T _J = 125 °C | 0.43 | V | | |
| TJ | Range | -55 to +150 | °C | | |

| VOLTAGE RATINGS | | | | | |
|--------------------------------------|------------------|---------------|-------|--|--|
| PARAMETER | SYMBOL | VS-MBRS340-M3 | UNITS | | |
| Maximum DC reverse voltage | V _R | 40 | V | | |
| Maximum working peak reverse voltage | V _{RWM} | 40 | v | | |

| ABSOLUTE MAXIMUM RATINGS | | | | | |
|--|--------------------------------|---|---|--------|-------|
| PARAMETER | SYMBOL | TEST CONDITIONS | | VALUES | UNITS |
| Maximum average forward current I _{F(AV)} - | | 50 % duty cycle at T_L = 118 °C, rectangular waveform | | 3.0 | |
| | | 50 % duty cycle at T_L = 110 °C, rectangular waveform | | 4.0 | |
| Maximum peak one cycle non-repetitive surge current | | 5 µs sine or 3 µs rect. pulse | Following any rated load condition and with rated | 1580 | A |
| | 10 ms sine or 6 ms rect. pulse | V _{RRM} applied | 80 | | |
| Non-repetitive avalanche energy | E _{AS} | T _J = 25 °C, I _{AS} = 1.0 A, L = 12 mH | | 6 | mJ |
| Repetitive avalanche current | I _{AR} | Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical | | 1.0 | А |

Revision: 26-Aug-14 1 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishav.com/doc?91000





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| ELECTRICAL SPECIFICATIONS | | | | | |
|---------------------------------|--------------------------------|--|-------------------------|--------|-------|
| PARAMETER | SYMBOL | TEST CO | NDITIONS | VALUES | UNITS |
| | | 3 A | T 05 %O | 0.525 | V |
| Maximum forward voltage drap | V (1) | 6 A | T _J = 25 °C | 0.68 | |
| Maximum forward voltage drop | V _{FM} ⁽¹⁾ | 3 A | T 405.00 | 0.43 | |
| | | 6 A | T _J = 125 °C | 0.57 | |
| | | T _J = 25 °C | | 2.0 | |
| Maximum reverse leakage current | I _{RM} ⁽¹⁾ | T _J = 100 °C | $V_R = Rated V_R$ | 20 | mA |
| | | T _J = 125 °C | | 35 | |
| Maximum junction capacitance | CT | V_R = 5 V_{DC} (test signal range 100 kHz to 1 MHz), 25 $^\circ\text{C}$ | | 230 | pF |
| Typical series inductance | L _S | Measured lead to lead 5 mm from package body 3.0 | | 3.0 | nH |
| Maximum voltage rate of change | dV/dt | Rated V _R 10 000 V | | V/µs | |

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

| THERMAL - MECHANICAL SPECIFICATIONS | | | | |
|---|--|---|-------------|-------|
| PARAMETER | SYMBOL | SYMBOL TEST CONDITIONS | | UNITS |
| Maximum junction and storage temperature range | T _J ⁽¹⁾ , T _{Stg} | | -55 to +150 | °C |
| Maximum thermal resistance, junction to lead | R _{thJL} ⁽²⁾ | | 12 | °C/W |
| Maximum thermal resistance, junction to ambient | R _{thJA} | DC operation | 46 | C/ W |
| Approximate weight | | | 0.24 | g |
| Approximate weight | | | 0.008 | oz. |
| Marking device | | Case style SMC (similar to DO-214AB) 34 | | ļ |

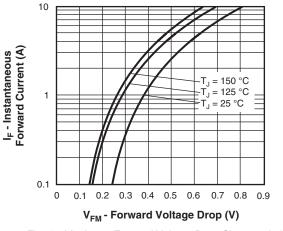
Notes

 $^{(1)} \quad \frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}} \quad \text{thermal runaway condition for a diode on its own heatsink}$

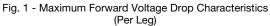
⁽²⁾ Mounted 1" square PCB

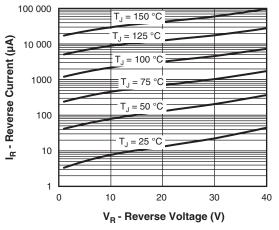
VS-MBRS340-M3

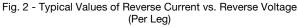
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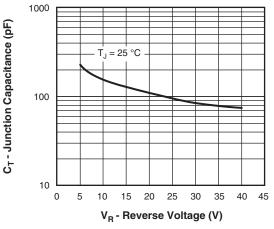


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage (Per Leg)

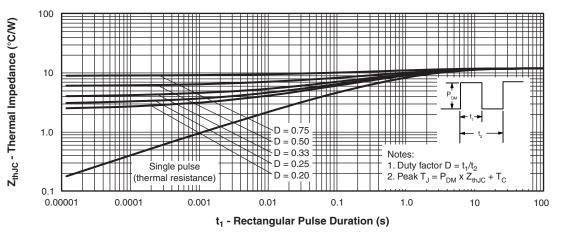
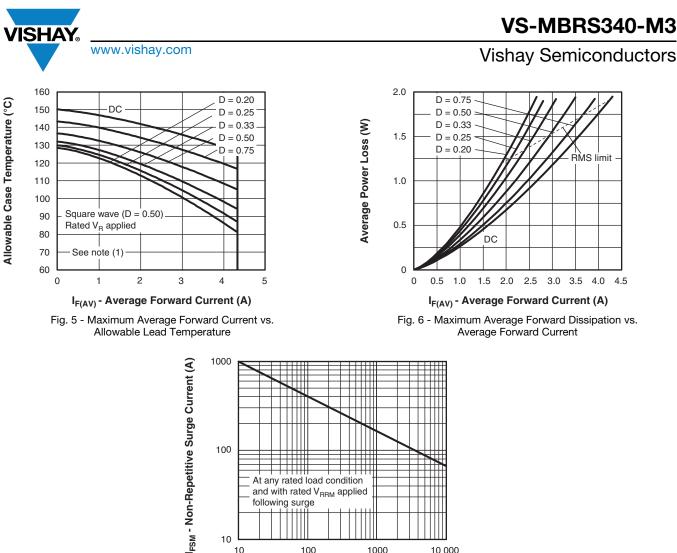


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics (Per Leg)



At any rated load condition and with rated V_{BBM} applied

100

t_p - Square Wave Pulse Duration (μs) Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

1000

10 000

following surge

10 10

Note

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, x \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see} \ \mathsf{fig.} \ \mathsf{6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, x \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

VS-MBRS340-M3



and termination lead (Pb)-free

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ORDERING INFORMATION TABLE

| Device code | VS- | MBR | S | 3 | 40 | - M 3 |
|-------------|------------|-----|---|----------------------|----|--------------|
| | 1 | 2 | 3 | 4 | 5 | 6 |
| | 1 · 2 · | Sch | , | niconduc BR serie | | oduct |
| | 4 | | | ng (3 = 3 | , | |
| | 5 - 6 - | | 0 | ng (40 = gen-free | , | -compliant |

| ORDERING INFORMATION (Example) | | | | | | |
|--------------------------------|--|------|------------------------------------|--|--|--|
| PREFERRED P/N | FERRED P/N PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION | | | | | |
| VS-MBRS340-M3/9AT | 9AT | 3500 | 13" diameter plastic tape and reel | | | |

| LINKS TO RELATED DOCUMENTS | | | | |
|-------------------------------------|--------------------------|--|--|--|
| Dimensions www.vishay.com/doc?95402 | | | | |
| Part marking information | www.vishay.com/doc?95403 | | | |
| Packaging information | www.vishay.com/doc?95404 | | | |
| SPICE model | www.vishay.com/doc?95366 | | | |

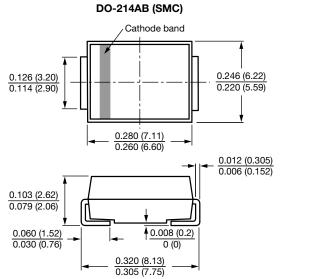


Outline Dimensions

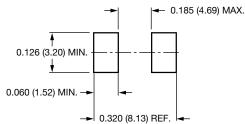
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SMC

DIMENSIONS in inches (millimeters)



Mounting Pad Layout





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