

# UNISONIC TECHNOLOGIES CO., LTD

BTA06 Preliminary TRIAC

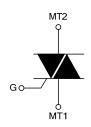
# **6A TRIACS**

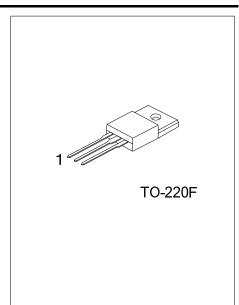
#### ■ DESCRIPTION

The UTC **BTA06** is a 6A triacs which can be operated in 4 quadrants, it uses UTC's advanced technology to provide customers with high commutation performances, etc.

The UTC **BTA06** is suitable for AC switching application and phase control application such as fan speed and temperature modulation control, lighting control and static switching relay, either in through-hole or surface-mount packages.

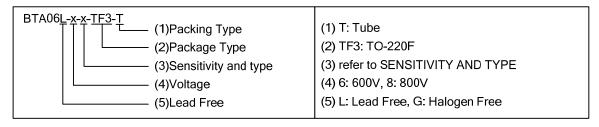
#### ■ SYMBOL





#### ORDERING INFORMATION

Ordering Number		Dookogo	Pin .	Assignr	Dooking	
Lead Free	Halogen Free	Package	1	2	3	Packing
BTA06L-x-x-TF3-T	BTA06G-x-x-TF3-T	TO-220F	MT1	MT2	G	Tube

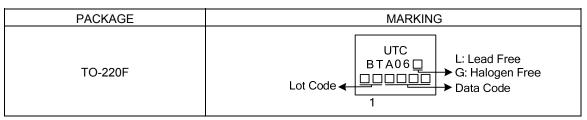


# ■ SENSITIVITY AND TYPE

	VOLT	AGE	OENOTIV/ITV	TVDE		
PART NUMBER 600V 800V		SENSITIVITY	TYPE			
В	0	0	50mA	STANDARD		
С	0	0	25mA	STANDARD		

#### ⊚: Available

#### MARKING INFORMATION



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#### ■ ABSOLUTE MAXIMUM RATINGS

PARAMETER			SYMBOL	RATINGS	UNIT
RMS On-State Current (Full Sine Wave)	T <sub>C</sub> =105°C		I <sub>T(RMS)</sub>	6	Α
Non Repetitive Surge Peak On-State	F=50Hz	t=20ms	I <sub>TSM</sub>	60	Α
Current (Full Cycle T <sub>J</sub> initial=25°C)	F=60Hz	=60Hz t=16.7ms		63	Α
I <sup>2</sup> t Value for Fusing	t <sub>P</sub> =10ms		l <sup>2</sup> t	21	$A^2s$
Critical Rate of Rise of On-State Current: I <sub>G</sub> =2xI <sub>GT</sub> , tr≤100ns	F=120Hz	T <sub>J</sub> =125°C	dl/dt	50	A/µs
Peak Gate Current	t <sub>P</sub> =20µs	T <sub>J</sub> =125°C	$I_{GM}$	4	Α
Average Gate Power Dissipation		T <sub>J</sub> =125°C	$P_{G(AV)}$	1	W
Operating Junction Temperature			TJ	-40~+125	°C
Storage Junction Temperature			T <sub>STG</sub>	-40~+150	°C

Note: Absolute maximum ratings are those values beyond which the device could be permanently damaged. Absolute maximum ratings are stress ratings only and functional device operation is not implied.

# ■ THERMAL RESISTANCES

PARAMETER	SYMBOL	RATINGS	UNIT
Junction to Ambient	$\theta_{JA}$	60	°C/W
Junction to Case (AC)	$\theta_{JC}$	2.7	°C/W

# ■ ELECTRICAL CHARACTERISTICS (T<sub>J</sub>= 25°C, unless otherwise specified)

# **FOR STANDARD (4 QUADRANTS)**

DADAMETED	OVAROL	TEST CONDITIONS		С			В			LINUT
PARAMETER	SYMBOL			MIN	TYP	MAX	MIN	TYP	MAX	UNIT
Gate Trigger Current	1		1-11-111			25			50	mA
(Note 1)	I <sub>GT</sub>	$V_D$ =12V, $R_L$ =30 $\Omega$	IV			50			100	mA
Gate Trigger Voltage	$V_{GT}$		ALL			1.3			1.3	V
Gate Non-Trigger Voltage	$V_{\text{GD}}$	$V_D=V_{DRM}, R_L=3.3k\Omega,$ $T_J=125^{\circ}C$ ALL		0.2			0.2			V
Holding Current (Note 2)	I <sub>H</sub>	I <sub>T</sub> =500mA				25			50	mA
Latabia a Ocument	ı	Ig=1.2Igт	I-III-IV			40			50	mA
Latching Current	lι	IG=1.ZIGT	II			80			100	mA
Critical Rate of Rise of	dV/dt	V <sub>D</sub> =67%V <sub>DRM</sub> , Gate O	pen,	200	200		400			V/µs
Off-State Voltage (Note 2)	aviat	T <sub>J</sub> =125°C		200			400			ν/μ3
Critical Rate of Rise of										
Off-State Voltage at	(dV/dt)c	$(dI/dt)c=2.7A/ms, T_J=$	125°C	5			10			V/µs
Commutation (Note 2)										

# ■ STATIC CHARACTERISTICS

PARAMETER	SYMBOL	TEST CONDITIONS		MIN	TYP	MAX	UNIT
Peak On-State Voltage (Note 2)	$V_{TM}$	I <sub>TM</sub> =8.5A, t <sub>P</sub> =380μs	T <sub>J</sub> =25°C			1.55	V
Threshold Voltage (Note 2)	$V_{TO}$		T <sub>J</sub> =125°C			0.85	V
Dynamic Resistance (Note 2)	$R_D$		T <sub>J</sub> =125°C			60	mΩ
Repetitive Peak Off-State Current	I <sub>DRM</sub>	- \	TJ=25°C			5	μΑ
	I <sub>RRM</sub>	$V_{DRM}=V_{RRM}$	T <sub>J</sub> =125°C			1	mΑ

Notes: 1. Minimum  $I_{GT}$  is guaranteed at 5% of  $I_{GT}$  max.

2. For both polarities of MT2 referenced to MT1.

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