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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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BCR5AS-12A

Triac

Medium Power Use

REJ03G0291-0200 Rev.2.00 Nov 30, 2007

Features

• $I_{T (RMS)} : 5 A$ $V_{DRM} : 600 \text{ V}$

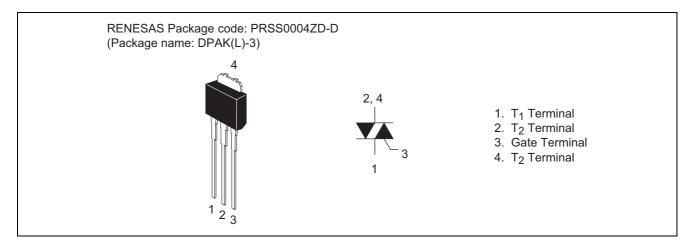
 I_{FGTI} , I_{RGTI} , $I_{RGT III}$: 30 mA

Non-Insulated Type

Planar Passivation Type

Lead Mounted Type

Outline



Applications

Hybrid IC, solid state relay, switching mode power supply, light dimmer, electric fan, electric blanket, washing machine, and other general purpose control applications

Maximum Ratings

| Parameter | Symbol | Voltage class | Unit | |
|--|-----------|---------------|------|--|
| r ai ainetei | Cymbol | 12 | Onit | |
| Repetitive peak off-state voltage ^{Note1} | V_{DRM} | 600 | V | |
| Non-repetitive peak off-state voltage ^{Note1} | V_{DSM} | 720 | V | |

BCR5AS-12A

| Parameter | Symbol | Ratings | Unit | Conditions |
|--------------------------------|----------------------|--------------|------------------|---|
| RMS on-state current | I _{T (RMS)} | 5 | A | Commercial frequency, sine full wave 360° conduction, Tc = 103°C ^{Note3} |
| Surge on-state current | I _{TSM} | 50 | А | 60Hz sinewave 1 full cycle, peak value, non-repetitive |
| I ² t for fusing | l ² t | 10.4 | A ² s | Value corresponding to 1 cycle of half wave 60Hz, surge on-state current |
| Peak gate power dissipation | P_{GM} | 3 | W | |
| Average gate power dissipation | P _{G (AV)} | 0.3 | W | |
| Peak gate voltage | V_{GM} | 10 | V | |
| Peak gate current | I _{GM} | 2 | Α | |
| Junction temperature | Tj | - 40 to +125 | °C | |
| Storage temperature | Tstg | - 40 to +125 | °C | |
| Mass | _ | 0.26 | g | Typical value |

Notes: 1. Gate open.

Electrical Characteristics

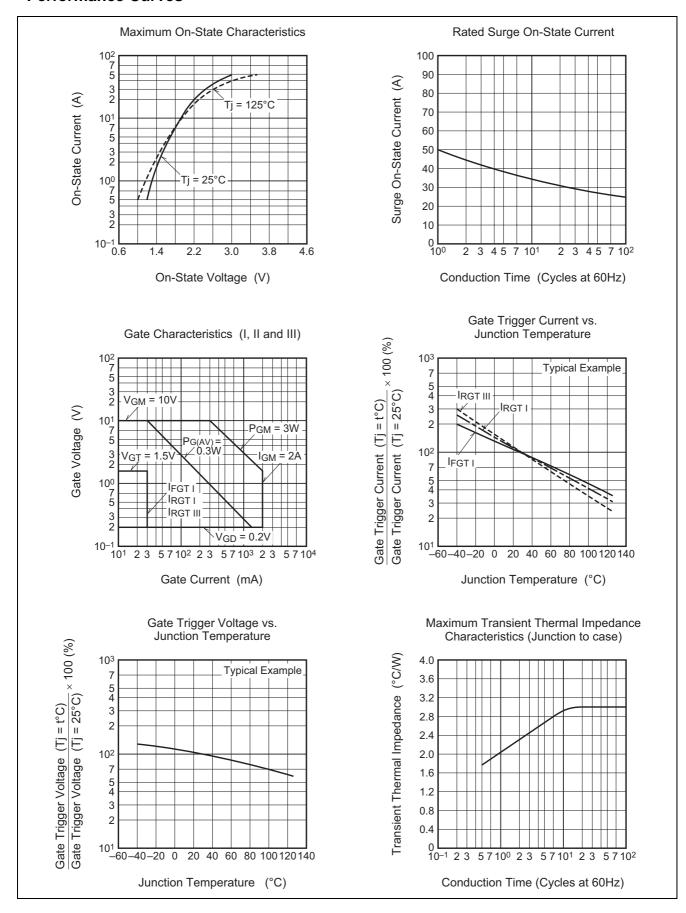
| Parameter | | Symbol | Min. | Тур. | Max. | Unit | Test conditions |
|---|-----|-----------------------|------|------|------|------|--|
| Repetitive peak off-state current | | I _{DRM} | _ | _ | 2.0 | mA | Tj = 125°C, V _{DRM} applied |
| On-state voltage | | V_{TM} | _ | _ | 1.8 | V | Tc = 25°C, I _{TM} = 7 A, Instantaneous measurement |
| Gate trigger voltage ^{Note2} | I | V_{FGTI} | _ | _ | 1.5 | V | $Tj = 25$ °C, $V_D = 6$ V, $R_L = 6$ Ω, |
| | II | V_{RGTI} | _ | _ | 1.5 | V | $R_G = 330 \Omega$ |
| | III | V_{RGTIII} | _ | _ | 1.5 | V | |
| Gate trigger current ^{Note2} | I | I_{FGTI} | _ | _ | 30 | mA | $Tj = 25$ °C, $V_D = 6$ V, $R_L = 6$ Ω, |
| II | | I_{RGTI} | _ | _ | 30 | mA | $R_G = 330 \Omega$ |
| | | I _{RGTIII} | _ | | 30 | mA | |
| Gate non-trigger voltage | | V_{GD} | 0.2 | _ | _ | V | $Tj = 125$ °C, $V_D = 1/2 V_{DRM}$ |
| Thermal resistance | | R _{th (j-c)} | _ | _ | 3.0 | °C/W | Junction to case ^{Note3} |
| Critical-rate of rise of off-state commutating voltage ^{Note4} | | (dv/dt)c | 5 | _ | _ | V/μs | Tj = 125°C |

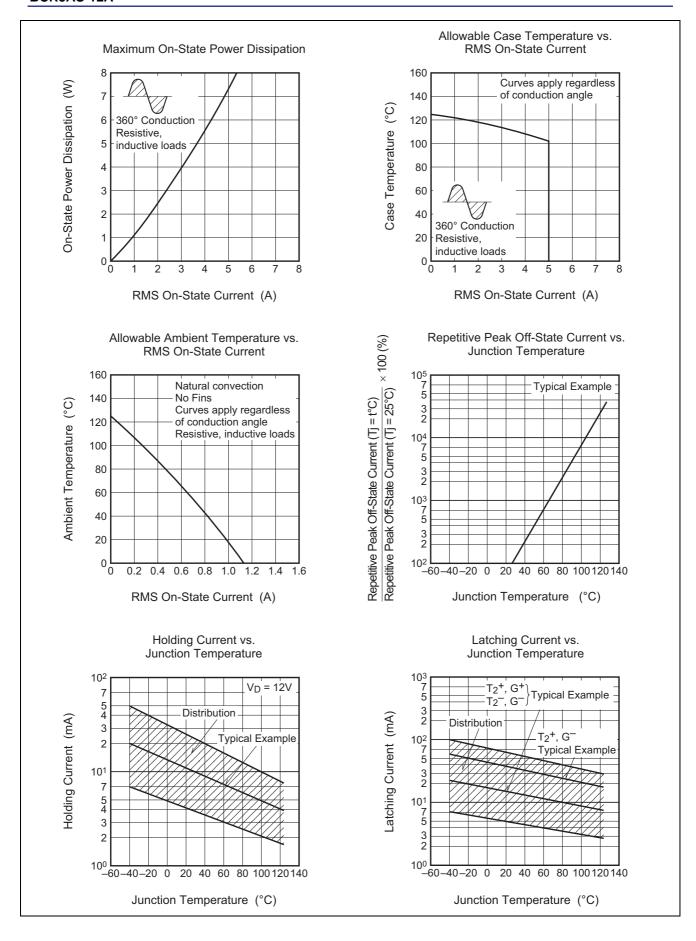
Notes: 2. Measurement using the gate trigger characteristics measurement circuit.

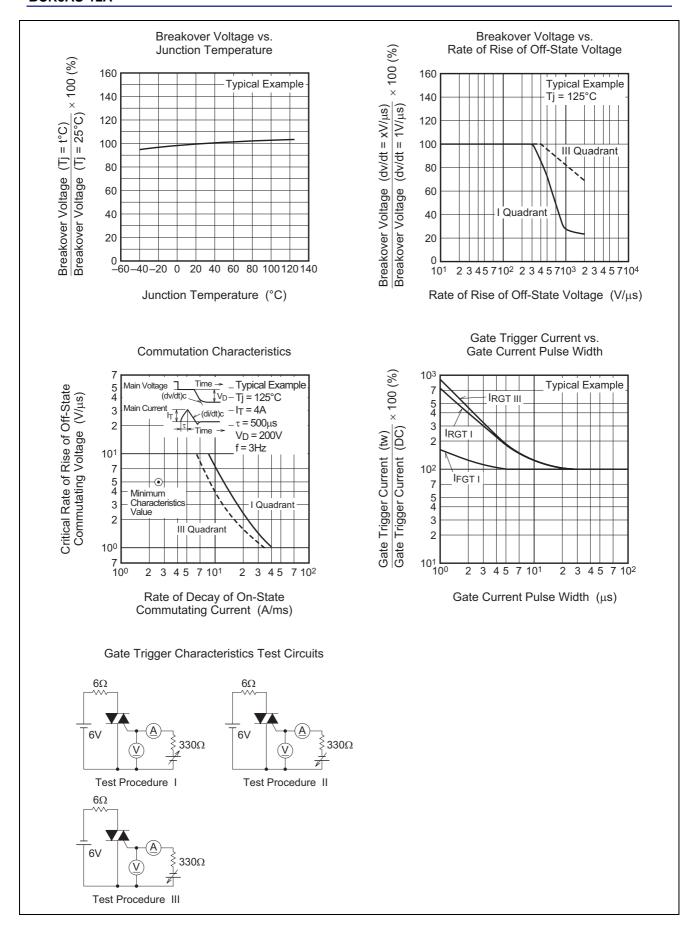
- 3. Case temperature is measured on the T_2 tab.
- 4. Test conditions of the critical-rate of rise of off-state commutating voltage is shown in the table below.

| Test conditions | Commutating voltage and current waveforms (inductive load) | | |
|---|--|--|--|
| 1. Junction temperature Tj = 125°C | Supply Voltage → Time | | |
| 2. Rate of decay of on-state commutating current (di/dt)c = -2.5 A/ms | Main Current (di/dt)c → Time | | |
| 3. Peak off-state voltage $V_D = 400 \text{ V}$ | Main Voltage — Time (dv/dt)c | | |

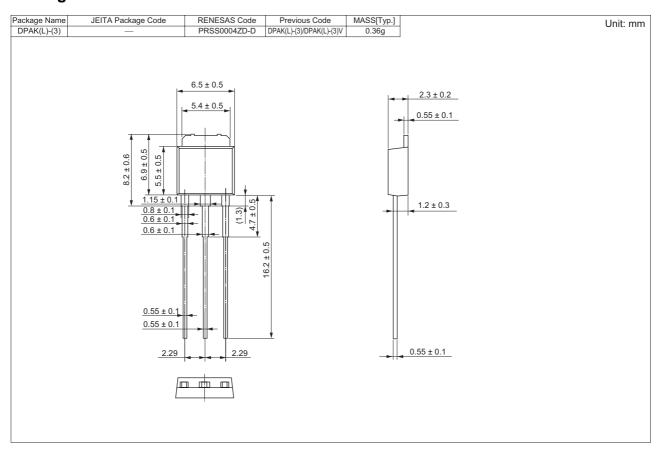
Performance Curves







Package Dimensions



Order Code

| Lead form | Standard packing | Quantity | Standard order code | Standard order code example |
|---------------|------------------|----------|---------------------|-----------------------------|
| Straight type | Vinyl sack | 100 | Type name – A1 | BCR5AS-12A-A1 |

Note: Please confirm the specification about the shipping in detail.

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