
2SJ280(L), 2SJ280(S)

Silicon P-Channel MOS FET

HITACHI

November 1996

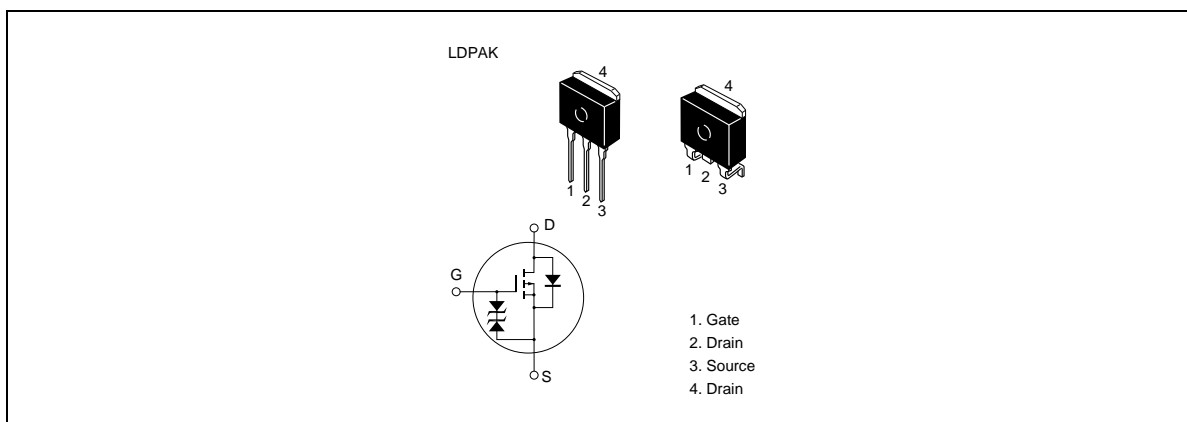
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- 4 V gate drive device can be driven from 5 V source
- Suitable for switching regulator, DC-DC converter
- Avalanche ratings

Outline



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Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V_{DSS}	-60	V
Gate to source voltage	V_{GSS}	±20	V
Drain current	I_D	-30	A
Drain peak current	$I_{D(pulse)}^{*1}$	-120	A
Body to drain diode reverse drain current	I_{DR}	-30	A
Avalanche current	I_{AP}^{*3}	-30	A
Avalanche energy	E_{AR}^{*3}	77	mJ
Channel dissipation	Pch^{*2}	75	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

- Notes
1. $PW \leq 10 \mu s$, duty cycle $\leq 1\%$
 2. Value at $T_c = 25^\circ C$
 3. Value at $Tch = 25^\circ C$, $Rg \geq 50 \Omega$

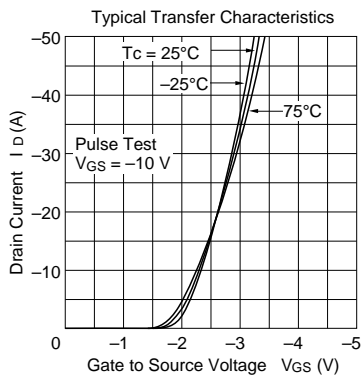
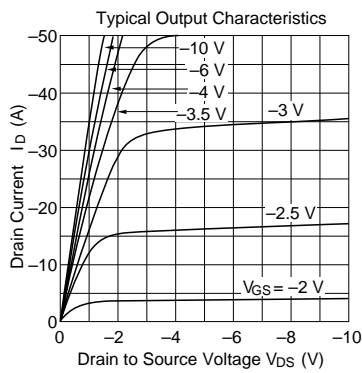
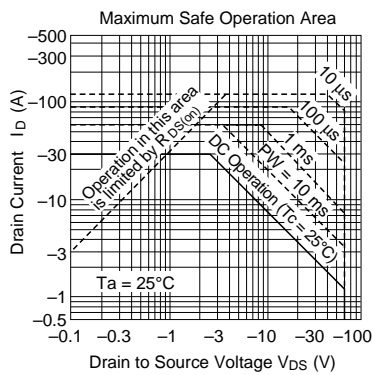
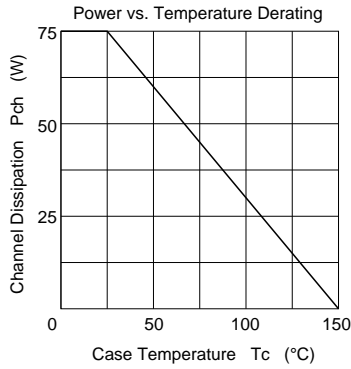
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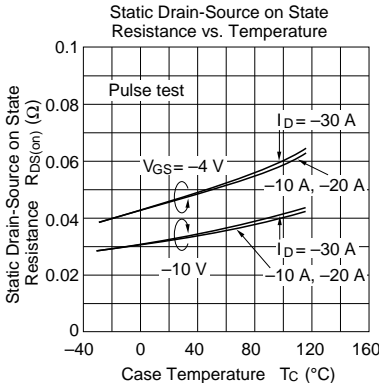
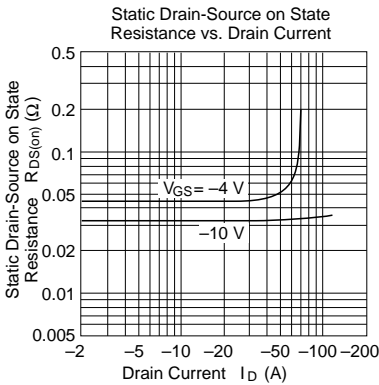
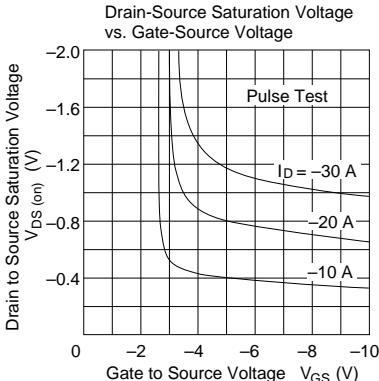
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	-60	—	—	V	$I_D = -10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	± 20	—	—	V	$I_G = \pm 200 \text{ }\mu\text{A}, V_{DS} = 0$
Gate to source leak current	I_{GSS}	—	—	± 10	μA	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltage drain current	I_{DSS}	—	—	-250	μA	$V_{DS} = -50 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.0	—	-2.25	V	$I_D = -1 \text{ mA}, V_{DS} = -10 \text{ V}$
Static drain to source on state resistance	$R_{DS(on)}$	—	0.033	0.043	Ω	$I_D = -15 \text{ A}, V_{GS} = -10 \text{ V}^{*1}$
			—	0.045	0.06	Ω
Forward transfer admittance	$ y_{fs} $	17	25	—	S	$I_D = -15 \text{ A}, V_{DS} = -10 \text{ V}^{*1}$
Input capacitance	C_{iss}	—	3300	—	pF	$V_{DS} = -10 \text{ V}, V_{GS} = 0,$ $f = 1 \text{ MHz}$
Output capacitance	C_{oss}	—	1500	—	pF	
Reverse transfer capacitance	C_{rss}	—	480	—	pF	
Turn-on delay time	$t_{d(on)}$	—	30	—	ns	$I_D = -15 \text{ A}, V_{GS} = -10 \text{ V},$ $R_L = 2 \text{ }\Omega$
Rise time	t_r	—	170	—	ns	
Turn-off delay time	$t_{d(off)}$	—	500	—	ns	
Fall time	t_f	—	390	—	ns	
Body to drain diode forward voltage	V_{DF}	—	-1.5	—	V	$I_F = -30 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery time	t_{rr}	—	200	—	ns	$I_F = -30 \text{ A}, V_{GS} = 0,$ $di_F/dt = 50 \text{ A}/\mu\text{s}$

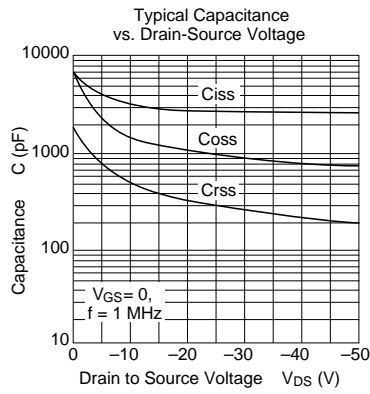
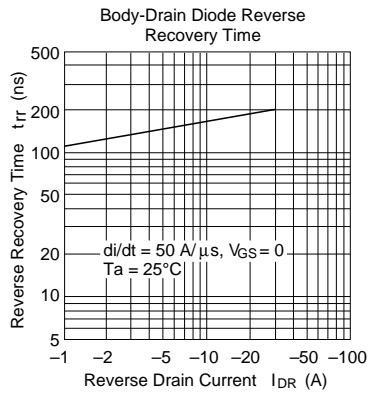
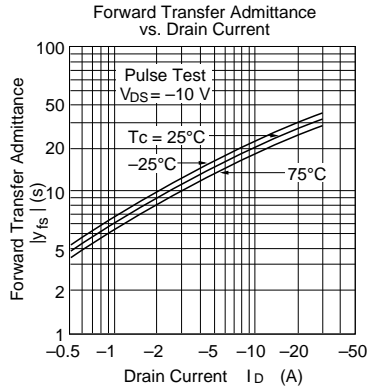
Note 1. Pulse test

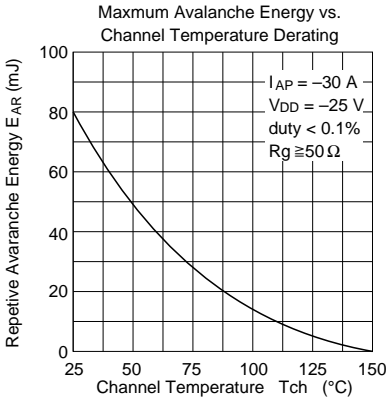
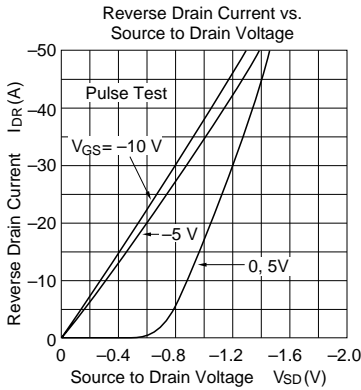
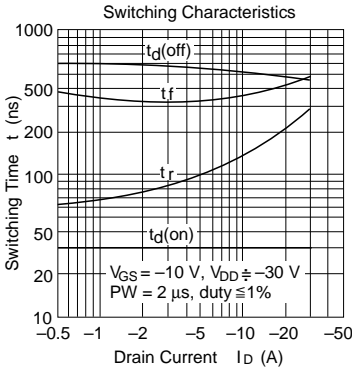
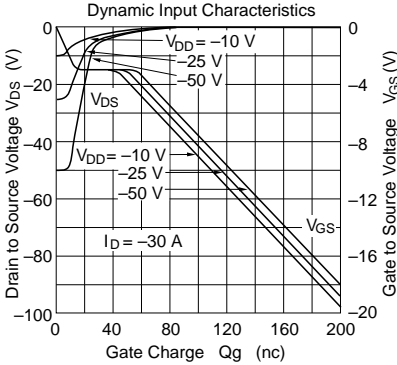
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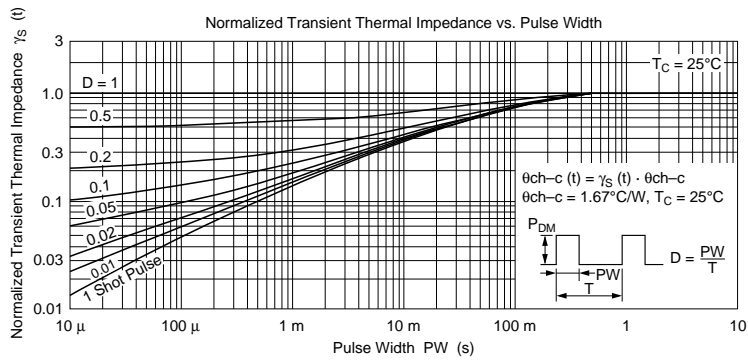
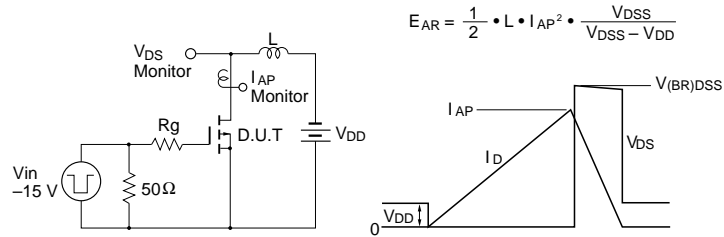
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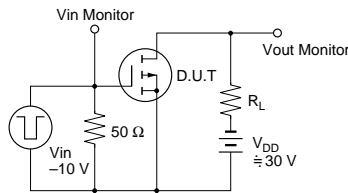


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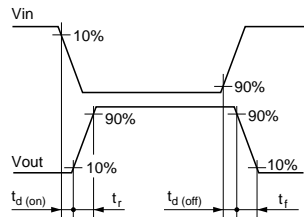
Avalanche Test Circuit and Waveform



Switching Time Test Circuit



Waveforms



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