

"Low Side Chopper" IGBT SOT-227 (Ultrafast IGBT), 50 A



SOT-227

PRODUCT SUMMARY						
V _{CES}	1200 V					
I _C DC	50 A at 92 °C					
V _{CE(on)} typical at 50 A, 25 °C	3.22 V					
Package	SOT-227					
Circuit	Chopper low side switch					

FEATURES

- NPT Generation V IGBT technology
- Square RBSOA
- HEXFRED® clamping diode
- Positive V_{CE(on)} temperature coefficient
- · Fully isolated package
- Speed 8 kHz to 60 kHz
- Very low internal inductance (≤ 5 nH typical)
- · Industry standard outline
- UL approved file E78996



• Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

BENEFITS

- · Designed for increased operating efficiency in power conversion: UPS, SMPS, welding, induction heating
- Easy to assemble and parallel
- · Direct mounting on heatsink
- Plug-in compatible with other SOT-227 packages
- · Low EMI, requires less snubbing

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS	MAX.	UNITS	
Collector to emitter voltage	V _{CES}		1200	V	
O at the second leaders and	,	T _C = 25 °C	84		
Continuous collector current	I _C	T _C = 80 °C	57		
Pulsed collector current	I _{CM}		150	^	
Clamped inductive load current	I _{LM}		150	А	
Diode continuous forward current		T _C = 25 °C	76		
	l _F	T _C = 80 °C	52		
Gate to emitter voltage	V _{GE}		± 20	V	
Power dissipation, IGBT	Б	T _C = 25 °C	431		
	P _D	T _C = 80 °C	242	1	
Power dissipation, diode		$T_{C} = 25 ^{\circ}\text{C}$ 278 $T_{C} = 80 ^{\circ}\text{C}$ 156		W	
	P _D				
RMS isolation voltage	V _{ISOL}	Any terminal to case, t = 1 min	2500	V	



ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS	
Collector to emitter breakdown voltage	V _{BR(CES)}	V _{GE} = 0 V, I _C = 1 mA	1200	-	-		
		$V_{GE} = 15 \text{ V}, I_{C} = 25 \text{ A}$	-	2.46	-		
Callegter to emitter valtage		V _{GE} = 15 V, I _C = 50 A	-	3.22	2.80	V	
Collector to emitter voltage	V _{CE(on)}	V _{GE} = 15 V, I _C = 25 A, T _J = 125 °C	-	2.84	3.60		
		V _{GE} = 15 V, I _C = 50 A, T _J = 125 °C	-	3.78	3.00		
Gate threshold voltage	V _{GE(th)}	V _{CE} = V _{GE} , I _C = 500 μA	4	5	4		
Temperature coefficient of threshold voltage	V _{GE(th)} /ΔT _J	V _{CE} = V _{GE} , I _C = 1 mA (25 °C to 125 °C)	-	- 10	-	mV/°C	
Collector to emitter leakage current I _{CES}		V _{GE} = 0 V, V _{CE} = 1200 V	-	6	50	μA	
	ICES	V _{GE} = 0 V, V _{CE} = 1200 V, T _J = 125 °C	-	0.7	2.0	mA	
Diode reverse breakdown voltage	V_{BR}	I _R = 1 mA	1200	-	-	V	
Diode forward voltage drop	V _{FM}	I _C = 25 A, V _{GE} = 0 V	-	1.99	2.42	V	
		I _C = 50 A, V _{GE} = 0 V	-	2.53	3.00		
		I _C = 25 A, V _{GE} = 0 V, T _J = 125 °C	-	1.96	2.30		
		I _C = 50 A, V _{GE} = 0 V, T _J = 125 °C	-	2.66	3.08		
District and the latest and the		V _R = V _R rated	-	4	50	μΑ	
Diode reverse leakage current	I _{RM}	T _J = 125 °C, V _R = V _R rated	-	0.6	3.0	mA	
Gate to emitter leakage current	I _{GES}	V _{GE} = ± 20 V	-	-	± 200	nA	

SWITCHING CHARACTERISTICS (T _J = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
Total gate charge (turn-on)	Qg			-	400	-	
Gate to emitter charge (turn-on)	Q _{ge}	$I_C = 50 \text{ A}, V_{CC} = 600 \text{ V}, \text{ V}$	_{GE} = 15 V	-	43	-	nC
Gate to collector charge (turn-on)	Q _{gc}			-	187	-	
Turn-on switching loss	E _{on}	I _C = 50 A, V _{CC} = 600 V,		-	2.72	-	- mJ
Turn-off switching loss	E _{off}	$V_{GE} = 15 \text{ V}, R_g = 5 \Omega,$		-	1.11	-	
Total switching loss	E _{tot}	$L = 500 \mu H, T_J = 25 °C$		-	3.83	-	
Turn-on switching loss	E _{on}		Energy losses include tail and diode recovery (see fig. 18)	-	3.94	-	
Turn-off switching loss	E _{off}			-	2.31	-	
Total switching loss	E _{tot}	$I_C = 50 \text{ A}, V_{CC} = 600 \text{ V},$		-	6.25	-	
Turn-on delay time	t _{d(on)}	$V_{GE} = 15 \text{ V}, R_g = 5 \Omega,$		-	191	-	- ns
Rise time	t _r	$L = 500 \mu H, T_J = 125 ^{\circ}C$		-	53	-	
Turn-off delay time	t _{d(off)}			-	223	-	
Fall time	t _f			-	143	-	1
Reverse bias safe operating area	RBSOA	T_J = 150 °C, I_C = 150 A, R_g = 22 Ω , V_{GE} = 15 V to 0 V, V_{CC} = 900 V, V_P = 1200 V			Fullsquare		
Diode reverse recovery time	t _{rr}			-	129	161	ns
Diode peak reverse current	I _{rr}	I _F = 50 A, dI _F /dt = 200 A/μs, V _R = 200 V			11	14	Α
Diode recovery charge	Q _{rr}		700	1046	nC		
Diode reverse recovery time	t _{rr}	/	-	208	257	ns	
Diode peak reverse current	I _{rr}	I _F = 50 A, dI _F /dt = 200 A/μs, V _B = 200 V, T _J = 125 °C		-	17	21	Α
Diode recovery charge	Q _{rr}	VH - 200 V, IJ - 120 O	-	1768	2698	nC	



THERMAL AND MECHANICAL SPECIFICATIONS							
PARAMETER		SYMBOL		MIN.	TYP.	MAX.	UNITS
Junction and storage temperature range		T _J , T _{Stg}		- 40	-	150	°C
Junction to case	IGBT	R _{thJC}		-	-	0.29	
	Diode			-	-	0.45	°C/W
Case to heatsink		R _{thCS}	Flat, greased surface	ı	0.05	-	
Weight				-	30	-	g
Mounting torque				-	-	1.3	Nm
Case style			SOT-227				

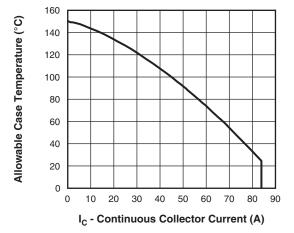


Fig. 1 - Maximum DC IGBT Collector Current vs.
Case Temperature

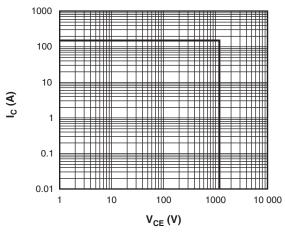


Fig. 2 - IGBT Reverse Bias SOA $T_J = 150~^{\circ}\text{C}, V_{GE} = 15~\text{V}$

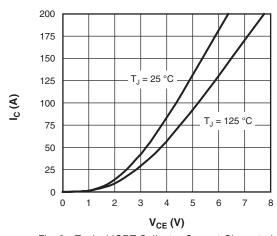


Fig. 3 - Typical IGBT Collector Current Characteristics

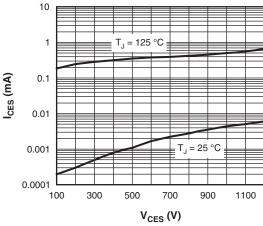


Fig. 4 - Typical IGBT Zero Gate Voltage Collector Current



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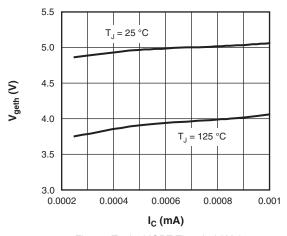


Fig. 5 - Typical IGBT Threshold Voltage

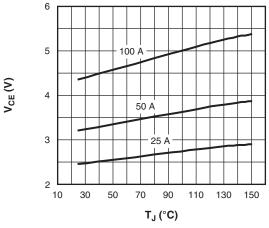


Fig. 6 - Typical IGBT Collector to Emitter Voltage vs. Junction Temperature, $V_{GE} = 15 \text{ V}$

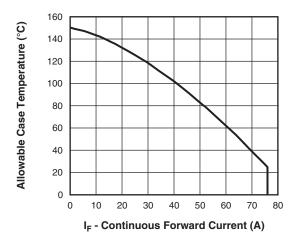


Fig. 7 - Maximum DC Forward Current vs. Case Temperature

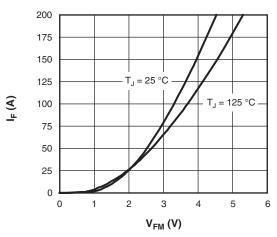


Fig. 8 - Typical Diode Forward Characteristics

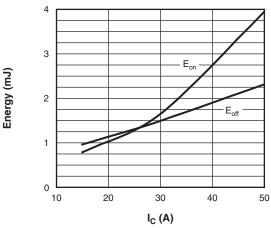


Fig. 9 - Typical IGBT Energy Loss vs. I_C T_J = 125 °C, L = 500 μ H, V_{CC} = 600 V, R_g = 5 Ω , V_{GE} = 15 V

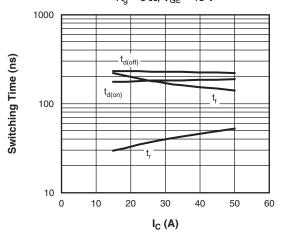


Fig. 10 - Typical IGBT Switching Time vs. I_C T_J = 125 °C, L = 500 μ H, V_{CC} = 600 V, R_g = 5 Ω , V_{GE} = 15 V





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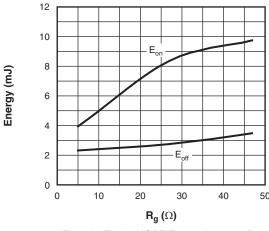


Fig. 11 - Typical IGBT Energy Loss vs. R_g T_J = 125 °C, I_C = 50 A, L = 500 μ H, V_{CC} = 600 V, V_{GE} = 15 V

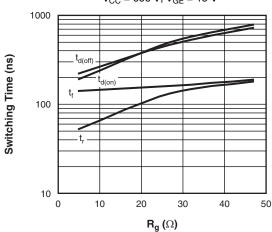


Fig. 12 - Typical IGBT Switching Time vs. R_g T_J = 125 °C, L = 500 μ H, V_{CC} = 600 V, I_C = 50 A, V_{GE} = 15 V

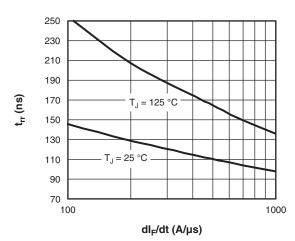


Fig. 13 - Typical t_{rr} Diode vs. dI_F/dt $V_R = 200 \text{ V}, I_F = 50 \text{ A}$

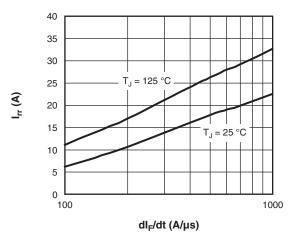


Fig. 14 - Typical I_{rr} Diode vs. dI_F/dt $V_R = 200 \text{ V}, I_F = 50 \text{ A}$

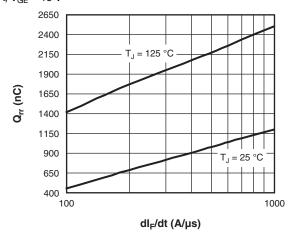


Fig. 15 - Typical Q_{rr} Diode vs. dI_F/dt , $V_R = 200 \text{ V}$, $I_F = 50 \text{ A}$

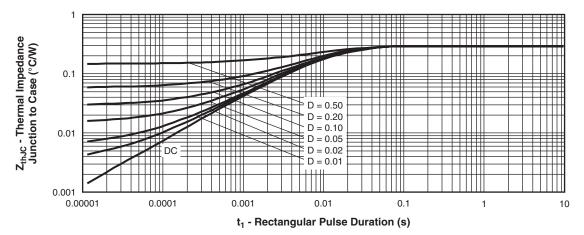


Fig. 16 - Maximum Thermal Impedance ZthJC Characteristics (IGBT)

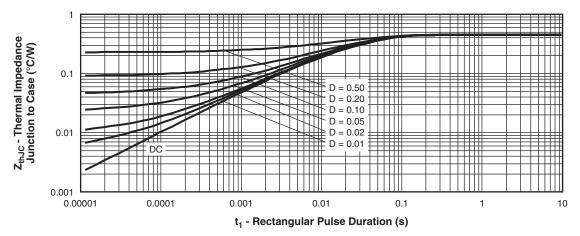
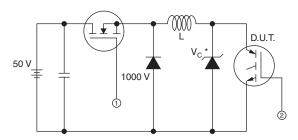


Fig. 17 - Maximum Thermal Impedance Z_{thJC} Characteristics (Diode)





- * Driver same type as D.U.T.; V_C = 80 % of $V_{\rm ce(max)}$ * Note: Due to the 50 V power supply, pulse width and inductor will increase to obtain Id

Fig. 18a - Clamped Inductive Load Test Circuit

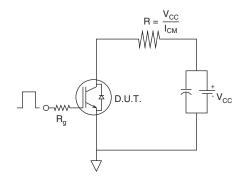


Fig. 18b - Pulsed Collector Current Test Circuit

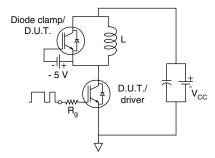


Fig. 19a - Switching Loss Test Circuit

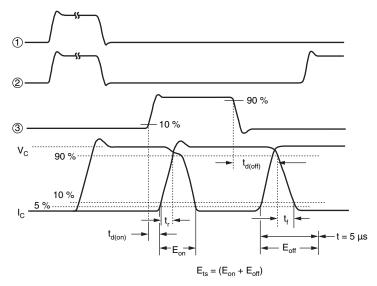
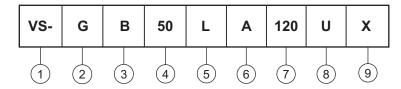


Fig. 19b - Switching Loss Waveforms Test Circuit



ORDERING INFORMATION TABLE

Device code



Vishay Semiconductors product

- Insulated Gate Bipolar Transistor (IGBT)

B = IGBT Generation 5

- Current rating (50 = 50 A)

- Circuit configuration (L = Low side chopper)

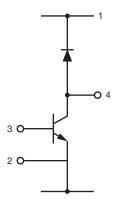
6 - Package indicator (A = SOT-227)

Voltage rating (120 = 1200 V)

- Speed/type (U = Ultrafast IGBT)

- X = F/W HEXFRED® diode

CIRCUIT CONFIGURATION

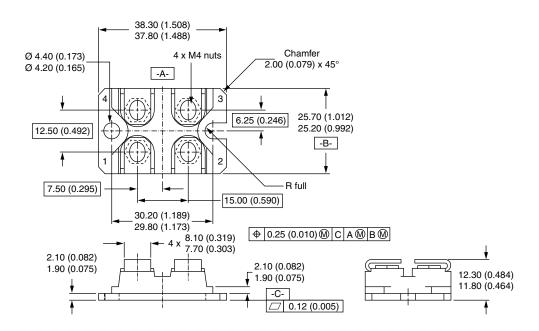


LINKS TO RELATED DOCUMENTS						
Dimensions	www.vishay.com/doc?95036					
Packaging information	www.vishay.com/doc?95037					



SOT-227

DIMENSIONS in millimeters (inches)



Notes

- Dimensioning and tolerancing per ANSI Y14.5M-1982
- · Controlling dimension: millimeter

Document Number: 95036 Revision: 28-Aug-07



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Revision: 02-Oct-12 Document Number: 91000