**Product data sheet** 

## 1. General description

Dual common cathode power Schottky diode designed for high frequency switched mode power supplies in a SOT186A (TO-220F) "full pack" plastic package.

## 2. Features and benefits

- High junction temperature capability
- Isolated package
- Low leakage current
- Negligible switching losses
- Optimised design to give low V<sub>F</sub> and high T<sub>j(max)</sub>

# 3. Applications

- DC to DC converters
- Freewheeling diode
- OR-ing diode
- Switched mode power supply rectifier

## 4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>RRM</sub>	repetitive peak reverse voltage		-	-	100	V
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5 ; T <sub>h</sub> ≤ 134 °C; square-wave pulse; per diode; <u>Fig. 1</u> ; <u>Fig. 2</u> ; <u>Fig. 3</u>	-	-	10	А
I <sub>O(AV)</sub>	average output current	$\delta$ = 0.5 ; square-wave pulse; both diodes conducting	-	-	20	А
Tj	junction temperature		-	-	175	°C
Static charac	teristics					
V <sub>F</sub>	forward voltage	I <sub>F</sub> = 3 A; T <sub>j</sub> = 125 °C; <u>Fig. 6</u>	-	0.53	0.58	V
I <sub>R</sub>	reverse current	$V_R = 100 \text{ V}; T_j = 25 \text{ °C}; Fig. 7$	-	-	3	μA





# 5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	A1	anode 1	mb	A1
2	K	cathode		
3	A2	anode 2		K sym125
mb	n.c.	mb; isolated	TO-220F (SOT186A)	

# 6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
NXPS20S100CX	TO-220F	plastic single-ended package; isolated heatsink mounted; 1 mounting hole; 3-lead TO-220 "full pack"	SOT186A

## 7. Marking

Table 4. Marking codes

Type number	Marking code
NXPS20S100CX	NXPS20S100CX

# 8. Limiting values

Table 5. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
$V_{RRM}$	repetitive peak reverse voltage		-	100	V
I <sub>F(AV)</sub>	average forward current	$\delta$ = 0.5 ; T <sub>h</sub> ≤ 134 °C; square-wave pulse; per diode; <u>Fig. 1</u> ; <u>Fig. 2</u> ; <u>Fig. 3</u>	-	10	Α
I <sub>O(AV)</sub>	average output current	$\delta$ = 0.5; square-wave pulse; both diodes conducting	-	20	А
I <sub>FSM</sub>	non-repetitive peak forward current	$t_p$ = 10 ms; $T_{j(init)}$ = 25 °C; sine-wave pulse; Fig. 4	-	150	Α

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Symbol	Parameter	Conditions	Min	Max	Unit
T <sub>stg</sub>	storage temperature		-65	175	°C
T <sub>j</sub>	junction temperature		-	175	°C

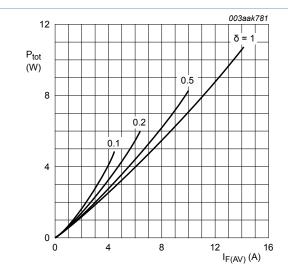


Fig. 1. Forward power dissipation as a function of average forward current; square waveform; per diode; maximum values

$$I_{F(AV)} = I_{F(RMS)} \times \sqrt{\delta}$$
  
 $V_O = 0.597 \text{ V}; R_S = 0.011 \Omega$ 

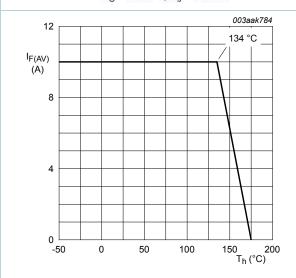


Fig. 3. Average forward current as a function of heatsink temperature; per diode; maximum values

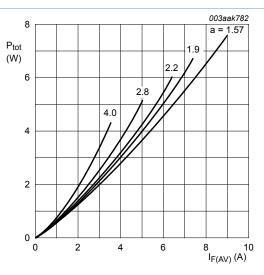


Fig. 2. Forward power dissipation as a function of average forward current; sinusoidal waveform; per diode; maximum values

a = form factor = 
$$I_{F(RMS)}/I_{F(AV)}$$
  
V<sub>O</sub> = 0.597 V; R<sub>S</sub> = 0.011  $\Omega$ 

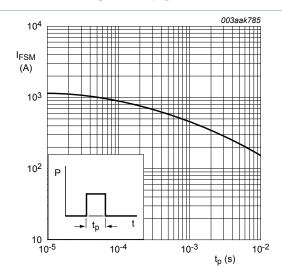


Fig. 4. Non-repetitive peak forward current as a function of pulse width; square waveform; per diode; maximum values

## 9. Thermal characteristics

Table 6. Thermal characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R <sub>th(j-h)</sub>	thermal resistance from junction to	with heatsink compound; per diode; Fig. 5	-	-	5	K/W
heatsink	with heatsink compound; both diodes conducting	-	-	4	K/W	
R <sub>th(j-a)</sub>	thermal resistance from junction to ambient	in free air	-	55	-	K/W

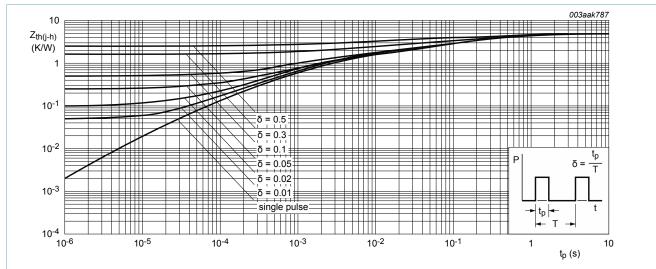


Fig. 5. Transient thermal impedance from junction to heatsink as a function of pulse width; per diode; maximum values

### 10. Isolation characteristics

Table 7. Isolation characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V <sub>isol(RMS)</sub>	RMS isolation voltage	50 Hz < f < 60 Hz; sinusoidal waveform; RH ≤ 65 %; clean and dust free; from all terminals to external heatsink	-	_	2500	V
C <sub>isol</sub>	isolation capacitance	from cathode to external heatsink; f = 1 MHz	-	10	-	pF

## 11. Characteristics

Table 8. Characteristics

Symbol	Parameter	Conditions	Min	Тур	Max	Unit		
Static char	acteristics							
V <sub>F</sub> forward voltage	forward voltage	I <sub>F</sub> = 3 A; T <sub>j</sub> = 25 °C; <u>Fig. 6</u>	-	0.67	0.72	V		
		I <sub>F</sub> = 10 A; T <sub>j</sub> = 25 °C; <u>Fig. 6</u>	-	0.8	0.85	V		
		I <sub>F</sub> = 3 A; T <sub>j</sub> = 125 °C; <u>Fig. 6</u>	-	0.53	0.58	V		
		I <sub>F</sub> = 10 A; T <sub>j</sub> = 125 °C; <u>Fig. 6</u>	-	0.66	0.71	V		
I <sub>R</sub>	reverse current	V <sub>R</sub> = 100 V; T <sub>j</sub> = 25 °C; <u>Fig. 7</u>	-	-	3	μΑ		
		V <sub>R</sub> = 100 V; T <sub>j</sub> = 125 °C; <u>Fig. 7</u>	-	-	3	mA		
Dynamic characteristics								
C <sub>d</sub>	diode capacitance	f = 1 MHz; V <sub>R</sub> = 10 V; T <sub>j</sub> = 25 °C; <u>Fig. 8</u>	-	130	-	pF		

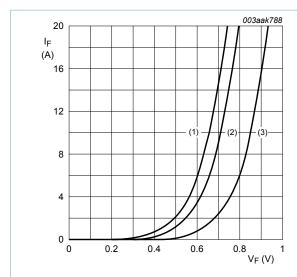


Fig. 6. Forward current as a function of forward voltage; per diode

(1)  $T_i = 125$  °C; typical values;

(2)  $T_i = 125$  °C; maximum values;

(3) T<sub>i</sub> = 25 °C; maximum values;

 $V_O = 0.597 \text{ V}; R_S = 0.011 \Omega$ 

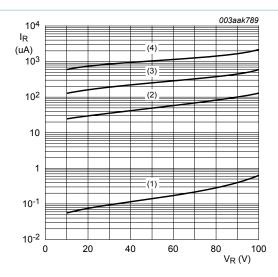


Fig. 7. Reverse leakage current as a function of reverse voltage; per diode; typical values

(1)  $T_j = 25$  °C; typical values;

(2) T<sub>j</sub> = 100 °C; typical values;

(3) T<sub>1</sub> = 125 °C; typical values;

(4)  $T_i = 150$  °C; typical values

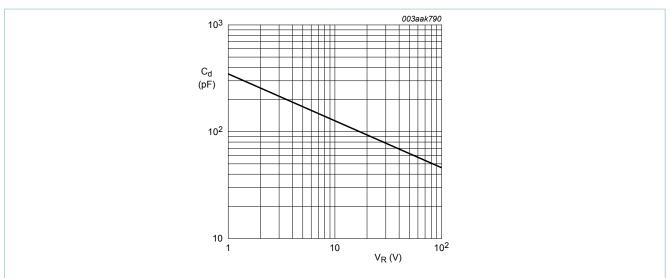
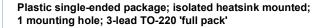


Fig. 8. Diode junction capacitance as a function of applied reverse voltage; per diode; typical values

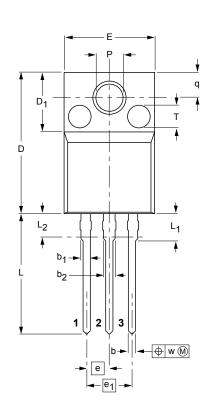
$$f = 1 \text{ MHz}; T_j = 25 \text{ }^{\circ}\text{C}$$

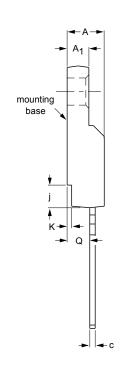
6/10

# 12. Package outline



SOT186A





0 5 10 mm

### DIMENSIONS (mm are the original dimensions)

UNIT	Α	A <sub>1</sub>	b	b <sub>1</sub>	b <sub>2</sub>	С	D	D <sub>1</sub>	E	е	e <sub>1</sub>	j	К	L	L <sub>1</sub>	L <sub>2</sub> <sup>(1)</sup> max.	Р	Q	q	T <sup>(2)</sup>	w
mm	4.6 4.0	2.9 2.5	0.9 0.7	1.1 0.9	1.4 1.0	0.7 0.4	15.8 15.2	6.5 6.3	10.3 9.7	2.54	5.08	2.7 1.7	0.6 0.4	14.4 13.5	3.30 2.79	3	3.2 3.0	2.6 2.3	3.0 2.6	2.5	0.4

#### Notes

- 1. Terminal dimensions within this zone are uncontrolled.
- 2. Both recesses are #  $2.5 \times 0.8$  max. depth

OUTLINE		EUROPEAN	ISSUE DATE		
VERSION	IEC	JEDEC	JEITA	PROJECTION	ISSUE DATE
SOT186A		3-lead TO-220F			<del>-02-04-09</del> 06-02-14

Fig. 9. Package outline TO-220F (SOT186A)

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